



AI-deck 1.1

SKU: 114992445

The AI-deck 1.1 is built around the GAP8 RISC-V multi-core MCU build for AI on the edge purposes. Adding to this a QVGA monochrome camera and a ESP32 WiFi MCU. This all together creates a pretty good platform to develop low power AI on the edge for a drone.



This product is in early access stage. It means that while the hardware is working and tested, the software is still pretty much work in progress. For more information see our [early access description page](#).

1. Introduction

The AI-deck 1.1 extends the computational capabilities with the GAP8 and will enable complex artificial intelligence-based workloads to run onboard with the possibility to achieve fully autonomous navigation capabilities. The ESP32 adds wifi connectivity with the possibility to stream images as well as handling control. This lightweight and low power combination we believe open up many research and development areas for the micro sized Crazyflie 2.X UAV.

The AI-deck 1.1 version only has minor updates compared to the AI-deck 1.0. The Himax camera module is now the monochrome version and the GAP8 MCU is now revision C instead of B.

2. Early access

The AI-deck 1.1 is currently in the early-access development stage and even though the hardware has been extensively tested, the software and documentation is in an early stage. While in the early-access phase we recommend only experienced users for this board.

3. Features

- GAP8 - Ultra low power 8+1 core RISC-V MCU
- Himax HM01B0 - Ultra low power 320×320 monochrome camera.
- 512 Mbit HyperFlash and 64 Mbit HyperRAM
- ESP32 for WiFi and more (NINA-W102)
- 2 x Cortex-M 10-pin JTAG for GAP8 and ESP32
- Button connected to ESP32 for UART bootloader or other action
- 1 x green LED connected to GAP8 (A2)
- 1 x green LED connected to ESP32 (GPIO_24)
- Can work stand alone. Power board on VCOM.

4. Electrical specification

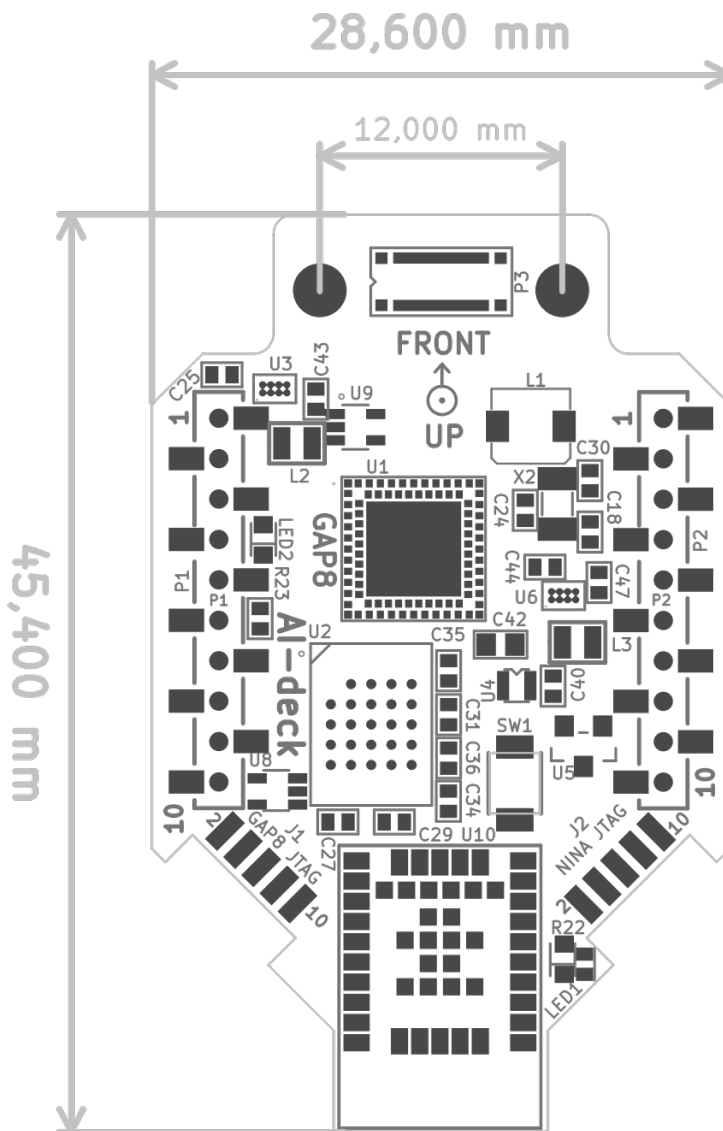
- Power supply 3V-5V @ VCOM up to 300mA
- 1-wire memory for automatic expansion board detection
- UART connected between GAP8 and Crazyflie (RX1, TX1)
- UART connected between ESP32 (RX2, TX2)
- ESP32 sysboot pin connected to Crazyflie (IO_1)
- Reset to GAP8 and ESP32 connected to Crazyflie (IO_4)
- SPI between GAP8 and ESP32
- GAP8 (B1) -> ESP32 (GPIO_5) io
- ESP32 (GPIO_25) -> GAP8 (A13) io

5. Mechanical specifications

- Weight: 4.4g

- Size (WxHxD): 30x52x8mm
- Designed for mounting over/under the Crazyflie 2.X

6. Mechanical drawing



7. Package contents

- 1 x AI-deck 1.1
- 2 x Long pin headers (pin 15mm - plastic 4mm - pin 6mm)

8. Errata

- GAP8 (B1) is 1.8v pin and should only be used as output from GAP8 to NINA. No workaround.

9. Hardware revisions

Revision	Comment
C1	Initial release

10. History

Version	Comment	Date
1	Initial release	2021-02-12