LB1941T

Monolithic Linear IC For Digital Cameras Constant Current Forward/Reverse Driver IC

Overview

The LB1941T is a single-channel forward/reverse driver IC that provides a constant current control function. Its low-saturation output makes it appropriate for voice coil motor control, and it is optimal for use as the shutter driver IC in digital cameras.

Functions

- Constant current control (I_O = 400mA when Rf = 0.5Ω)
- Ultraminiature package (MSOP8: 150mil)
- Built-in thermal protection circuit
- Includes a rapid charge/rapid discharge circuit for stable shutter operation.
- Built-in reference voltage circuit (0.2V typical)

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		-0.3 to +10.5	V
Output current	I _O max		600	mA
Output applied voltage	V _O max		-0.3 to V _{CC} +0.3	V
Input applied voltage	V _{IN} max	IN1, IN2	-0.3 to +10.5	V
Allowable power dissipation	Pd max	Mounted on a specified board.*	400	mW
Operating temperature	Topr		-20 to +75	°C
Storage temperature	Tstg		-55 to +150	°C

Note*: Mounted on a board: 114.3mm×76.1mm×1.6mm, glass epoxy resin.

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



Allowable Operating Range at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Function-guaranteed voltage range	VOPR		2.5 to 10	V
Constant-current set range	IOUT	Set with RFG-GND resistance	50 to 500	mA
Input low level voltage	VIL	IN1, IN2	-0.3 to +0.5	V
Input high level voltage	VIH	IN1, IN2	2.0 to 10	V

Electrical Characteristics at $Ta = 25^{\circ}C$

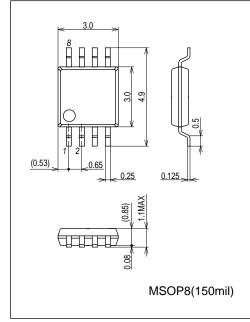
P t			Ratings				
Parameter	Symbol Conditions		min	typ	max	Unit	
Standby Current dissipation	ISTB	V _{CC} = 8.5V			1.0	μΑ	
Constant-current H bridge drive	circuit						
Output saturation voltage	V _O (sat)1	$V_{CC} = 3V$, $I_{O} = 200$ mA (Upper + Lower side)		0.20	0.35	V	
	V _O (sat)2	$V_{CC} = 4V, I_{O} = 400$ mA (Upper + Lower side)		0.50	0.70	V	
Output constant current	IOUT	$V_{CC} = 4V, R_L = 3\Omega, RF = 0.5\Omega$	375	400	424	mA	
Temperature dependence of output constant current (reference $Ta = 25^{\circ}C$)	Δl _O	$V_{CC} = 4V, R_L = 3\Omega, RF = 0.5\Omega *$ (Ta = -10 to +60°C)	-2		+2	%	
Operating current dissipation	ICC	$V_{CC} = 4V, R_{L} = 0\Omega$, (No load, full drive)		14	21	mA	
Thermal protection operating temperature	TSD	Design target value *	150	180	210	°C	
Control input circuit			•				
Control pin maximum input	ΙΗ	V _{IH} = 5.5V, V _{CC} = 5.5V		80	100	μΑ	
current	۱ _{IL}	V _{IL} = GND	-1		0	μA	

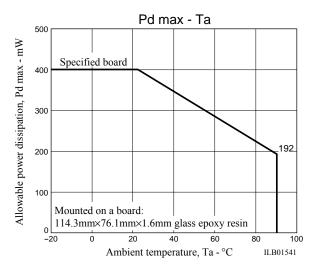
Note*: These items are design target values and are not tested.

Package Dimensions

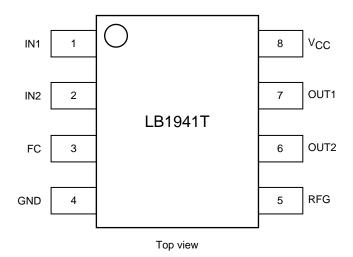
unit : mm (typ)

3245B

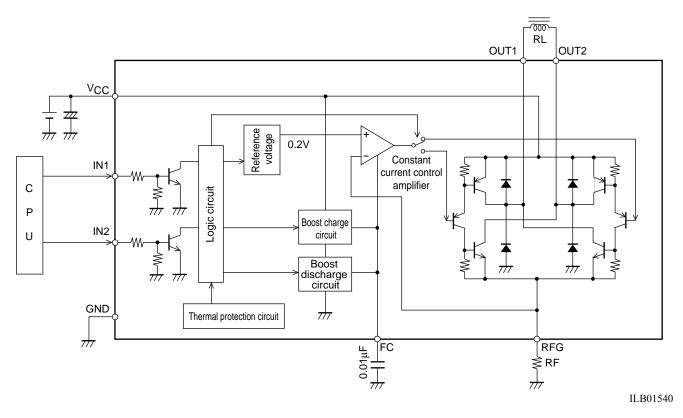




Pin Assignment



Block Diagram



Boost charge and discharge circuits

In order to keep the output response time constant during mode transfer from the standby state to forward (reverse) rotation, this IC incorporates boost charge and discharge circuits for external capacitor connected to the FC pin. The external capacitor connected to the FC pin is for output phase compensation (to suppress oscillation), for which 0.01 to 0.1μ F is recommended. Note that increase in the capacitor value results in increase in the time necessary for the constant current control to rise.

Truth Table

Input		Output		Mada	
IN1	IN2	OUT1	OUT2	Mode	
L	L	OFF	OFF	Standby 1*	
Н	L	Н	L	Forward rotation	
L	Н	L	Н	Reverse rotation	
н	н	OFF	OFF	Standby 2*	

Cautions for use

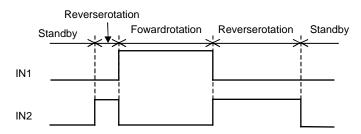
Standby states 1 and 2 in the above truth table differ as follows:

Standby state 1All circuits in IC are not operating and the current dissipation is almost zero.

Standby state 2The constant-current control amplifier output in IC is in the full drive condition. Both OUT1 and OUT2 are OFF. The current dissipation is a few mA.

During mode transfer from the standby state 1 to forward (reverse) rotation, the current rises from the output current zero condition to the required constant current value. On the other hand, during mode transfer from the standby state 2 to forward (reverse) rotation, full drive is applied once to the output, then the current lowers to the required current value.

Therefore, select the standby 1 state when putting IC in the standby state. A typical drive sequence is shown in the figure below.



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