HI0805N310R-10

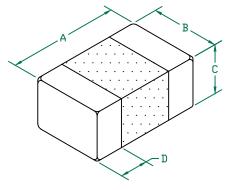
PHYSICAL DIMENSIONS:

A 2.00 [.079] \pm 0.20 [.008]

B 1.25 [.049] ± 0.20 [.008]

C 0.90 [.035] ± 0.20 [.008]

D 0.51 [.020] ± 0.25 [.010]

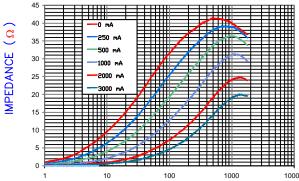


ELEC.	ELECTRICAL CHARACTERISTICS:								
Z @ 100MHz (<u>Ω</u>)		DCR $\left(\begin{array}{c}\Omega\end{array}\right)$	Rated Current						
Nominal	31								
Minimum	23								
Maximum	39	0.03	3,000 mA						

NOTES: UNLESS OTHERWISE SPECIFIED

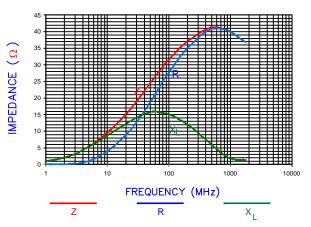
- TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, PAPER TAPE.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATING TEMPERATURE TEMP: -40℃~+125℃ (INCLUDING SELF-HEATING)
- 5. COSMETIC SPECIFICATION REFER TO WI-QA-124.

Z vs FREQUENCY IMPEDANCE UNDER DC BIAS



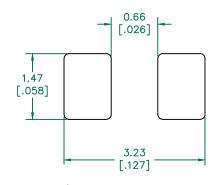
FREQUENCY (MHz)

|Z| , R, AND X vs. FREQUENCY



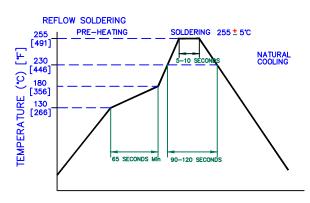
AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture.

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.763 [.030] to this dimension.)

RECOMMENDED SOLDERING CONDITIONS



		DIMENSIONS ARE IN mm [INCHES].			This print is the property of Laird						
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					rights to design or invention are						
					reserved.						
	-				1						
					PROJECT/PART NUMBER:	REV F	PART TYPE:	DRAWN BY:			
1 -	ا ہ	OPERATING TEMPERATURE	00 (00 (10	/13 QU	HI0805N310R-10		CO-FIRE	JRK			
	С	UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13		11100031431011 10	CO-FIRE		UKK			
	В	CHANGE TO PAPER TAPE	03/02/10	JUN	DATE: 07/23/09 SCAL	E NT	SHEET:				
	Α	ORIGINAL DRAFT	07/23/09	JRK	CAD #			of 1			
	REV	DESCRIPTION	DATE	INT	100 F 111000ENT40D 40 0	~		01 1			

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