

# TFE32 Series Low ESR Tuning Fork Crystal

### Features

- 32.7680kHz Frequency Reference
- Low ESR Tuning Fork Crystal Design, <50k Ohms</li>
- Hermetic Ceramic Surface Mount Package
- Ideal for High Density Circuit Boards
- Frequency Tolerance, ±20ppm Standard
- Parabolic Temperature Coefficient
- Tape and Reel Packaging, EIA-481

### **Applications**

- Real Time Clock Reference
- Low Power FPGAs & MCUs

Wearable Electronics

Healthcare Devices

- Data Loggers
- Smart Meters

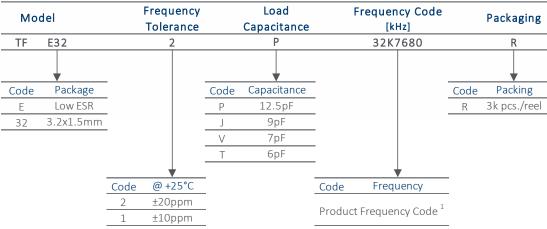
### Description

CTS TFE32 Series is designed to pair with low power microcontrollers requiring a Real Time Clock reference with an ESR of 50k Ohms maximum. This series will support general commercial and industrial applications.

Battery Powered Applications

Portable Electronics

### **Ordering Information**



Notes:

1] Frequency is recorded with two leading digits before the 'K' and 4 significant digits after the 'K' [including zeros].

### Not all performance combinations and frequencies may be available. Contact your local CTS Representative or CTS Customer Service for availability.

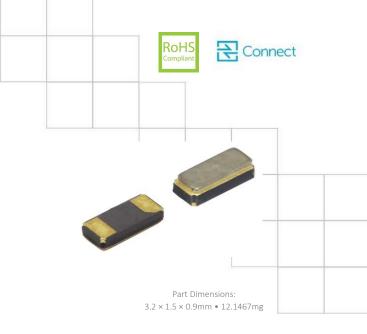
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### DOC#008-0531-0 Rev. F

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# **Electrical Specifications**

### **Operating Conditions**

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNIT
Operating Temperature	TA	-	-40	+25	+85	°C
Turnover Temperature	T <sub>M</sub>	-	+20	+25	+30	°C
Storage Temperature	T <sub>STG</sub>	-	-55	-	+125	°C

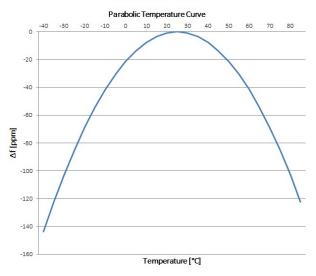
### Frequency Stability

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNIT
Frequency	fo	-	32.7680			kHz
Frequency Tolerance [Note 1]	Δf/f <sub>o</sub>	Standard @ +25°C	-20	-	20	ppm
Parabolic Coefficient	ß	See Figure 1	-0.034 ±0.010			ppm/°C <sup>2</sup>
Aging	∆f/f₀	First Year @ +25°C	-3	-	3	ppm

### **Crystal Parameters**

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNIT
Operating Mode	-	-	Flexura	-		
Load Capacitance [Note 1]	CL	Standard	-	12.5	-	pF
Shunt Capacitance	Co	-	-	1.2	1.5	pF
Motional Capacitance	C1	-	-	-	6.0	fF
Series Resistance	R <sub>1</sub>	-	-	-	50	kΩ
Drive Level	DL	-	-	0.1	0.5	μW
Insulation Resistance	Ri	+100Vdc ±15Vdc	500	-	-	MΩ
1.] See Ordering Information for availab	le options.					

### Figure 1



Frequency Stability  $\left[\Delta f\right]$  at a given temperature,

 $\Delta f = \beta [T_A - T_M]^2$ 

B = Parabolic Coefficient $T_A = Ambient Temperature$  $T_M = Turnover Temperature$  Ex. Find frequency stability at  $T_A = +45$  °C  $\Delta f = -0.034[45-25]^2$   $\Delta f = -0.034[20]^2$  $\Delta f = -13.6ppm$ 

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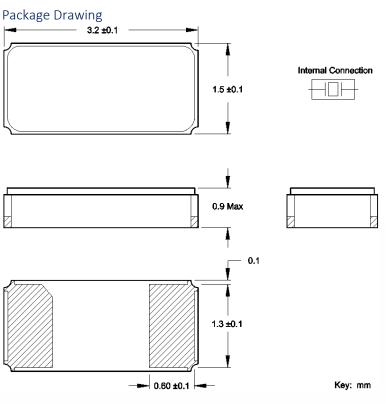
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# **Mechanical Specifications**



# Recommended Pad Layout

### Marking Information

Contact factory for marking formats that apply to this model series.

### Notes

- 1. JEDEC termination code (e4). Barrier-plating is nickel [Ni] with gold [Au] flash plate.
- Reflow conditions per JEDEC J-STD-020; +260°C maximum, 20 seconds.
- 3. MSL = 1.
- 4. Due to the large world-wide production volumes for this model series, product variability may exist between production date codes, such as package coloring and product marking format. CTS guarantees form-fit-function performance to published data sheet parameters. Contact your local CTS Representative or CTS Customer Service with specific questions.

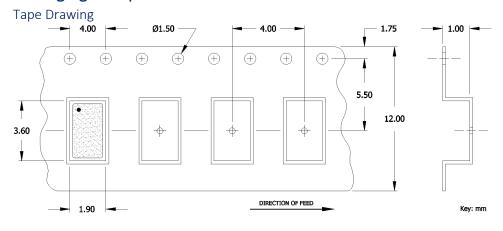
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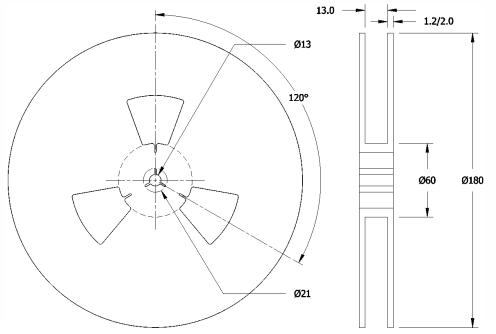


# TFE32 Series Low ESR Tuning Fork Crystal

### Packaging - Tape and Reel



**Reel Drawing** 



### Notes

- 1. Device quantity is 3k pieces maximum per 180mm reel.
- 2. Complete CTS part number, frequency value, date code and manufacturing site code information must appear on reel and carton labels.

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# **Mouser Electronics**

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

# CTS:

 TFE322P32K7680R
 TFE322T32K7680R
 TFE321J32K7680R
 TFE321P32K7680R
 TFE321T32K7680R

 TFE321V32K7680R
 TFE322V32K7680R
 TFE322J32K7680R
 TFE322D32K7680R
 TFE322D32K7680R