

- Applications
 - Broadcast Video Systems
 - Storage Area Networking
 - Test and Measurement

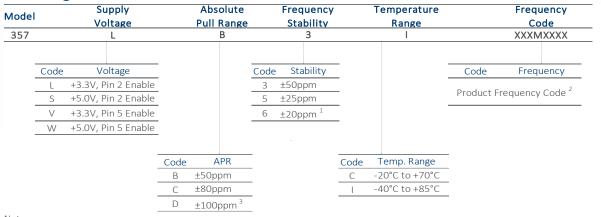
- Ethernet/GbE/SyncE
- Broadband Access
- Phase-Locked Loop

- Networking Equipment
- Fiber Channel

Description

CTS Model 357 is a low-cost, high-performance voltage-controlled oscillator supporting HCMOS output. M357 has excellent stability and low phase jitter performance.

Ordering Information



Notes:

1] Only available with "C" temperature range.

2] Frequency is recorded with 3 leading digits before and 4 significant digits after the "M" [including zeroes].

- [Ex. 3.579545MHz = 003M5795; 14.31818MHz = 014M3181; 25MHz = 025M0000; 125MHz = 125M0000]
- 3] Consult factory for availability.

Not all performance combinations and frequencies may be available. Contact your local CTS Representative or CTS Customer Service for availability.

This product is specified for use only in standard commercial applications. Supplier disclaims all express and implied warranties and liability in connection with any use of this product in any non-commercial applications or in any application that may expose the product to conditions that are outside of the tolerances provided in its specification.

DOC# 008-0244-0 Rev. I

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Electrical Specifications

Operating Conditions

operating conditions							
PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNIT	
Maximum Supply Voltage	V _{CC}	-	-0.5	-	7.0	V	
Maximum Control Voltage	Vc	-	-0.5	-	V _{CC}	V	
Supply Voltage		11.09/	2.97	3.3	3.63	N/	
Supply Voltage	V _{CC}	±10%	4.5	5.0	5.5	V	
Supply Current	Тур	pical @ C _L = 15 pF, V _{CC} = +3.3V, T _A = +25°C					
		1.5MHz to <20MHz	-	5	20		
	Icc	20MHz to <40MHz	-	8	30	mA	
		40MHz to <60MHz	-	10	40		
		60MHz to 122.88MHz	-	12	45		
Output Load	CL	-	-	-	15	pF	
0	т		-20	. 25	+70	*	
Operating Temperature	T _A	-	-40	+25	+85	°C	
Storage Temperature	T _{STG}	-	-55	-	+125	°C	

Frequency Stability

PARAMETER	SYMBOL	CONDITIONS	MIN TYP MAX		UNIT	
Frequency Range	f _O	-	1	.5 - 122.88	3	MHz
Frequency Stability [Note 1]	$\Delta f/f_O$	±20ppm stability, -20°C to +70°C only	2	0, 25, or 50)	±ppm
Absolute Pull Range [Note 2]	APR	-	50, 80, 100		±ppm	
Aging	∆f/f ₂₅	First Year @ +25°C, nominal V $_{CC}$ and V $_{C}$	-3	-	3	ppm

1.] Inclusive of initial tolerance at time of shipment, changes in supply voltage, load, temperature and 1st year aging.

2.] Minimum guaranteed frequency shift from f_0 over variations in temperature, aging, power supply and load.

Output Parameters

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNIT	
Output Type	-	-		HCMOS		-	
	V _{OH}	Logic '1' Level, CMOS Load	0.9V _{CC}	-	-		
Output Voltage Levels	V _{OL}	Logic '0' Level, CMOS Load	-	-	$0.1 V_{CC}$	V	
Output Duty Cycle	SYM	@ 50% Level	45	- 55 %			
Rise and Fall Time	T _R , T _F	@ 10%/90% Levels	-	3.5	5.0	ns	
Start Up Time	Ts	Application of V_{CC}	-	-	10	ms	
Enable Function	Standby						
Enable Input Voltage	VIH	Pin 2 or Pin 5 Logic '1', Output Enabled	$0.7V_{CC}$	-	-	V	
Disable Input Voltage	VIL	Pin 2 or Pin 5 Logic '0', Output Standby	-	-	$0.3V_{CC}$	V	
Standby Current	I _{STB}	Pin 2 or Pin 5 Logic '0', Output Standby	-	-	10	μΑ	
Enable Time	T _{PLZ}	Pin 2 or Pin 5 Logic '1'	-	-	2	ms	
Phase Jitter, RMS	tjrms	tjrms Bandwidth 12kHz - 20MHz - C		0.5	1	ps	
Phase Noise	-	- See Typical Plots		-	-		

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Electrical Specifications

Control Voltage

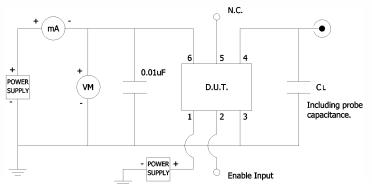
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Control Voltono	N/	V _{CC} = +3.3V	0.30	1.65	3.00	
Control Voltage	Vc	V _{CC} = +5.0V 0.50 2.50		2.50	4.50	V
$\label{eq:frequency} \textbf{Frequency Deviation} \qquad \Delta f/f_{O}$		25°C at Time of Shipment, over Vc range		135		ppm
Linearity L		Best Straight Line Fit -		5	10	%
Gain Transfer Kv		Pull Sensitivity; @ +1.65V, +25°C	-	65	-	ppm/V
Input Impedance	Z _{Vc}	-	100	-	-	kOhms
Modulation Roll-off -		@ -3dB	10	-	-	kHz
Transfer Function	-	-		Positive		-

Enable Truth Table

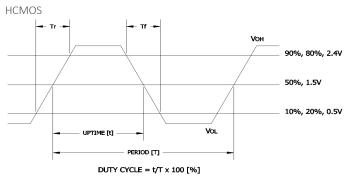
Pin 2	Pin 4
Logic '1'	Output Enabled
Open	Output Enabled
	Output Disabled,
Logic 'O'	High Impedance

Test Circuit

HCMOS



Output Waveform



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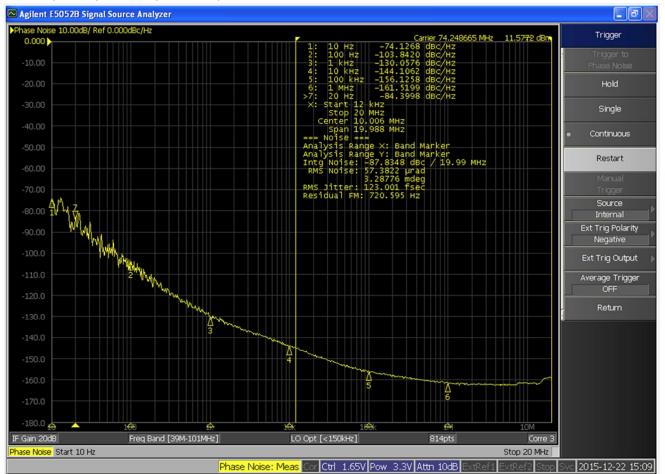


Electrical Specifications

Performance Data

Phase Noise [typical]

74.25MHz, HCMOS, V_{CC} = +3.3V, V_C = +1.65V, T_A = +25°C



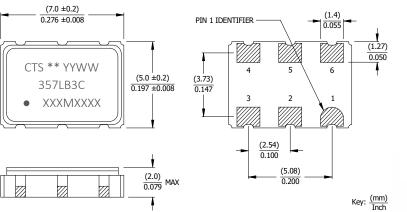
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Mechanical Specifications

Package Drawing

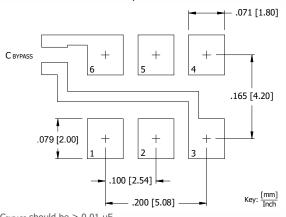


1. ** - Manufacturing Site Code.

Marking Information

- 2. YYWW Date code, YY year, WW week.
- 3. Truncated CTS part number.
- 4. XXXMXXXX Frequency marked with 4
- significant digits after the 'M'.

Recommended Pad Layout



C_{BYPASS} should be ≥ 0.01 uF.

Pin Assignments

Pin	Symbol	Function
1	Vc	Voltage Control
2	EOH	Enable [standby]
3	GND	Circuit & Package Ground
4	Output	RF Output
5	N.C.	No Connect
6	V _{CC}	Supply Voltage

Notes

- 1. JEDEC termination code (e4). Barrier-plating is nickel [Ni] with gold [Au] flash plate.
- 2. Reflow conditions per JEDEC J-STD-020; +260°C maximum, 20 seconds.
- 3. MSL = 1.

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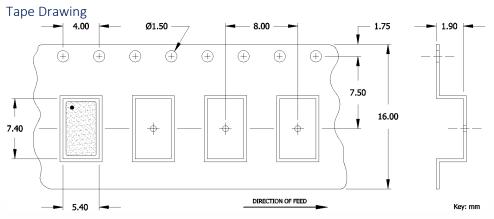
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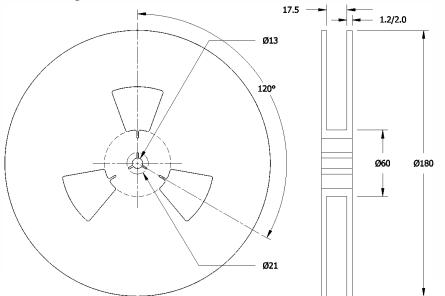
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Packaging - Tape and Reel



Reel Drawing



Notes

- 1. Device quantity is 1k pieces maximum per 180mm reel.
- 2. Complete CTS part number, frequency value and date code information must appear on reel and carton labels.

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Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

CTS:

 357LB3I001M5440
 357LB3I004M0960
 357LB3I006M1760
 357LB3I016M3840
 357LB3I019M4400

 357LB3I020M0000
 357LB3I027M00000
 357LB3I032M7680
 357LB3I035M3280
 357LB3I038M8800

 357LB3I040M0000
 357LB3I044M7360
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 357LB3I061M4400
 357LB3I077M7600

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 357LC5C027M1450
 357LD3C027M0000

 357LD3I012M2880
 357LD5C027M0000
 357LD5C040M0000
 357LD5C040M0000
 357LD5C040M0000

Tusonix / CTS:

357-001-X5U0-152MLF 357-000-X5U0-102M