HCV1707

High current power inductors



Product features

- · Flat-wire construction
- · Low DCR, high efficiency
- · Secure 3 terminal mounting
- 17.8 mm x 14.35 mm footprint surface mount package in a 6.9 mm height
- · Ferrite core material
- Moisture Sensitivity Level: 1

Applications

• Compatible with Picor® Cool-Power® ZVS Buck and Buck-Boost Regulator Families

Environmental data

- Storage temperature range (Component): $-55~^{\circ}\mathrm{C}$ to $+125~^{\circ}\mathrm{C}$
- Operating temperature range: -55 °C to +125 °C (ambient plus self-temperature rise)
- Solder reflow temperature:
 J-STD-020 (latest revision) compliant
- Halogen free, lead free, RoHS compliant





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Product specifications

Part Number ⁷	OCL¹ (μΗ) ±10%	FLL- (μH) minimum	I ³ (A)	I _{sat} 1 ⁴ (A)	I _{sat} 2 ⁵ (A)	I _{sat} 3 ⁶ (A)	DCR (mΩ) ±10% @ +20 °C
HCV1707R1-R48-R	0.48	0.42	32	55	68	47	1.7

- 1. Open Circuit Inductance (OCL) Test Parameters: 100 kHz, 0.1 Vrms, 0.0 Adc, +25 °C
- 2. Full Load Inductance (FLL) Test Parameters: 100 kHz, 0.1 Vrms, I_{sat}1, +25 °C
- 3. I_{ms}: DC current for an approximate temperature rise of 40 °C without core loss. Derating is necessary for AC currents. PCB layout, trace thickness and width, air-flow, and proximity of other heat generating components will affect the temperature rise. It is recommended that the temperature of the part not exceed +125 °C under worst case operating conditions verified in the end application.
- 4. I_{sat}1: Peak current for approximately 2% rolloff @ +25 °C
- 5. I_{sat}2: Peak current for approximately 2% rolloff @ -55 °C
- 6. I 3: Peak current for approximately 2% rolloff @ +125 °C
- 7. Part Number Definition: HCV1707Rx-Rxxx-R HCV1707= Product code and size

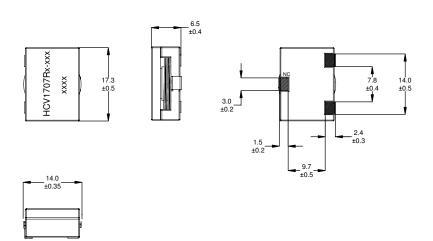
Rx= Version indicator

Rxx= Inductance value in µH, R= decimal point,

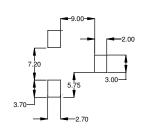
If no R is present last character equals number of zeros

-R suffix = RoHS compliant

Dimensions (mm)



Recommended Pad Layout



Schematic

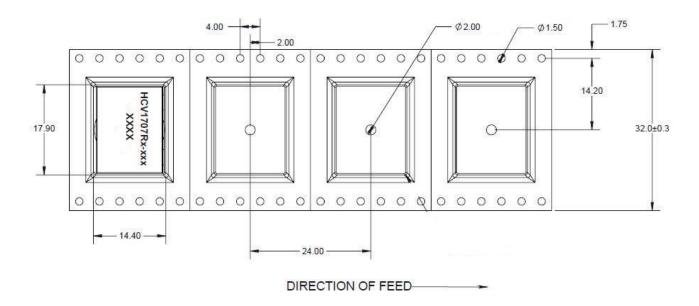


Part marking: HCV1707Rx-Rxx, Rxx= inductance value in uH, R= decimal point, if no R is present last character equals number of zeros xxxx=lot code

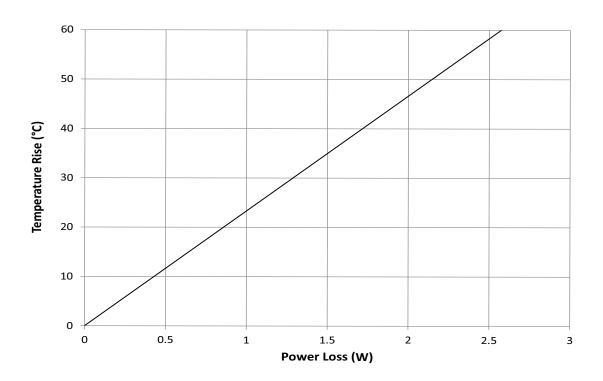
All soldering surface to be coplanar within 0.1 millimeters Tolerances are ± 0.15 millimeters unless stated otherwise Pad layout tolerances are ± 0.1 millimeters unless stated otherwise Pin NC is for mounting stability. No connection. Do not route traces or vias underneath the inductor

Packaging information (mm)

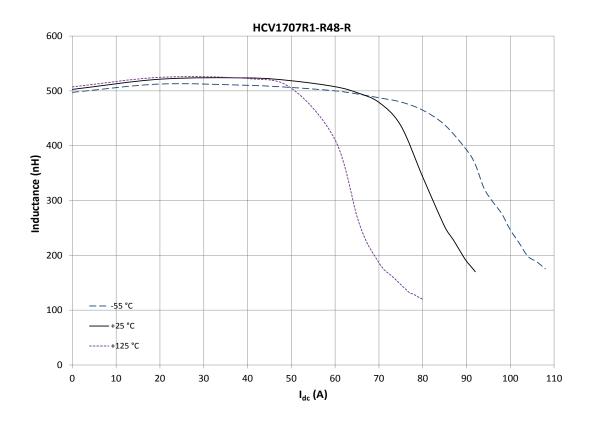
Supplied in tape and reel packaging, 350 parts per 13" diameter reel



Temperature rise vs. total loss



Inductance characteristics



Solder reflow profile

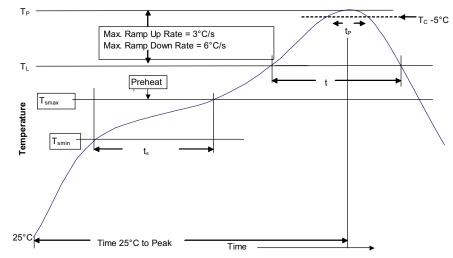


Table 1 - Standard SnPb Solder (T_C)

Package Thickness	Volume mm3 <350	Volume mm3 ≥350
<2.5mm)	235 °C	220 °C
≥2.5mm	220 °C	220 °C

Table 2 - Lead (Pb) Free Solder (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ 350 - 2000	Volume mm³ >2000
<1.6mm	260 °C	260 °C	260 °C
1.6 – 2.5mm	260 °C	250 °C	245 °C
>2.5mm	250 °C	245 °C	245 °C

Reference JDEC J-STD-020

Profile Feature	Standard SnPb Solder	Lead (Pb) Free Solder
Preheat and Soak • Temperature min. (T _{smin})	100 °C	150 °C
• Temperature max. (T _{smax})	150 °C	200 °C
• Time (T _{Smin} to T _{Smax}) (t _S)	60-120 Seconds	60-120 Seconds
Average ramp up rate T_{Smax} to T_p	3°C/ Second Max.	3 °C/ Second Max.
Liquidous temperature (TL) Time at liquidous (tL)	183 °C 60-150 Seconds	217 °C 60-150 Seconds
Peak package body temperature (Tp)*	Table 1	Table 2
Time (t _p)** within 5 °C of the specified classification temperature (T _C)	20 Seconds**	30 Seconds**
Average ramp-down rate (T_p to T_{smax})	6 °C/ Second Max.	6 °C/ Second Max.
Time 25 °C to Peak Temperature	6 Minutes Max.	8 Minutes Max.

 $^{^{\}star}$ Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum.

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^{**} Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.

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