

1. General description

WSJM65R044DW is a high voltage N-channel MOSFET in TO247 package, which utilizes the advanced super-junction technology to provide superior FOM $R_{DS(on)} * Q_g$ among silicon based MOSFETs. It is particularly suitable for applications require extreme high efficiency and power density.



2. Features and benefits

- Superior FOM $R_{DS(on)} * Q_g$
- Extremely low switching loss
- Integrated ultrafast body diode
- 100% avalanche tested

3. Applications

- EV charger
- High efficiency power supplies
- On board charger
- Inverters

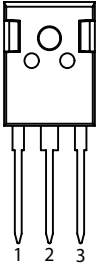
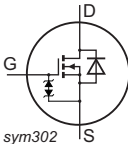
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes	Values			Unit
Absolute maximum rating							
V _{DS}	drain-source voltage			650			V
V _{GS}	gate-source voltage			±30			V
I _D	continuous drain current	T _{mb} = 25 °C		67			A
P _{tot}	power dissipation	T _{mb} = 25 °C		480			W
T _j	junction temperature			-55 to 150			°C
Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
Static characteristics							
R _{DS(on)}	drain-source on-state resistance	V _{GS} = 10 V, I _D = 34 A		-	36	44	mΩ
Dynamic characteristics							
Q _{G(tot)}	total gate charge	I _D = 34 A; V _{DS} = 400 V; V _{GS} = 10 V		-	123	-	nC
E _{OSS}	coss stored energy	V _{GS} = 0 V; V _{DS} = 0 to 400 V		-	14.5	-	μJ

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		
2	D	drain		
3	S	source		
mb	D	mounting base; connected to drain		

6. Ordering information

Table 3. Ordering information

Type number	Package name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WSJM65R044DW	TO247	WSJM65R044DWQ	Tube	30	TO247N	20-July-2016

7. Marking

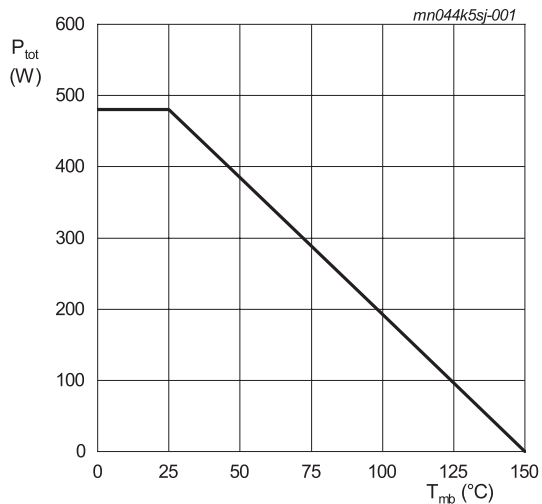
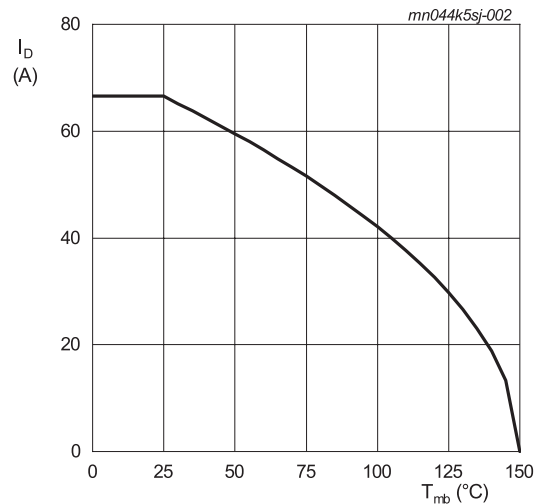
Table 4. Marking codes

Type number	Marking codes
WSJM65R044DW	WSJM 65R044DW

8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
V_{DS}	drain-source voltage			650	V
V_{GS}	gate-source voltage			± 30	V
I_D	continuous drain current	$T_{mb} = 25\text{ }^{\circ}\text{C}$		67	A
		$T_{mb} = 100\text{ }^{\circ}\text{C}$		42	A
I_{DM}	pulsed drain current	$T_{mb} = 25\text{ }^{\circ}\text{C}$		268	A
P_{tot}	power dissipation	$T_{mb} = 25\text{ }^{\circ}\text{C}$		480	W
E_{AS}	single pulse drain-to-source avalanche	$I_{AS} = 7.6\text{ A}$; $R_{GS} = 25\text{ }\Omega$; $V_{DD} = 50\text{ V}$; $T_J = 25\text{ }^{\circ}\text{C}$		288	mJ
E_{AR}	repetitive avalanche energy	$I_{AS} = 7.6\text{ A}$; $R_{GS} = 25\text{ }\Omega$; $V_{DD} = 50\text{ V}$; $T_J = 25\text{ }^{\circ}\text{C}$		1.01	mJ
I_{AS}	avalanche current, single pulse			7.6	A
dv/dt	MOSFET dv/dt ruggedness			72	V/ns
dv/dt	reverse diode dv/dt			50	V/ns
dI _F /dt	maximum diode commutation speed			850	A/ μ s
T_{stg}	storage temperature			-55 to 150	$^{\circ}\text{C}$
T_J	junction temperature			-55 to 150	$^{\circ}\text{C}$


Fig. 1. Normalized total power dissipation as a function of mounting base temperature

Fig. 2. Continuous Drain Current as a function of mounting base temperature

9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base			-	0.2	0.26	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air		-	45	-	K/W

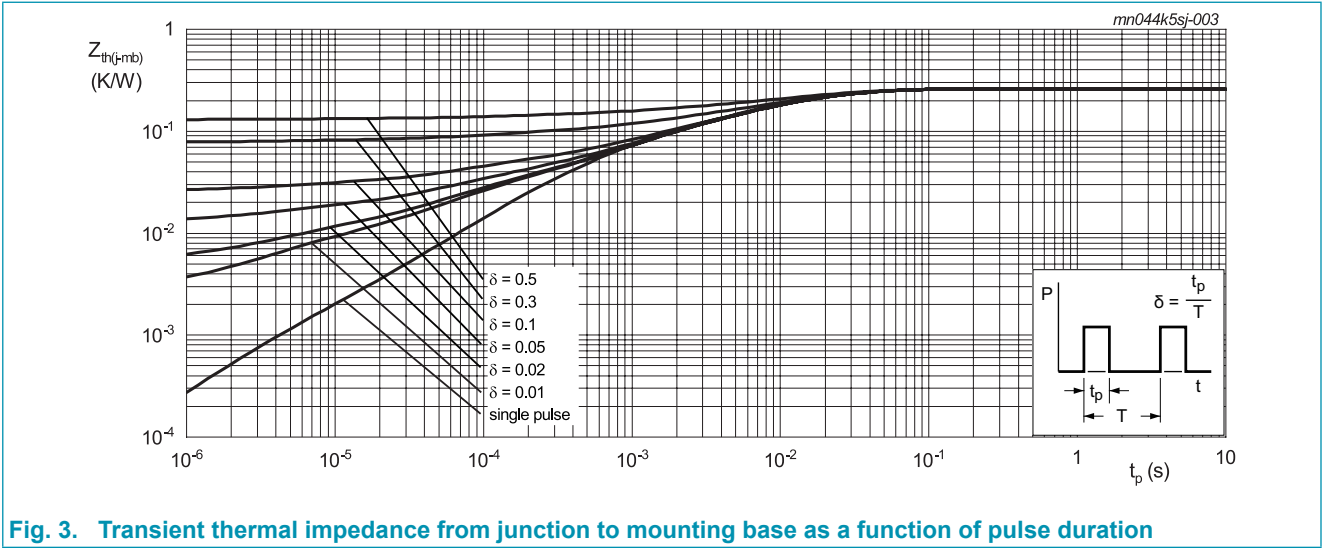


Fig. 3. Transient thermal impedance from junction to mounting base as a function of pulse duration

10. Characteristics

Table 7. Characteristics

$T_j = 25\text{ }^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
Static characteristics							
V _{(BR)DSS}	drain-source breakdown voltage	I _D = 1 mA; V _{GS} = 0 V		650	-	-	V
V _{GS(th)}	gate-source threshold voltage	I _D = 250 μA; V _{DS} = V _{GS}		3	-	5	V
I _{DSS}	drain leakage current	V _{DS} = 650 V; V _{GS} = 0 V		-	-	10	μA
		V _{DS} = 650 V; V _{GS} = 0 V; T _j = 125 °C		-	200	-	μA
I _{GSS}	gate leakage current	V _{GS} = ±30 V; V _{DS} = 0 V		-	-	±500	nA
R _{DS(on)}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 34 A		-	36	44	mΩ
R _G	gate resistance	f = 1 MHz		-	32	-	Ω
Dynamic characteristics							
Q _{G(tot)}	total gate charge	I _D = 34 A; V _{DS} = 400 V; V _{GS} = 10 V		-	123	-	nC
Q _{GS}	gate-source charge			-	40	-	nC
Q _{GD}	gate-drain charge			-	50	-	nC
C _{iss}	input capacitance	V _{DS} = 400 V; V _{GS} = 0 V; f = 250 kHz		-	6148	-	pF
C _{oss}	output capacitance			-	91	-	pF
C _{rss}	reverse transfer capacitance			-	0.6	-	pF
C _{o(er)}	effective output capacitance, energy related	V _{GS} = 0 V; V _{DS} = 0 to 400 V		-	181	-	pF
C _{o(tr)}	effective output capacitance, time related			-	1598	-	pF
t _{d(on)}	turn-on delay time	V _{DS} = 400 V; V _{GS} = 10 V; R _G = 2 Ω; I _D = 34 A		-	295	-	ns
t _r	rise time			-	25	-	ns
t _{d(off)}	turn-off delay time			-	433	-	ns
t _f	fall time			-	10	-	ns
Source-drain diode							
V _{SD}	source-drain voltage	V _{GS} = 0 V; I _S = 34 A		-	0.94	1.2	V
I _S	body-diode continuous current	T _{mb} = 25 °C		-	-	67	A
t _{rr}	reverse recovery time	V _R = 400 V; I _F = 34 A; dI _F /dt = 100 A/μs		-	184	-	ns
Q _{rr}	reverse recovered charge			-	1.6	-	μC
I _{rrm}	reverse recovery current			-	18	-	A

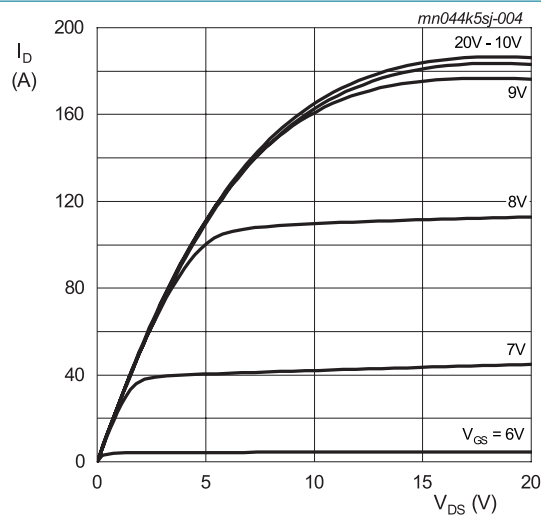
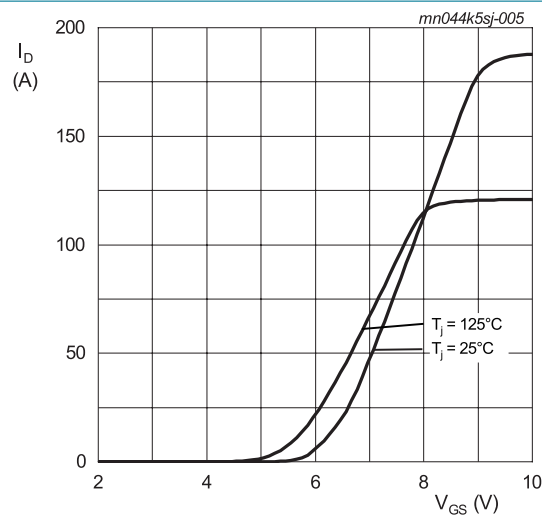
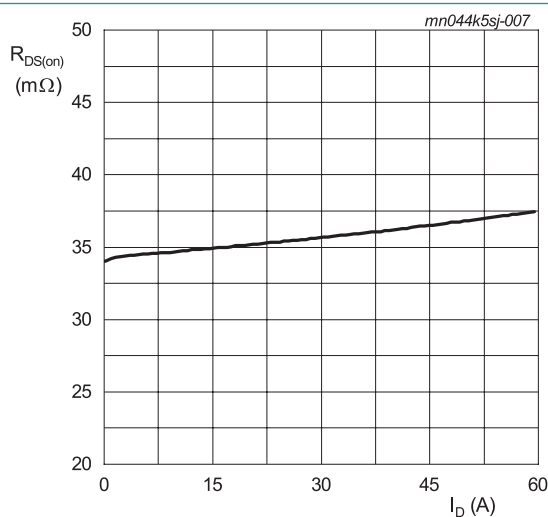


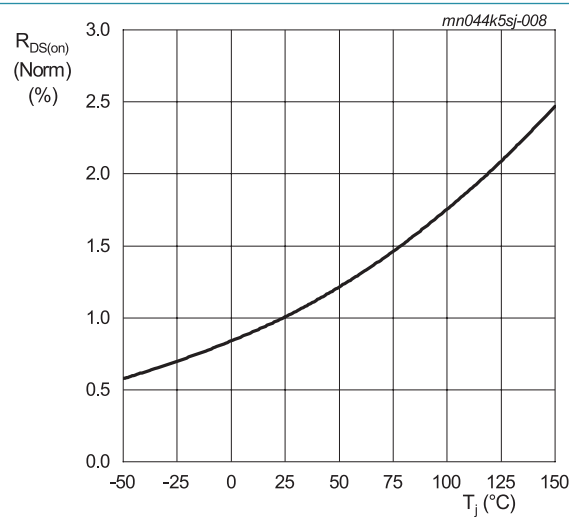
Fig. 4. Drain current as a function of drain-source voltage; typical values



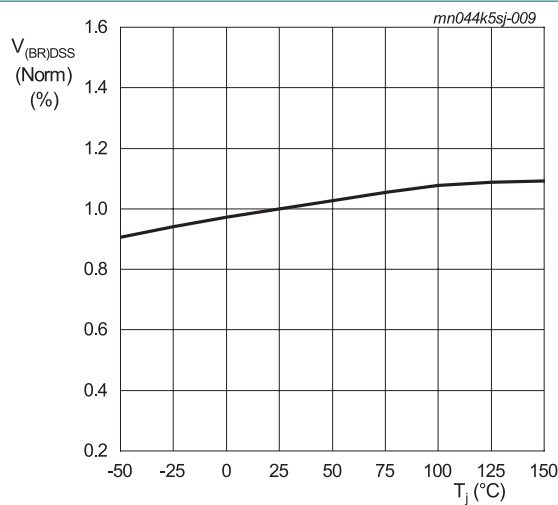
$V_{DS} = 20\text{ V}$
Fig. 5. Drain current as a function of gate-source voltage; typical values



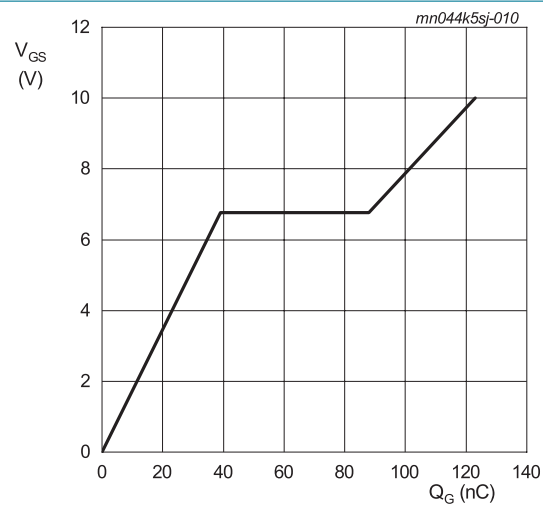
$V_{GS} = 10\text{ V}$
Fig. 6. Drain-source on-state resistance as a function of drain current; typical values



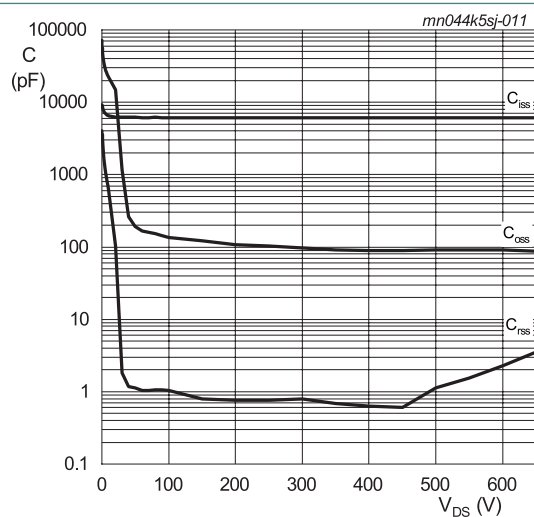
$V_{GS} = 10\text{ V}; I_D = 34\text{ A}$
Fig. 7. Normalized drain-source on-state resistance as a function of junction temperature



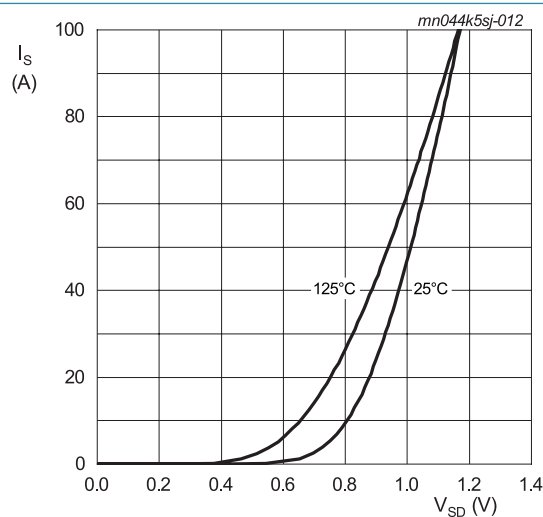
$I_D = 1\text{ mA}$
Fig. 8. Normalized drain-source breakdown voltage as a function of junction temperature



$I_D = 34\text{ A}; V_{DS} = 400\text{ V}$
Fig. 9. Gate-source voltage as a function of gate charge; typical values



$V_{GS} = 0\text{ V}; f = 250\text{ kHz}$
Fig 10. Capacitances as a function of drain-source voltage; typical values



$V_{GS} = 0\text{ V}$
Fig 11. Source current as a function of source-drain voltage; typical values

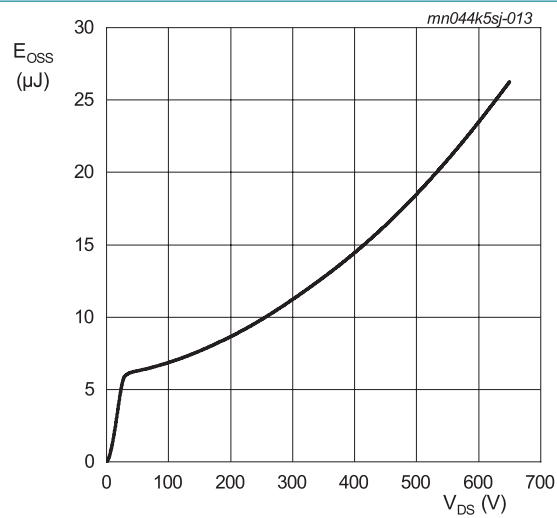


Fig. 12. Output capacitance stored energy as a function of drain-source voltage

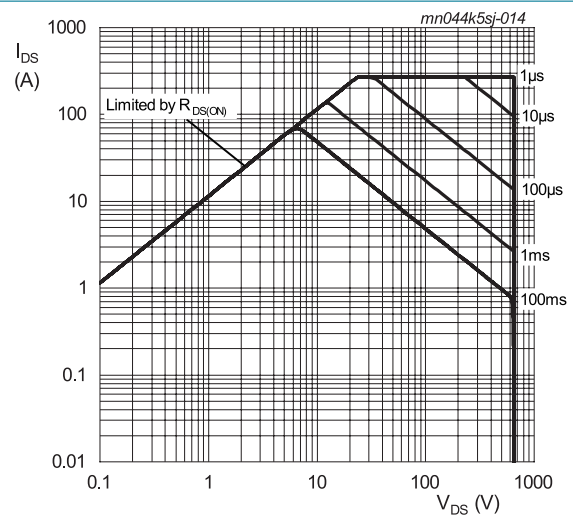
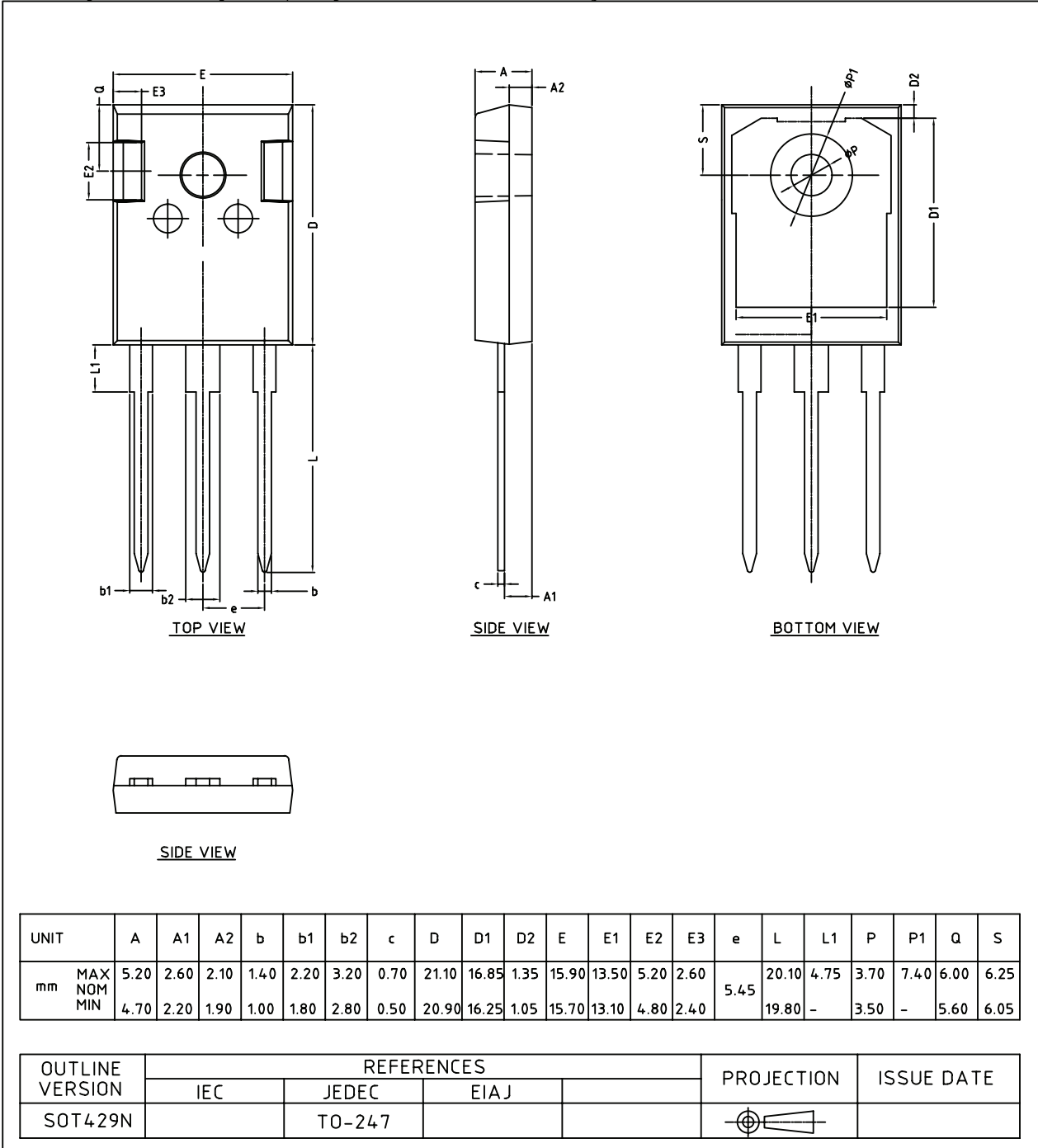


Fig. 13. Safe operating area

11. Package outline

Plastic single-ended through-hole package; heatsink mounted; 1 mounting hole; 3-lead TO-247 SOT429N



12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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- [2] The term 'short data sheet' is explained in section "Definitions".
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