

1. General description

WMS30N034V is a high performance logic level N-channel MOSFET in PDFN5X6 package, which utilizes advanced Trench MOSFET technology to provide low $R_{DS(on)}$ and gate charge. It is designed and qualified in a wide range of industrial and consumer applications.



2. Features and benefits

- Advance High Cell Density Trench Technology
- Low $R_{DS(on)}$ to Minimize Conduction Losses
- Low Capacitance to Minimize Switching Losses
- Optimized Gate Charge to Minimize Driver Losses
- 100% UIS Tested
- RoHS Compliant and Halogen Free

3. Applications

- DC-DC Converters
- BLDC Motor Control
- Load Switch
- Lithium-ion Battery Protection

4. Quick reference data

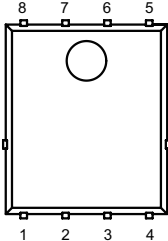
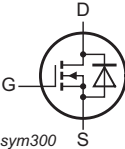
Table 1. Quick reference data

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Symbol	Parameter	Conditions	Notes	Values			Unit
Absolute maximum rating							
V _{DS}	drain-source voltage			30			V
V _{GS}	gate-source voltage			±20			V
I _D	continuous drain current	V _{GS} = 10 V; T _{mb} = 25 °C	[1]	105			A
P _{tot}	power dissipation	T _{mb} = 25 °C		54			W
T _j	junction temperature			-55 to 150			°C
Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
Static characteristics							
R _{DS(on)}	drain-source on-state resistance	V _{GS} = 10 V, I _D = 20 A		-	2.8	3.4	mΩ
		V _{GS} = 4.5 V, I _D = 20 A		-	3.8	6.0	mΩ
Dynamic characteristics							
Q _{G(tot)}	total gate charge	I _D = 20 A; V _{DS} = 15 V; V _{GS} = 10 V		-	74	-	nC

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1-3	S	source		
4	G	gate		
5-8	D	drain		

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WMS30N034V	PDFN5X6	WMS30N034VJ	Reel	4000	PDFN5X6N	21-Jul-2022

7. Marking

Table 4. Marking codes

Type number	Marking codes
WMS30N034V	WMS 30N034

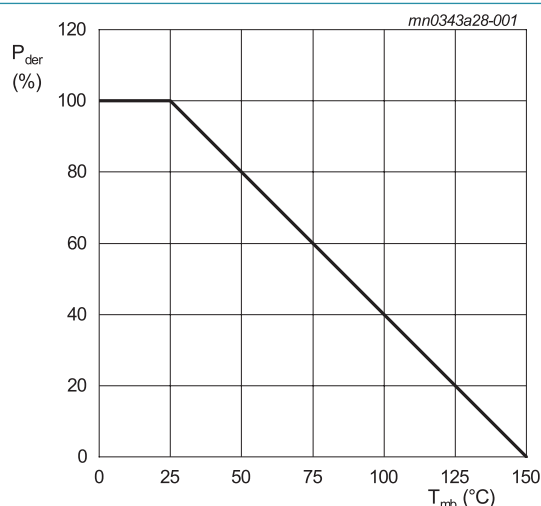
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

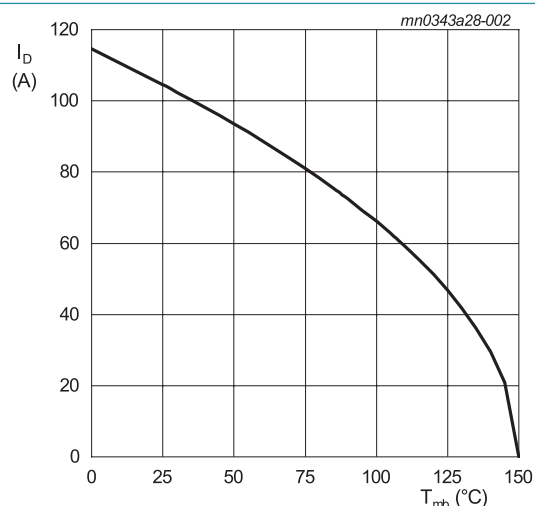
Symbol	Parameter	Conditions	Notes	Values	Unit
V_{DS}	drain-source voltage			30	V
V_{GS}	gate-source voltage			± 20	V
I_D	continuous drain current	$V_{GS} = 10\text{ V}; T_{mb} = 25\text{ }^{\circ}\text{C}$	[1]	105	A
		$V_{GS} = 10\text{ V}; T_{mb} = 120\text{ }^{\circ}\text{C}$		51	A
I_{DM}	pulsed drain current	$t_p = 10\text{ }\mu\text{s}; T_{mb} = 25\text{ }^{\circ}\text{C}$		420	A
P_{tot}	power dissipation	$T_{mb} = 25\text{ }^{\circ}\text{C}$		54	W
E_{as}	single pulse drain-to-source avalanche	$I_{AS} = 26\text{ A}; L = 0.1\text{ mH}; R_{GS} = 25\text{ }\Omega;$ $V_{GS} = 10\text{ V}; T_j = 25\text{ }^{\circ}\text{C}$		34	mJ
T_{stg}	storage temperature			-55 to 150	$^{\circ}\text{C}$
T_j	junction temperature			-55 to 150	$^{\circ}\text{C}$

[1] Calculated continuous current based on maximum allowable junction temperature. Package current limitation is 68 A.



$$P_{der} = (P_{tot} / P_{tot(25\text{ }^{\circ}\text{C})}) \times 100\%$$

Fig. 1. Normalized total power dissipation as a function of mounting base temperature



$$V_{GS} = 10\text{ V}$$

Fig. 2. Continuous Drain Current as a function of mounting base temperature

9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base			-	1.8	2.3	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[2]	-	-	55	K/W

[2] Surface mount on FR4 board of 1 inch², 1 oz copper.

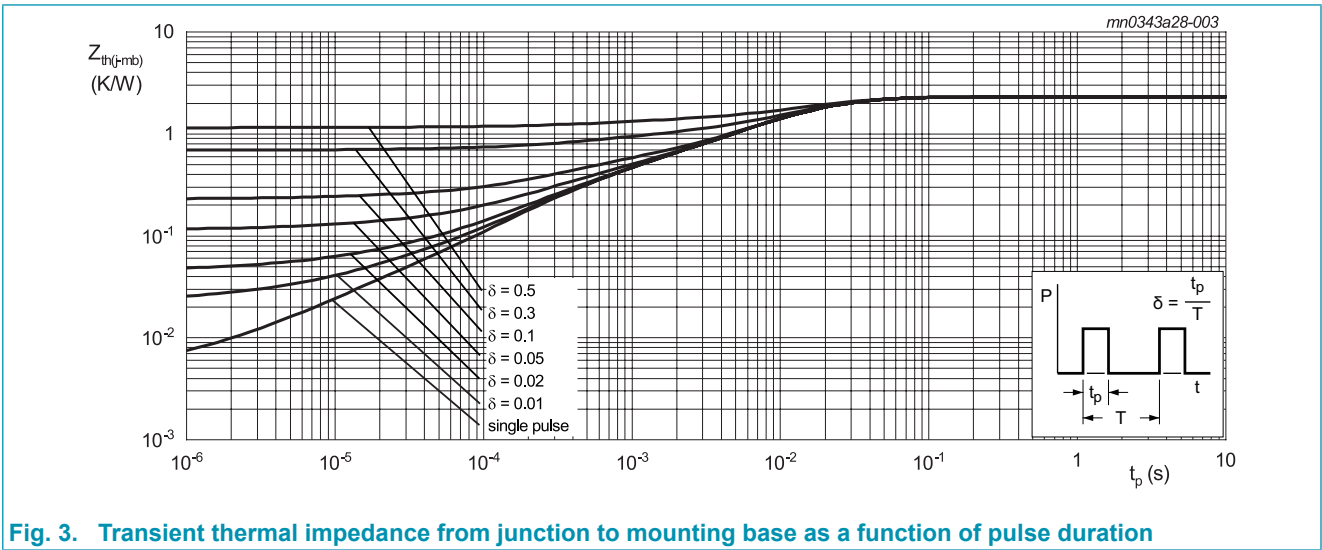


Fig. 3. Transient thermal impedance from junction to mounting base as a function of pulse duration

10. Characteristics

Table 7. Characteristics
 $T_j = 25\text{ }^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
Static characteristics							
V _{(BR)DSS}	drain-source breakdown voltage	I _D = 250 μA; V _{GS} = 0 V		30	-	-	V
V _{GS(th)}	gate-source threshold voltage	I _D = 250 μA; V _{DS} = V _{GS}		1	1.5	2.4	V
I _{DSS}	drain leakage current	V _{DS} = 30 V; V _{GS} = 0 V		-	-	1	μA
		V _{DS} = 30 V; V _{GS} = 0 V; T _j = 125 °C		-	-	100	μA
I _{GSS}	gate leakage current	V _{GS} = ±20 V; V _{DS} = 0 V		-	-	±100	nA
R _{DS(on)}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 20 A		-	2.8	3.4	mΩ
		V _{GS} = 4.5 V; I _D = 20 A		-	3.8	6.0	mΩ
R _G	gate resistance	f = 1 MHz		-	2.4	-	Ω
Dynamic characteristics							
Q _{G(tot)}	total gate charge	I _D = 20 A; V _{DS} = 15 V; V _{GS} = 10 V		-	74	-	nC
Q _{GS}	gate-source charge			-	12	-	nC
Q _{GD}	gate-drain charge			-	13	-	nC
C _{iss}	input capacitance	V _{DS} = 15 V; V _{GS} = 0 V; f = 1 MHz		-	3980	-	pF
C _{oss}	output capacitance			-	438	-	pF
C _{rss}	reverse transfer capacitance			-	362	-	pF
t _{d(on)}	turn-on delay time	V _{DS} = 15 V; V _{GS} = 10 V; R _G = 6 Ω; I _D = 20 A		-	8.5	-	ns
t _r	rise time			-	22	-	ns
t _{d(off)}	turn-off delay time			-	47	-	ns
t _f	fall time			-	31	-	ns
Source-drain diode							
V _{SD}	source-drain voltage	V _{GS} = 0 V; I _S = 1 A		-	0.69	1	V
		V _{GS} = 0 V; I _S = 1 A; T _j = 125 °C		-	0.52	-	V
I _S	body-diode continuous current	T _{mb} = 25 °C		-	-	61	A
t _{rr}	reverse recovery time	V _{GS} = 0 V; I _S = 20 A; di/dt = 100 A/μs		-	17	-	ns
Q _{rr}	reverse recovered charge			-	7.9	-	nC
I _{rrm}	reverse recovery current			-	0.9	-	A

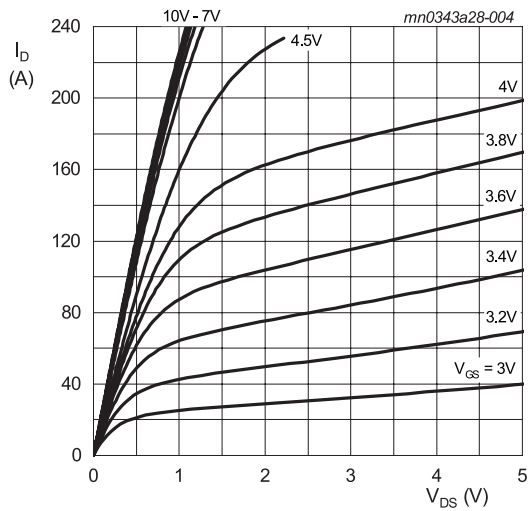


Fig. 4. Drain current as a function of drain-source voltage; typical values

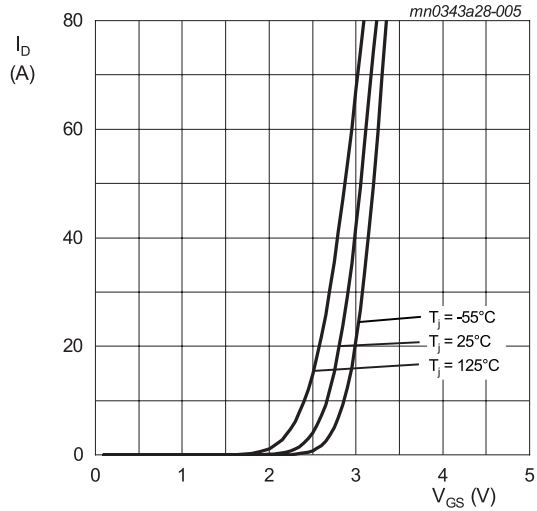


Fig. 5. Drain current as a function of gate-source voltage; typical values

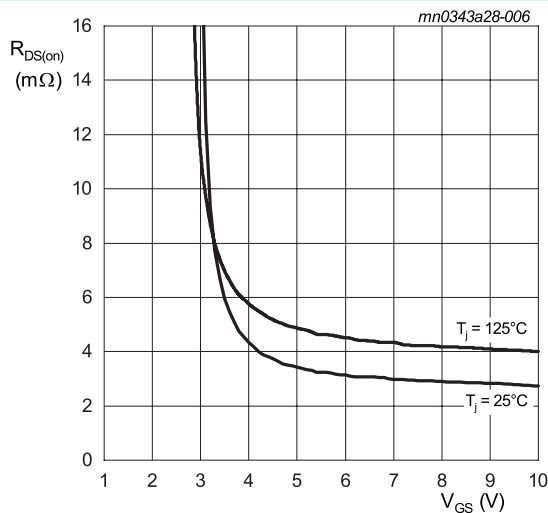


Fig. 6. Drain-source on-state resistance as a function of gate-source voltage; typical values

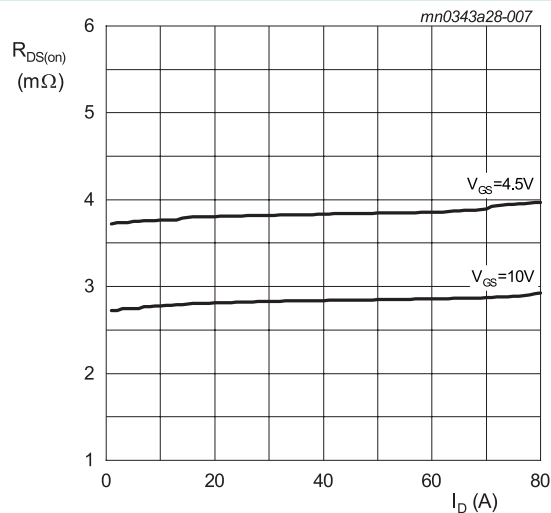
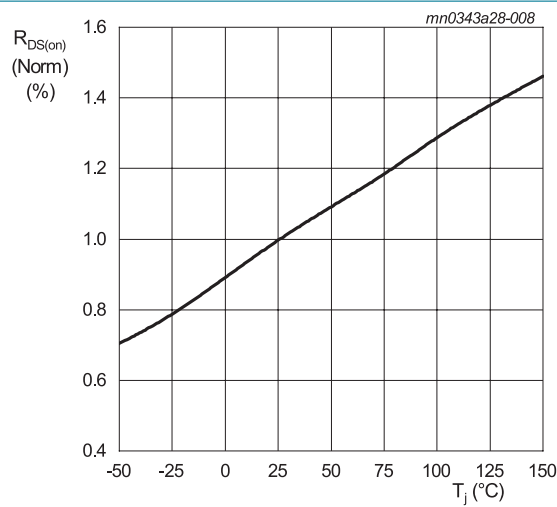
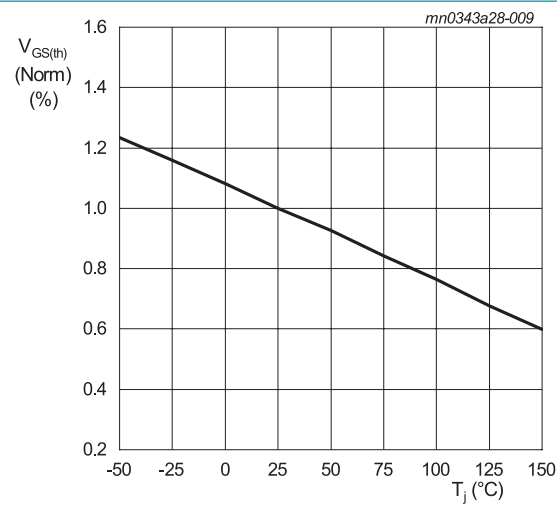


Fig. 7. Drain-source on-state resistance as a function of drain current; typical values



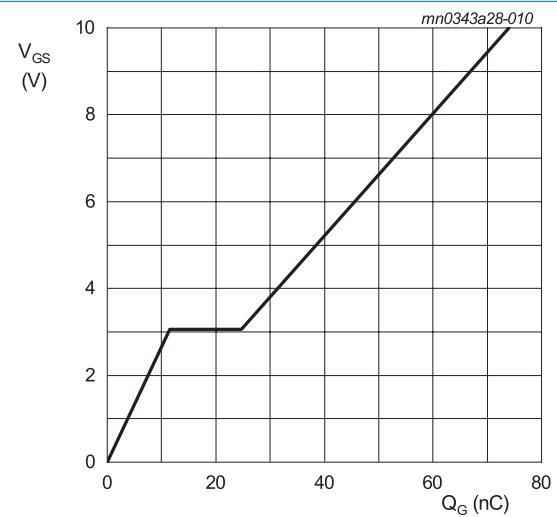
$V_{GS} = 10\text{ V}; I_D = 20\text{ A}$

Fig. 8. Normalized drain-source on-state resistance as a function of junction temperature



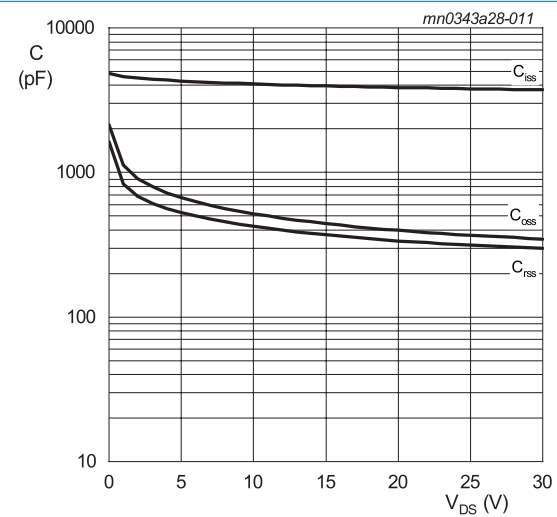
$V_{DS} = V_{GS}; I_D = 250\text{ }\mu\text{A}$

Fig. 9. Normalized gate-source threshold voltage as a function of junction temperature



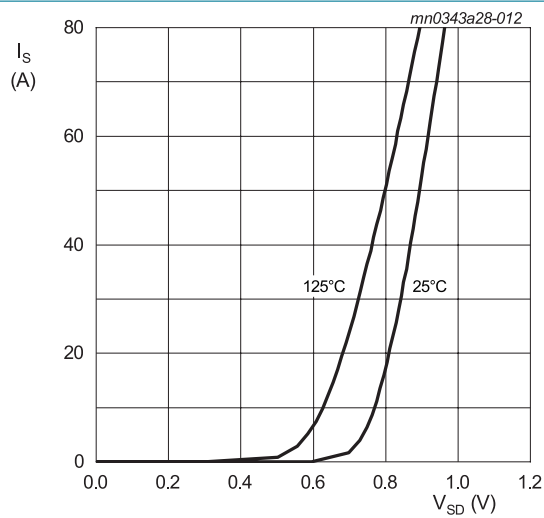
$I_D = 20\text{ A}; V_{DS} = 15\text{ V}$

Fig. 10. Gate-source voltage as a function of gate charge; typical values



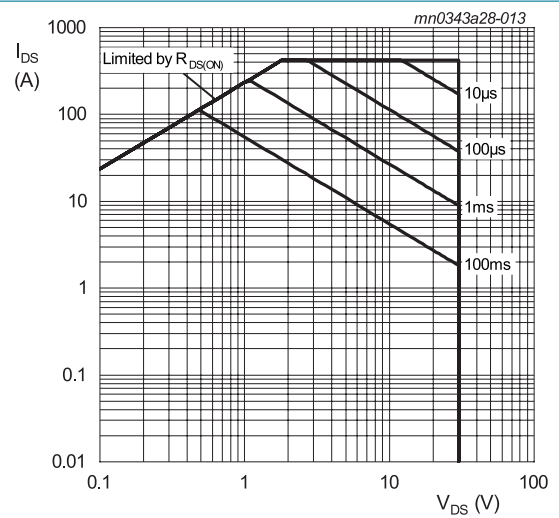
$V_{GS} = 0\text{ V}; f = 1\text{ MHz}$

Fig 11. Capacitances as a function of drain-source voltage; typical values



$V_{GS} = 0\text{ V}$

Fig 12. Source current as a function of source-drain voltage; typical values

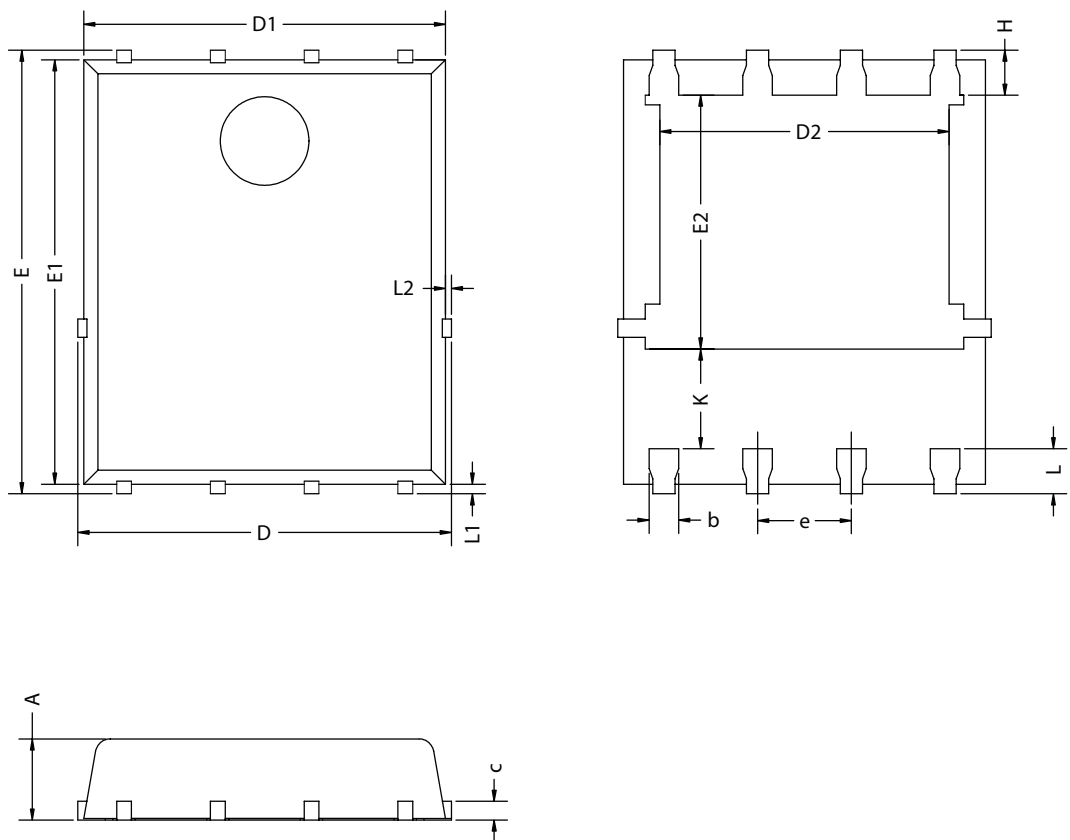


$T_{mb} = 25\text{ °C}$

Fig 13. Safe operating area

11. Package outline

PDFN5X6



Unit	A	b	c	D	D1	D2	E	E1	E2	e	H	K	L	L1	L2
min	1.00	0.35	0.21		4.80	3.91	5.90	5.70	3.34	1.27 (BSC)	0.51	1.10	0.51	0.06	
max	1.20	0.45	0.34	5.10	5.00	4.11	6.10	5.80	3.54		0.71		0.71	0.20	0.10

Note:
1. All dimensions don't include mold flash and metal protrusion.

12. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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- [1] Please consult the most recently issued document before initiating or completing a design.
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