WG50N65LDJ1

IGBT

Rev.01 - 18 May 2023 — Product data sheet

1. General description

WG50N65LDJ1 uses advanced Fine Trench Field-stop IGBT technology with anti-parallel diode in TO3PF package to provide extremely low on state voltage, and minimal switching performance. This device is ideal for low switching frequency power conversion applications.

2. Features and benefits

- Positive Temperature efficient for Easy Parallel Operating
- High Current Capability
- Low saturation Voltage $V_{CE(Sat)}$ = 1.25 V(Typ.) @ I_C = 50 A
- EMI Improved Design

3. Applications

- Solar Inverter
- UPS
- PFC
- Converters

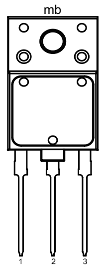
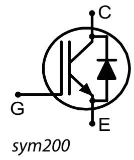
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter		Notes	Value			Unit
V_{CE}	Collector-emitter voltage, $T_j \geq 25$ °C			650			V
I_C	DC collector current, limited by $T_{j(max)}$ T_C = 100 °C			22			A
Symbol	**Parameter**	**Conditions**	**Notes**	**Min**	**Typ**	**Max**	**Unit**
Static characteristics							
$V_{CE(sat)}$	Collector-emitter saturation voltage	V_{GE} = 15 V; I_C = 50 A; T_j = 25 °C		-	1.25	1.55	V

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		
2	C	collector		
3	E	emitter		
mb	n.c.	mounting base; isolated		

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WG50N65LDJ1	TO3PF	WG50N65LDJ1Q	Tube	30	SOT1293	16-Mar-2006

7. Marking

Table 4. Marking codes

Type number	Marking codes
WG50N65LDJ1	WG50N 65LDJ1

8. Limiting values

Table 5. Limiting values

Symbol	Parameter	Notes	Value	Unit
V_{CE}	Collector-emitter voltage, $T_j \geq 25$ °C		650	V
I_C	DC collector current, limited by $T_{j(max)}$ $T_C = 25$ °C $T_C = 100$ °C		 43 22	A
$I_{C(puls)}$	Pulsed collector current, t_p limited by $T_{j(max)}$		150	A
-	Turn off safe operating area $V_{CE} \leq 600$ V, $T_j \leq 150$ °C, $t_p = 1$ µs		150	A
I_F	Diode forward current, limited by $T_{j(max)}$ $T_C = 25$ °C $T_C = 100$ °C		 25 12	A
I_{Fpuls}	Diode pulsed current, t_p limited by $T_{j(max)}$		40	A
V_{GE}	Gate-emitter voltage		±20	V
P_{tot}	Power dissipation $T_C = 25$ °C Power dissipation $T_C = 100$ °C		78 31	W
T_{stg}	Storage temperature		-55 to 150	°C
T_j	Operating junction temperature		-55 to 150	°C
-	Peak soldering temperture		260	°C
M	Mounting Torque with washer		0.55	Nm

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
$R_{th(j-c)}$	IGBT thermal resistance from junction to case			-	1.6	-	K/W
$R_{th(j-c)}$	Diode thermal resistance from junction to case			-	3.6	-	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient			-	40	-	K/W

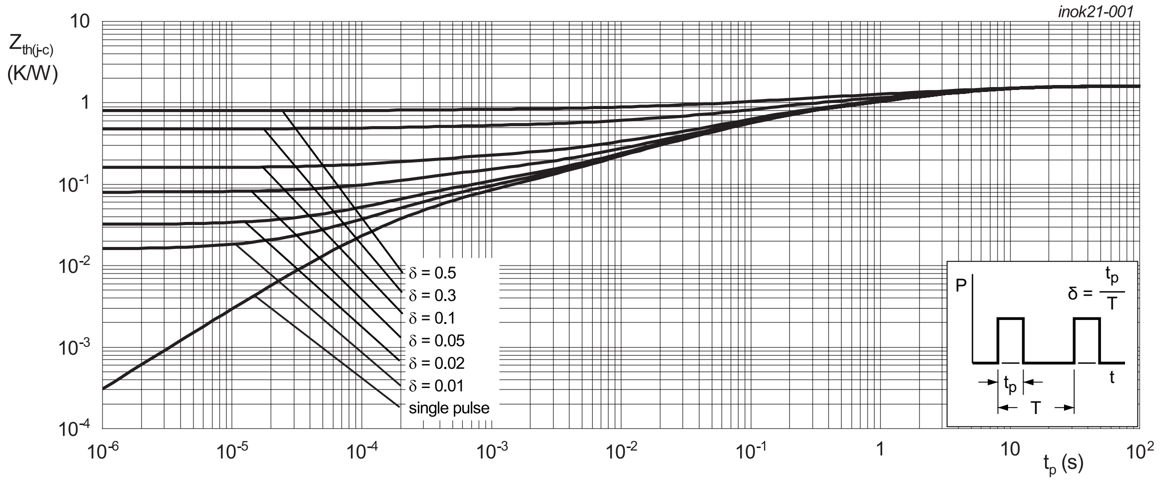


Fig. 1. Transient thermal impedance from junction to case as a function of pulse duration; IGBT

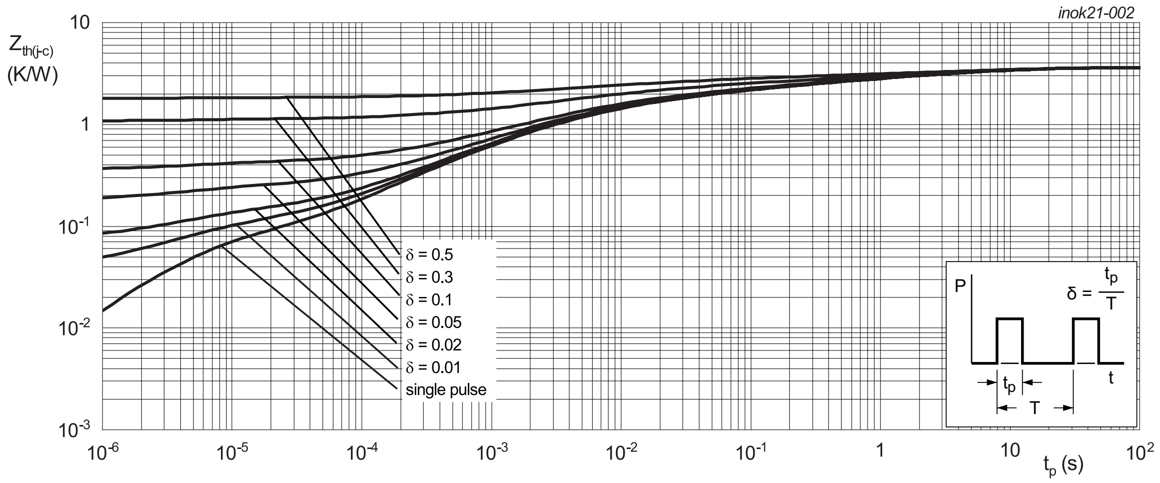


Fig. 2. Transient thermal impedance from junction to case as a function of pulse duration; Diode

10. Characteristics

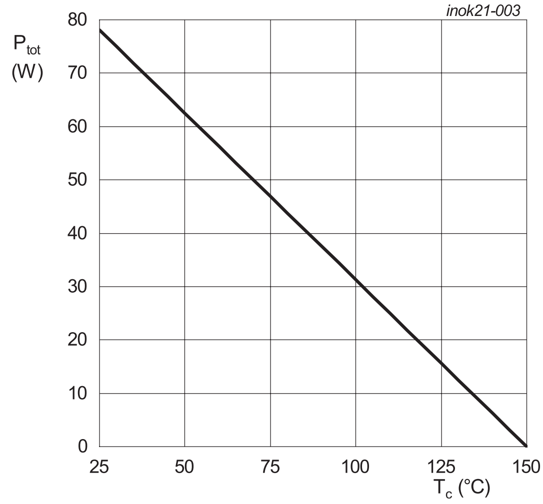
Table 7. Characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
Static characteristics							
BV_{CES}	Collector-emitter breakdown voltage	V_{GE} = 0 V; I_C = 50 uA		650	-	-	V
$V_{CE(sat)}$	Collector-emitter saturation voltage	V_{GE} = 15 V; I_C = 50 A; T_j = 25 °C		-	1.25	1.55	V
		V_{GE} = 15 V; I_C = 50 A; T_j = 150 °C		-	1.5	-	V
V_F	Diode forward voltage	V_{GE} = 0 V; I_F = 10 A; T_j = 25 °C		-	1.18	-	V
		V_{GE} = 0 V; I_F = 10 A; T_j = 150 °C		-	1.00	-	V
$V_{GE(th)}$	Gate-emitter threhold voltage	I_C = 500 uA; V_{CE} = V_{GE}		4	5	6	V
I_{CES}	Zero gate voltage collector current	V_{CE} = 650 V; V_{GE} = 0 V; T_j = 25 °C		-	-	100	uA
		V_{CE} = 650 V; V_{GE} = 0 V; T_j = 150 °C		-	-	1	mA
g_{fs}	Transconductance	V_{CE} = 20 V; I_C = 50 A		-	60	-	S
Dynamic characteristics							
C_{ies}	Input capacitance	V_{CE} = 30 V; V_{GE} = 0V; f = 1 MHz; T_j = 25 °C		-	5571	-	pF
C_{oes}	Output capacitance			-	92	-	pF
C_{res}	Reverse transfer capacitance			-	65	-	pF
Q_G	Gate charge	V_{CC} = 520 V; I_C = 50 A; V_{GE} = 15 V; T_j = 25 °C		-	237	-	nC

11. Switching Characteristics

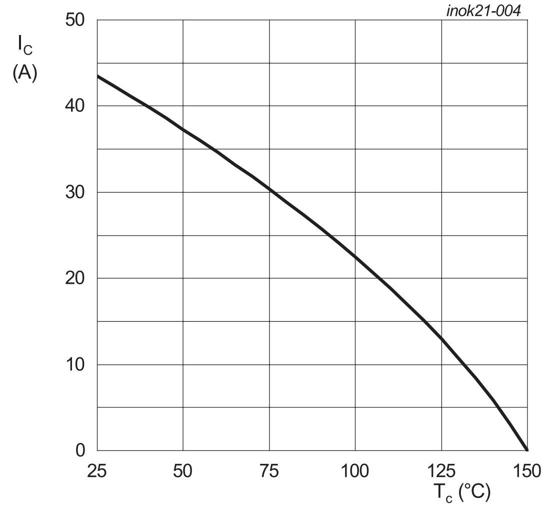
Table 8. Switching Characteristics, Inductive Load

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
IGBT characteristics							
$t_{d(on)}$	Turn-on delay time	T_j = 25 °C; V_{CC} = 400 V; I_C = 50 A; V_{GE} = 15V / 0V; R_G = 10 ohm		-	58	-	nS
t_r	Rise time			-	52	-	nS
$t_{d(off)}$	Turn-off delay time			-	336	-	nS
t_f	Fall time			-	74	-	nS
E_{on}	Turn-on energy			-	1.69	-	mJ
E_{off}	Turn-off energy			-	1.24	-	mJ
E_{ts}	Total switching energy			-	2.93	-	mJ
$t_{d(on)}$	Turn-on delay time	T_j = 150 °C; V_{CC} = 400 V; I_C = 50 A; V_{GE} = 15V / 0V; R_G = 10 ohm		-	56	-	nS
t_r	Rise time			-	52	-	nS
$t_{d(off)}$	Turn-off delay time			-	372	-	nS
t_f	Fall time			-	112	-	nS
E_{on}	Turn-on energy			-	2.29	-	mJ
E_{off}	Turn-off energy			-	1.69	-	mJ
E_{ts}	Total switching energy			-	3.98	-	mJ
Diode characteristics							
t_{rr}	Reverse recovery time	T_j = 25 °C; V_R = 400 V; I_F = 10 A; dI_F/dt = 500A/us		-	65	-	nS
Q_r	Reverse recovery charge			-	585	-	nC
I_{RM}	Reverse recovery peak current			-	16	-	A
t_{rr}	Reverse recovery time	T_j = 150 °C; V_R = 400 V; I_F = 10 A; dI_F/dt = 500A/us		-	100	-	nS
Q_r	Reverse recovery charge			-	1240	-	nC
I_{RM}	Reverse recovery peak current			-	22	-	A



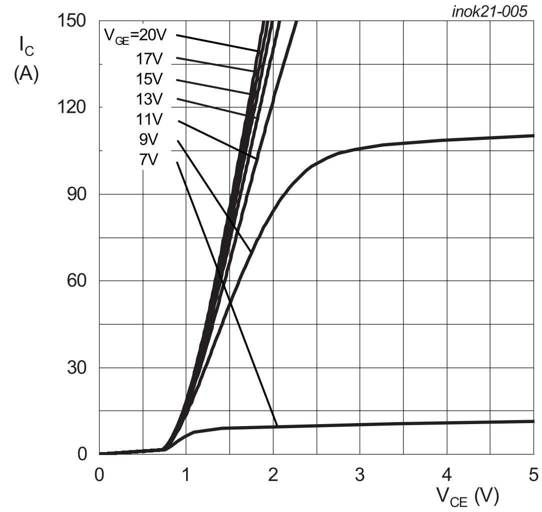
T_j ≤ 150 °C

Fig. 3. Power dissipation as a function of case temperature



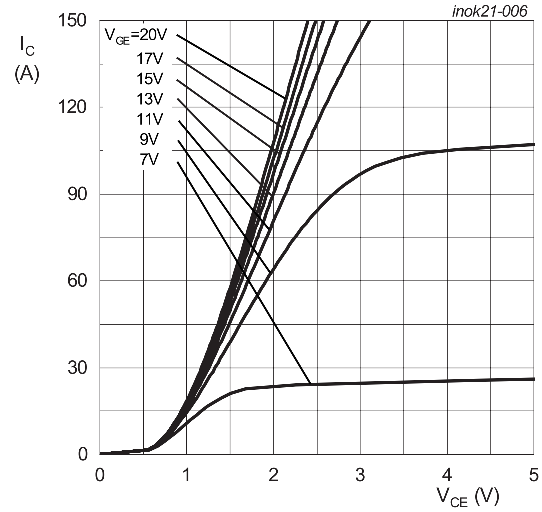
V_{GE} ≥ 15 V; T_j ≤ 150 °C

Fig. 4. Collector current as a function of case temperature



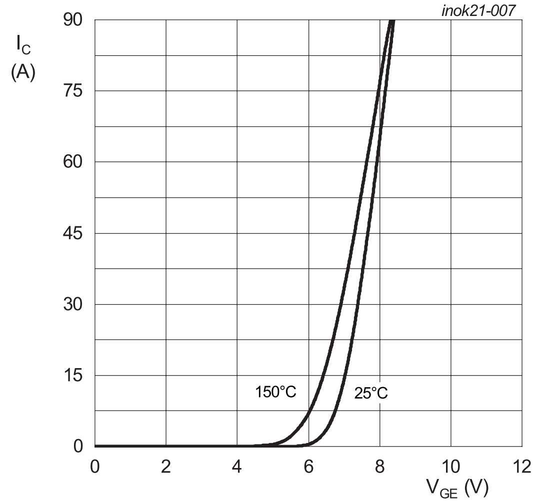
T_j = 25 °C

Fig. 5. Typical output characteristic



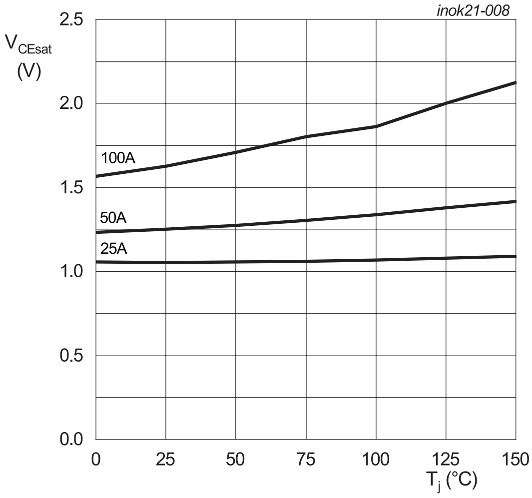
T_j = 150 °C

Fig. 6. Typical output characteristic



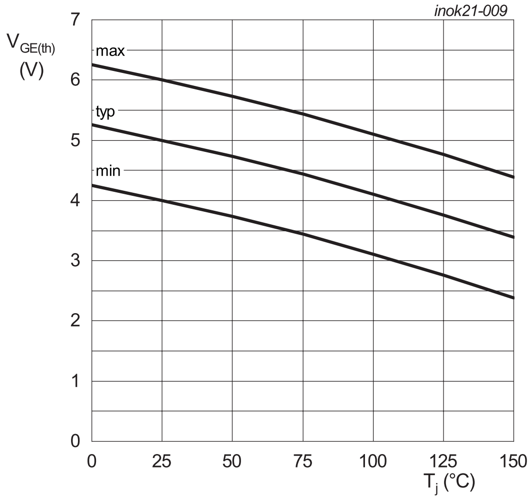
V_{CE} = 20 V

Fig. 7. Typical transfer characteristic



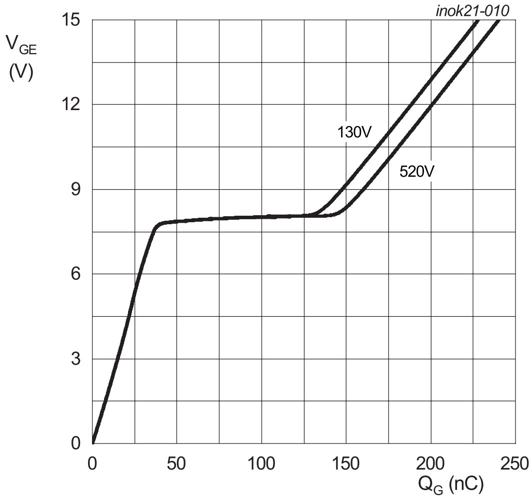
V_{GE} = 15 V

Fig. 8. Typical collector-emitter saturation voltage as a function of junction temperature



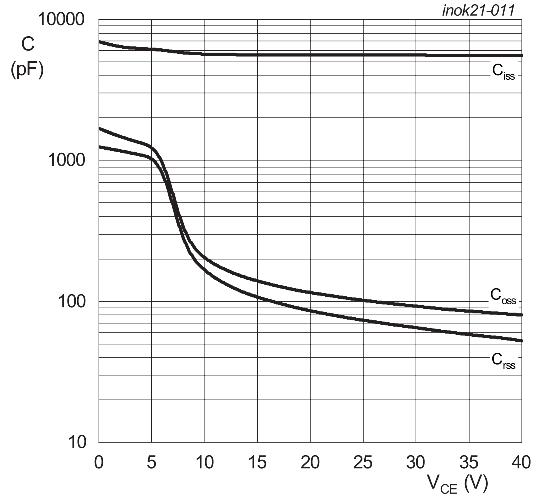
I_C = 500 uA

Fig. 9. Gate-emitter threshold voltage as a function of junction temperature



I_C = 50 A

Fig. 10. Typical gate charge



V_{GE} = 0 V; f = 1 MHz

Fig. 11. Typical capacitance as a function of collector-emitter voltage

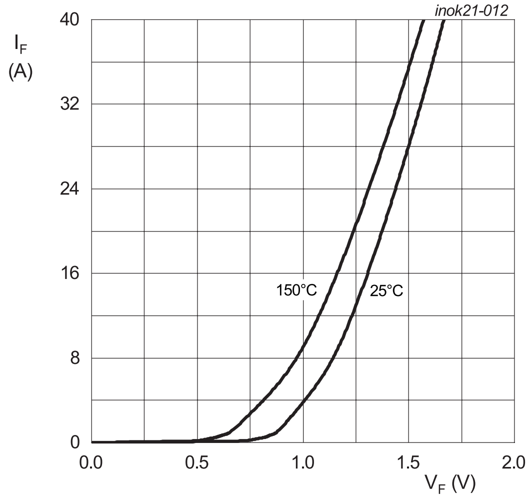
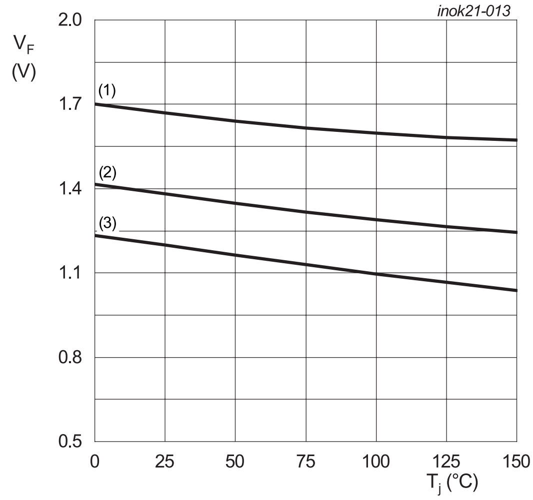
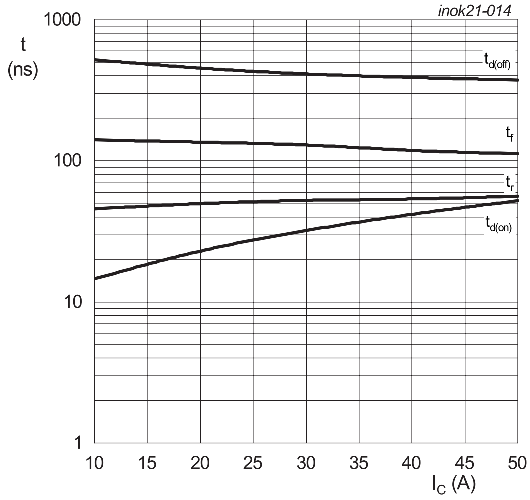


Fig. 12. Typical diode forward current as a function of forward voltage



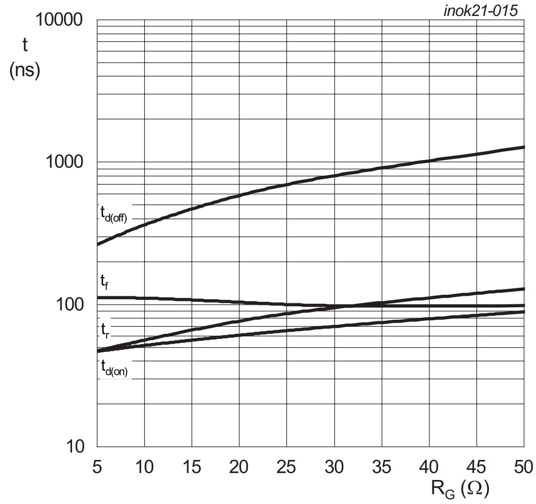
(1) I_F = 40 A
(2) I_F = 20 A
(3) I_F = 10 A

Fig. 13. Typical diode forward voltage as a function of junction temperature



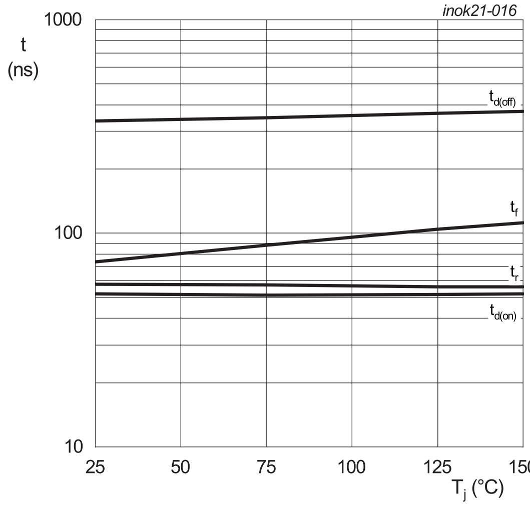
R_g = 10 Ω; V_{GE} = 15 V / 0 V; T_j = 150 °C;
V_{CE} = 400 V; inductive load

Fig. 14. Typical switching times as a function of collector current



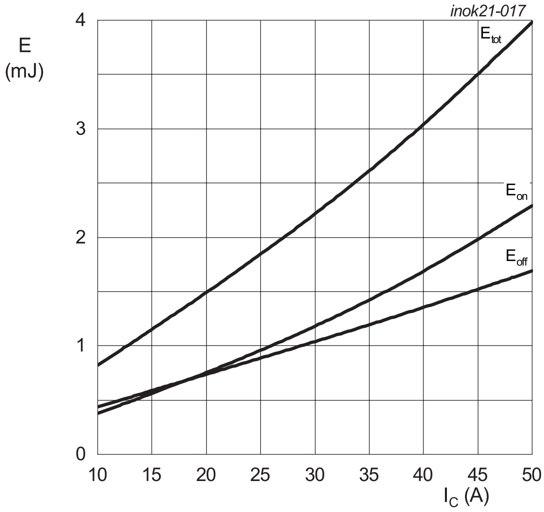
I_C = 50 A; V_{GE} = 15 V / 0 V; T_j = 150 °C;
V_{CE} = 400 V; inductive load

Fig. 15. Typical switching times as a function of gate resistance



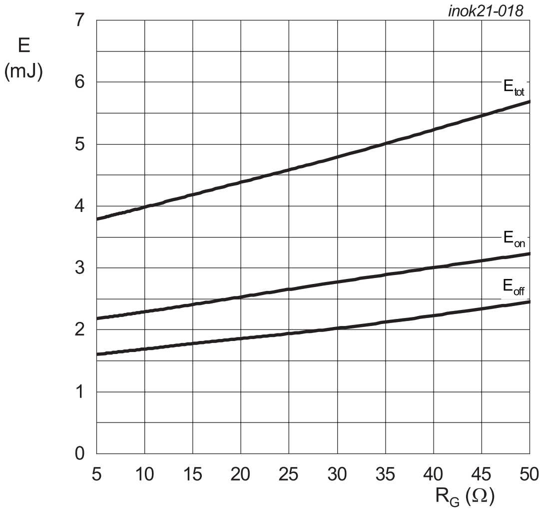
I_C = 50 A; V_{GE} = 15 V / 0 V; R_g = 10 Ω;
V_{CE} = 400 V; inductive load

Fig. 16. Typical switching times as a function of junction temperature



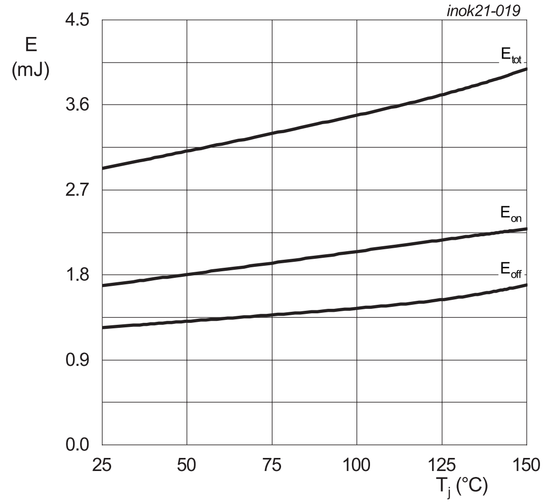
R_g = 10 Ω; V_{GE} = 15 V / 0 V; T_j = 150 °C;
V_{CE} = 400 V; inductive load

Fig. 17. Typical switching energy losses as a function of collector current



I_C = 50 A; V_{GE} = 15 V / 0 V; T_j = 150 °C;
V_{CE} = 400 V; inductive load

Fig. 18. Typical switching energy losses as a function of gate resistance



I_C = 50 A; V_{CE} = 15 V / 0 V; R_g = 10 Ω;
V_{CE} = 400 V; inductive load

Fig. 19. Typical switching energy losses as a function of junction temperature

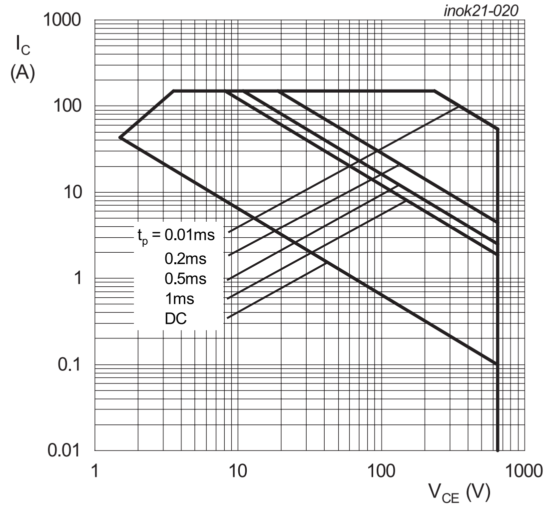


Fig. 20. Forward bias safe operating area

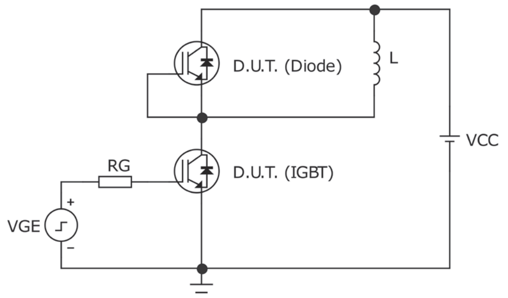


Fig. 21. Test circuit for inductive load switching

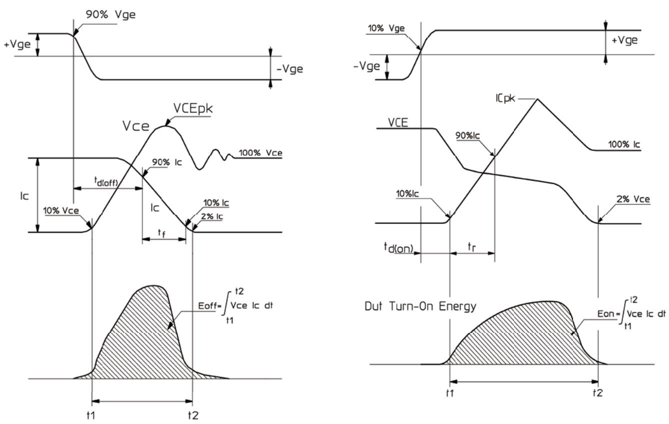
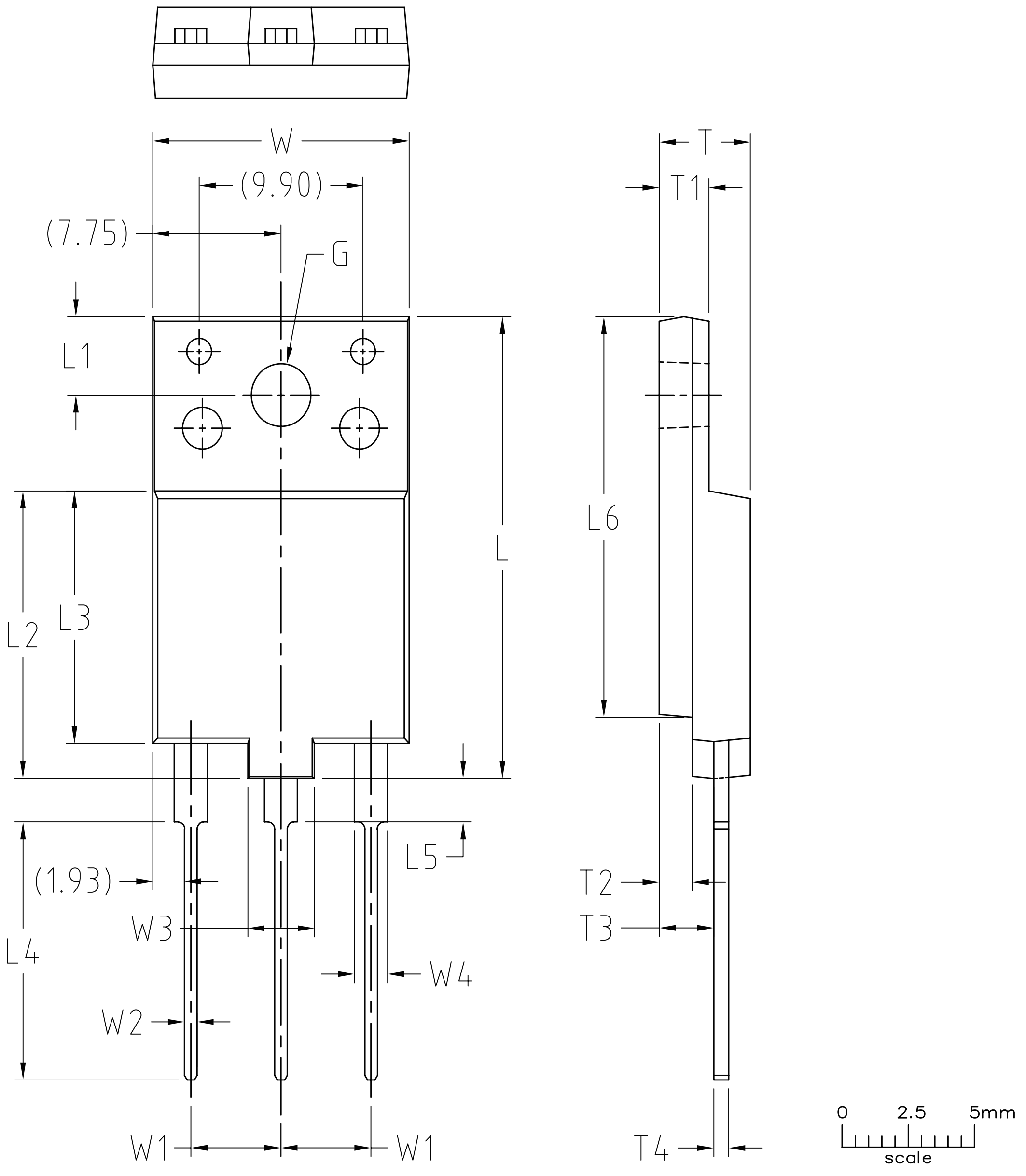


Fig. 22. Definition of switching times and losses

12. Package outline

Plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 3-lead TO-3P 'full pack' TO3PF



Remark : (X) the dimension X in blackets is for reference

UNIT	W	W1	W2	W3	W4	L	L1	L2	L3	L4	L5	L6	T	T1	T2	T3	T4	G(ø)
mm	15.7	5.75	0.95	4.20	2.20	26.7	4.6	16.7	14.7	15.0	2.7	23.2	5.7	3.2	2.2	3.5	1.1	3.8
	15.3	5.15	0.65	3.80	1.80	26.3	4.4	16.3	14.3	14.6	2.3	22.8	5.3	2.8	1.8	3.1	0.8	3.4

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
		TO-3PF				

13. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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14. Contents

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Date of release: 18 May 2023