

USB 2.0 High-Speed and Audio/USB/UART Switch with Negative Swing Capacity

Features

- Low Ron audio/USB analog switch
- Low USB Con: 7 pF
- Negative signal swing capable
- Low audio distortion
- USB switch -3 dB bandwidth: 1100 MHz
- High crosstalk and off-isolation
- Voltage supply operation: 2.7 to 5.5 V
- Three green packages: DQFN-10, QFN-10, MSOP-10

Applications

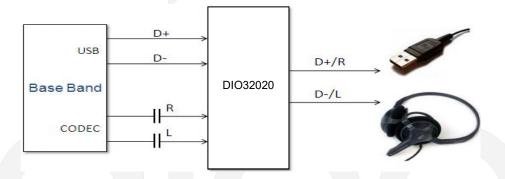
- Cell-phones/PDA
- MP3/MP4/PMP
- Portable instrumentation
- Battery powered communications
- Computer peripherals

Descriptions

The DIO32020 is dual SPDT (Single Pole / Double Throw) switch which combines low distortion audio/USB/UART and USB 2.0 high-speed data signal switching in the same low voltage device. This architecture is designed to allow negative signal passing as low as 3 V below ground. When a voltage is detected on V_{BUS}, the DIO32020 will immediately switch to USB mode. Due to ultra-low capacitance design, the audio channels (pin 3 and pin 4 input/outputs) are able to not only switch audio signal but also high speed USB signal or low voltage UART signal as well.

The DIO32020 provides packages with Green or RoHS tiny 10L packages and operates over a temperature range of -40°C to 85°C.

Block Diagram



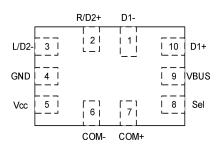
Ordering Information

Part Number	Top Marking	RoHS	TA	Package		
DIO32020LP10	YW0A	Green	-40 to 85°C	DQFN-10	Tape & Reel, 3000	
DIO32020QN10	YW0A	Green	-40 to 85°C	QFN-10	Tape &Reel, 3000	
DIO32020MP10	D32020	Green	-40 to 85°C	MSOP-10	Tape &Reel, 3000	

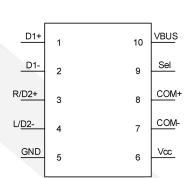


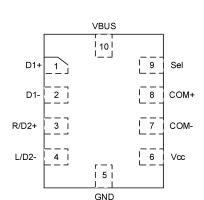
Pin Assignment

DQFN-10



MSOP-10





QFN-10

Figure 1. Top View

Pin Descriptions

Pin Name	Direction	Description
D1+, D1-	1/0	Differential USB Data Input 1
R/D2+, L/D2-	1/0	Audio R/L or differential USB data input 2
V _{BUS}		Switch selection
COM+/COM-	I/O	Data/audio common port
Sel		Control input
Vcc/ GND	Р	Power

Truth Table

Sel	V _{BUS}	L/D2-, R/D2+	D1+, D1-
Low	Low	ON	OFF
Low	High	OFF	ON
High	X	ON	OFF



Absolute Maximum Ratings

Stresses beyond those listed under the Absolute Maximum Rating table may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Symbol	Parameter		Rating	Unit		
Vcc	Supply Voltage		-0.5 to 6.5	V		
V _{BUS}	V _{BUS} Control Input Voltage		-0.5 to 6.5	V		
V _{IN}	A _{SEL} Control Input Voltage		-0.5 to 6.5	V		
V	USB Path Analog Signal Voltage		-3.5 to V _{CC}	V		
Vsw	V _{SW} Audio Path Analog Signal Voltage		-3.5 to 6.5	V		
	Storage Temperature		-65 to 150	°C		
1	A _{SEL} Control Input Current		5			
I _{IN}	V _{BUS} Control Input Current		5	μA		
I _{sw_con}	Analog Signal Continuous Current		Analog Signal Continuous Current		±100	mA
I _{SW_PK}	Analog Signal Peak Current		Analog Signal Peak Current ±500		±500	mA
ESD	HPM JEDEC: JESD22 A444	I/O to GND	5	kV		
ESD	HBM, JEDEC: JESD22-A114	Others	8	, KV		

Recommend Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. DIOO does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	Parameter	Rating	Unit
Vcc	Supply voltage	2.7 to 5.5	V
V	V _{BUS} control input voltage	0 to 5.5	V
V _{IN}	A _{SEL} control input voltage	0 to V _{CC}	V
V _{sw}	USB to COM analog signal voltage	nalog signal voltage -3 to V _{CC}	
Vsw	Audio to COM analog signal voltage	-3 to Vcc	V
T _A	Operating temperature range -40 to 85		°C



DC Electrical Characteristics

All typical value are at T_A = 25°C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Control in	out (T _A = 25°C, V _{CC} = 3.3 V)				
V _{IH}	A _{SEL} control input high voltage	V _{CC} = 2.7 - 4.2 V	1.3			V
V _{IL}	A _{SEL} control input low voltage	V _{CC} = 2.7 - 4.2 V			0.5	V
I _{IN}	A _{SEL} control input leakage current	0 ≤ V _{SW} ≤ V _{CC}		1	2	μA
V _{IH}	V _{BUS} control input high voltage	V _{CC} = 2.7 - 4.2 V	1.3			V
V _{IL}	V _{BUS} control input low voltage	V _{CC} = 2.7 - 4.2 V			0.5	V
I _{IN}	V _{BUS} control input leakage current	0 ≤ V _{SW} ≤ V _{CC}		1	2	μA
R _{VBUS_PD}	V _{BUS_CTRL} pull down resistance	I _{ON} = 10 mA		4000		kΩ
Supply cu	rrent and leakage (T _A = 25	°C, V _{CC} = 3.3 V)				
I _{OZ-USB}	Off leakage current of port DN/DP	DN/DP = 0 V to 4.2 V, R, L, COM+, COM- float	-1		1	uA
I _{OZ-AUDIO}	Off leakage current of port R/L	R/L = -3 V to 3 V, DN, DP, COM+, COM- float	-1		1	uA
I _{ON-USB}	On leakage current of USB switch	COM+/COM- = 0 V to 4.2 V, R, L, DN, DP float	-1		1	uA
I _{ON-AUDIO}	On leakage current of AUDIO switch	COM+/COM- = -3 V to 3 V, R, L, DN, DP float	-1		1	uA
I_{off}	Power off leakage	V _{CC} = 0 V	-1		1	uA
Icc	Quiescent supply			18	35	μA
USB switc	hes (D+, D-) (T _A =25°C, V _{CC}	=3.3V)				
Ron	On resistance	I _{ON} = 10 mA, V _{SW} = 0 V to V _{CC}		3.8		Ω
R _{FLATE}	On resistance flatness	I _{ON} = 10 mA, V _{SW} = 0 V to V _{CC}		15		mΩ
ΔR _{ON}	On resistance matching	I_{ON} = 10 mA, V_{SW} = 0 V to V_{CC}		100		mΩ
AUDIO sw	itches (R, L) (T _A =25°C, V _{CC}	=3.3V)				_
Ron	On resistance	I _{ON} =100 mA, V _{SW} = -3 to 3 V		0.8		Ω
R _{FLATE}	On resistance flatness	I _{ON} = 100 mA, V _{SW} =-3 to 3V		2		mΩ
ΔR_{ON}	On resistance matching	I _{ON} = 100 mA, V _{SW} = -3 to 3 V		50		mΩ



Electrical Characteristics (Continued)

All typical value are at T_A = 25°C unless otherwise specified.

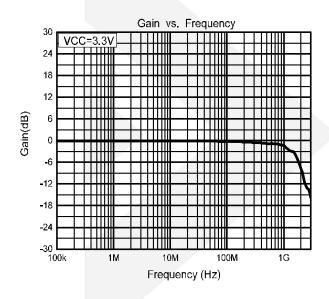
Symbol	Parameter	Conditions	Min	Тур	Max	Un
C param	eter (T _A = 25°C, V _{CC} = 3.3 V,	, R_L = 50 Ω , C_L = 5 pF, unless otherwise specifie	ed)			
	USB turn-on time	DP = DN = 1.5 V, R_L = 50 Ω, C_L = 35 pF		32		μs
t _{ON}	Audio turn-on time	L = R = 1.5 V, R_L = 50 Ω, C_L = 35 pF		32		μs
ı	USB turn-off time	DP = DN = 1.5 V, R_L = 50 Ω, C_L = 35 pF		100		ns
t _{OFF}	Audio turn-off time	L = R = 1.5 V, R_L = 50 Ω, C_L = 35 pF		150		ns
<u>.</u>	To the Make Time	Audio off to USB on		30		μ
Тввм	Break Before Make Time	USB off to Audio on		30		μ
BW	-3dB bandwidth USB channel	$R_L = 50 \Omega$, $C_L = 5 pF$		1100		MF
2:55	USB OFF-isolation	$V_{SW} = 1 V_{RMS}, R_L = 50 \Omega, f = 240 MHz$		-23		dl
OIRR	Audio OFF-isolation	$V_{SW} = 1 V_{RMS}, R_L = 50 \Omega, f = 1 \text{ kHz}$		-110		dl
	USB crosstalk	R _L = 50 Ω, f = 240 MHz		-45		dl
X _{TALK}	Audio crosstalk	R _L = 50 Ω, f = 1 kHz		-80		dl
		$R_L = 600 \Omega$, $f = 1 \text{ kHz}$, $V_{SW} = 2 V_{RMS}$, with A-weighted		-110		
THD+N	Total harmonic distortion + noise	$R_L = 32 \Omega$, $f = 1 \text{ kHz}$, $V_{SW} = 1 V_{RMS}$, with A-weighted		-100		dl
		R_L = 16 Ω , f = 1 kHz, V_{SW} = 0.5 V_{RMS} , with A-weighted		-100		
PSRR	Power supply rejection ratio	f = 10 kHz, R _{COM} = 50 Ω		-75		d
; apacitan	ce (T _A = 25°C, V _{CC} = 3.3 V, F	$R_L = 50 \Omega$, $C_L = 5 pF$, $f = 1 MHz$, $A_{SEL} = 0 V$, unles	ess otherwise	specified)		<u> </u>
Cin	A _{SEL} control input capacitance	V _{CC} = 0 V		2		р
	· ·	f = 1 MHz, V _{BUS} = VDD,		7		р



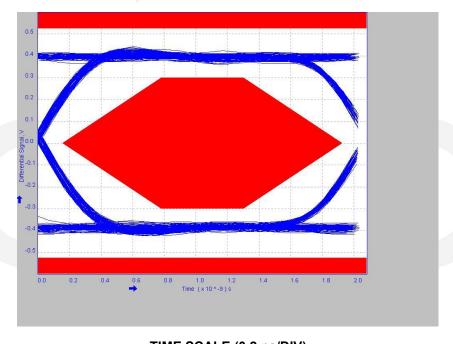
C _{ON}	Audio ON capacitance	V _{BUS} = LOW, V _{SEL} = LOW	7	pF
C _{OFF}	USB OFF capacitance	V _{BUS} = VDD, V _{SEL} = LOW	3	pF
C _{OFF}	Audio OFF capacitance	V _{BUS} = LOW, V _{SEL} = LOW	4	pF

Typical Performance Characteristics

 $T_A = 25$ °C, $V_{CC} = 3.3$ V, unless other otherwise specified.

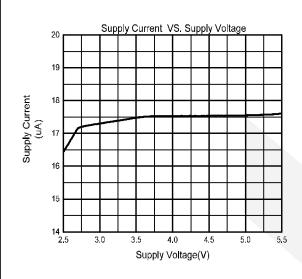


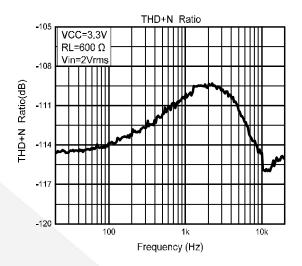
DIO32020 USB 2.0 high speed (480 Mbps) eye pattern



TIME SCALE (0.2 ns/DIV)
Figure 2 Eye Pattern: 480 Mbps with USB switch in signal path









Applications Design Guide

The DIO32020 is used in applications where slim and thin smartphone designs are expected. By sharing the USB connector between USB 2.0 data lines and audio headphone outputs, the designers can eliminate the use of bulky headphone jacks. Meanwhile, using the mini-USB connectors as audio outputs allows the end users to reduce the cost to buy too many types of cell phone accessories.

The DIO32020 unique architectures allow the part to have constant R_{ON} , R_{ON} (flatness) and THD performance independent of V_{CC} supply value. So in some applications such as mobile cell phone designs, if the designers want to achieve the lowest standby power consumption when the battery is turned OFF, it is highly recommended that DIO32020 be powered by 2.8 V, with no need of being powered by a battery (4.3 V) directly. This will help designers to be freed from the complex logic designs to ensure that the part will get into sleep mode.

The control pins are 1.8 V control logic compatible, so the parts can be controlled by the baseband processor GPIO directly without worrying about level shifting issues. Regarding high-speed signal integrity, the DIO32020 is recommended to be placed as close as possible to the USB controller outputs to reduce the signal reflection under high-speed mode (480 Mbps). In the meanwhile, the V_{CC} pin of the DIO32020 is required to have decoupling capacitors to reduce the supply ripples.

Below is the DIO32020 USB 2.0 high-speed (480 Mbps) eye diagram compliance test under near-end mode (most challenging mode).

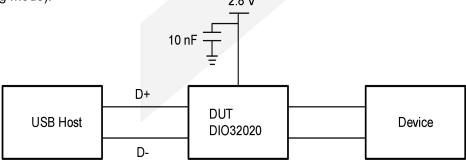
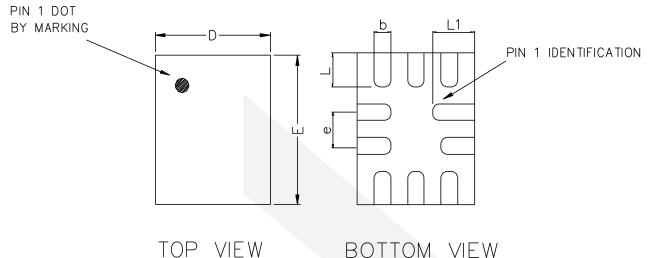


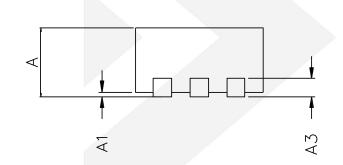
Figure 3. USB 2.0 high speed eye diagram test circuit

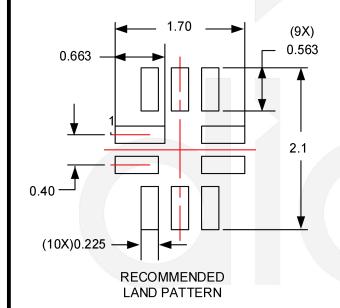
The DIO32020 features not only 100 dB total harmonic distortion (THD), but also superior off isolation capability. When the DIO32020 is powered under audio mode, audio channel can pass at least -3 V negative audio signal with up to 100 dB THD performance. Under USB mode or device being powered OFF (V_{CC} = 0), the negative audio signal on audio ports can be isolated as well to allow flexible design needs.







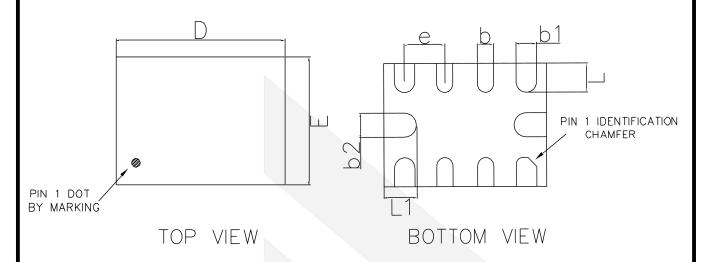


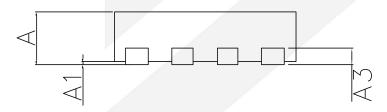


Common Dimensions (mm)						
PKG	UT	UT: ULTRA THIN				
REF	Min	Nom	Max			
Α	0.5	0.55	0.6			
A1	0		0.05			
A3	0.15 REF					
D	1.35	1.4	1.45			
E	1.75	1.8	1.85			
b	0.15	0.2	0.25			
L /	0.3	0.4	0.5			
L1	0.4	0.5	0.6			
е	0.4 BSC					







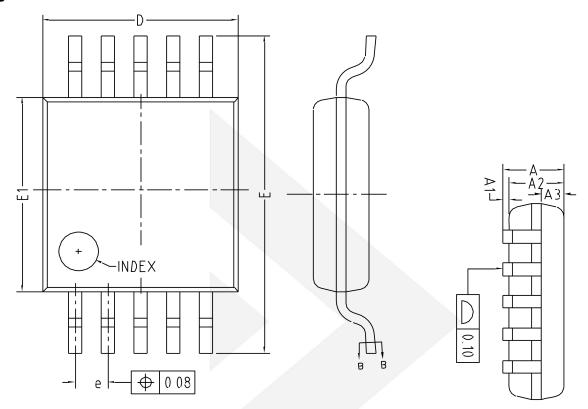


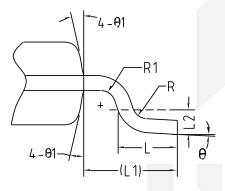
SIDE VIEW

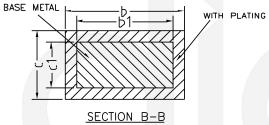
Cor	Common Dimensions (mm)					
PKG	UT:	UT: ULTRA THIN				
REF	Min	Nom	Max			
Α	0.50	0.55	0.60			
A1	0.00		0.05			
A3	0.15 REF					
D	1.95	2.00	2.05			
E	1.45	1.50	1.55			
b	0.15	0.20	0.25			
b1	0.20	0.25	0.30			
b2	0.25	0.30	0.35			
L	0.30	0.35	0.40			
L1	0.35	0.40	0.45			
е	0.50 BSC					



Physical Dimensions: MSOP-10







Common Dimensions (mm)					
Symbol	Min	Тур	Max		
Α	-	-	1.10		
A1	0	-	0.15		
A2	0.75	0.85	0.95		
A3	0.25	0.35	0.39		
b	0.18	-	0.27		
b1	0.17	0.20	0.23		
С	0.15	-	0.20		
c1	0.14	0.15	0.16		
D	2.90	3.00	3.10		
Е	4.70	4.90	5.10		
E1	2.90	3.00	3.10		
е	0.40	0.50	0.60		
L	0.40	0.60	0.80		
L1		0.95 REF			
L2		0.25 BSC			
R	0.07	-	-		
R1	0.07	-	-		
θ	0°	-	8°		
θ1	9°	12°	15°		



CONTACT US

Dioo is a professional design and sales corporation for high-quality and performance analog semiconductors. The company focuses on industry markets, such as, cell phone, handheld products, laptop, and medical equipment and so on. Dioo's product families include analog signal processing and amplifying, LED drivers and charger IC. Go to http://www.dioo.com for a complete list of Dioo product families.

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