

RJK0454DPB

40V, 40A, 4.9m Ω max. Silicon N Channel Power MOS FET Power Switching

R07DS1049EJ0300 (Previous: REJ03G1877-0200)

Rev.3.00

Apr 09, 2013

Features

- High speed switching
- Low drive current
- Low on-resistance $R_{DS(on)} = 3.9 \text{ m}\Omega \text{ typ. (at } V_{GS} = 10 \text{ V)}$
- Pb-free
- Halogen-free
- High density mounting

Outline

RENESAS Package code: PTZZ0005DA-A (Package name: LFPAK)

5
D
1, 2, 3 Source
4 Gate
5 Drain

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	40	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	40	Α
Drain peak current	I _{D(pulse)} Note1	160	Α
Body-drain diode reverse drain current	I _{DR}	40	Α
Avalanche current	I _{AP} Note 2	40	A
Avalanche energy	E _{AS} Note 2	13	mJ
Channel dissipation	Pch Note3	55	W
Channel to Case Thermal Resistance	θch-C	2.27	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at L=10uH, Tch = 25°C, Rg \geq 50 Ω
- 3. $Tc = 25^{\circ}C$

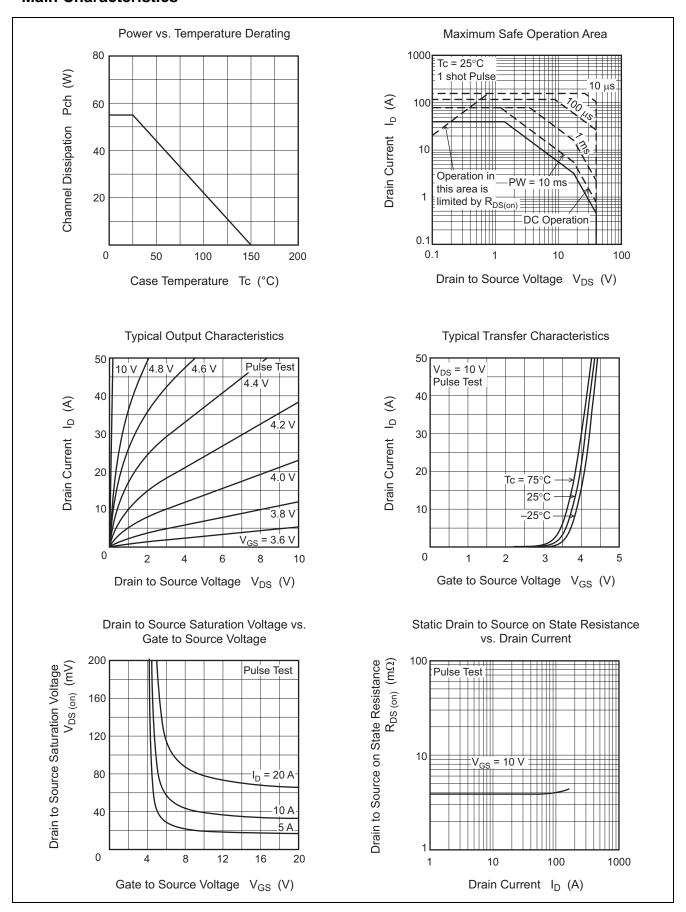
Electrical Characteristics

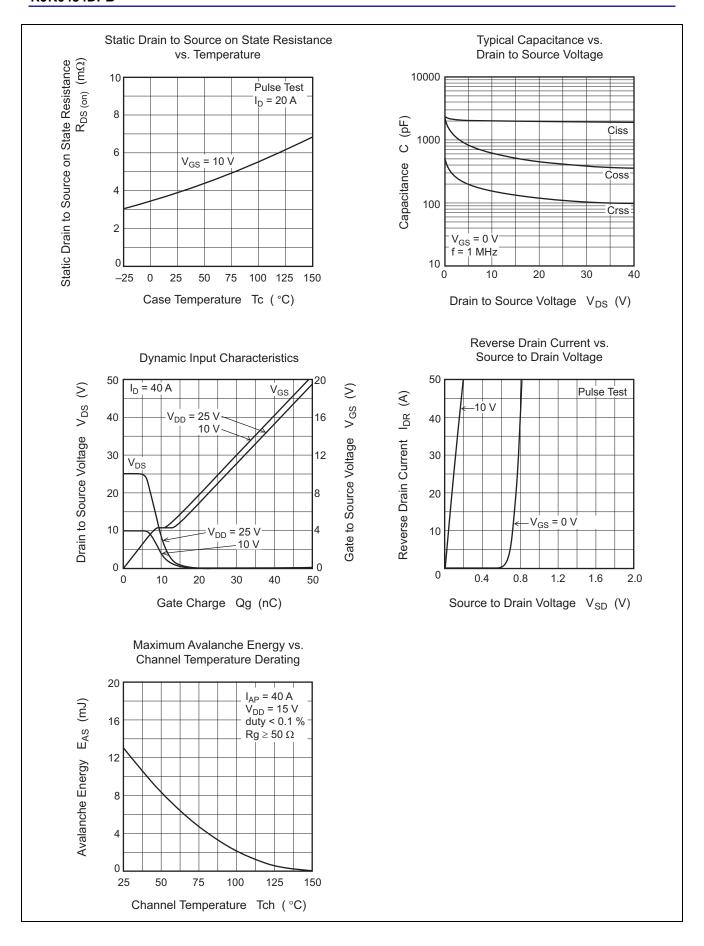
 $(Ta = 25^{\circ}C)$

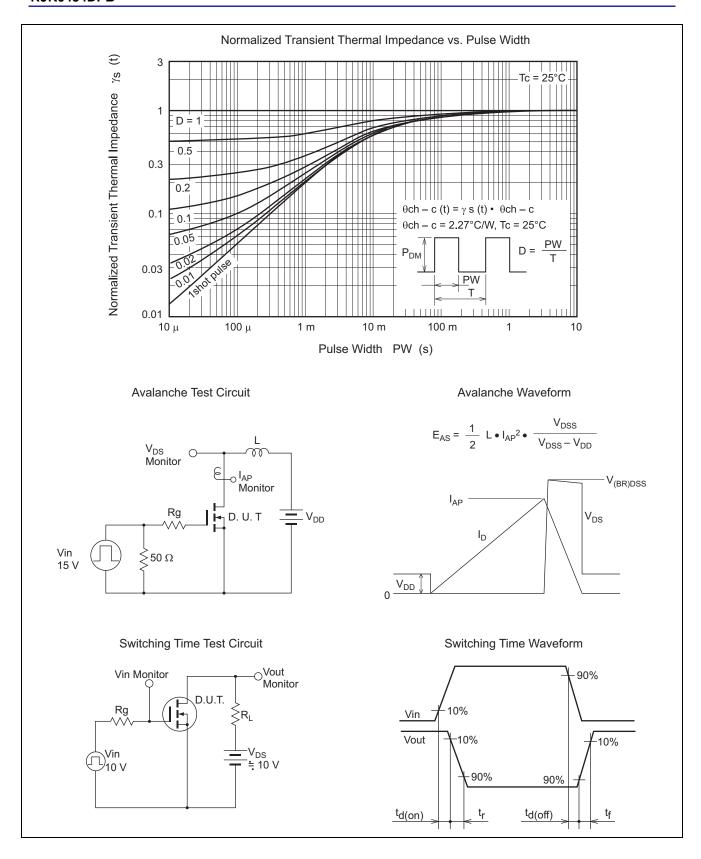
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	40	_		V	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V}$
Gate to source cutoff voltage	$V_{GS(off)}$	2.0	_	4.0	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R _{DS(on)}	_	3.9	4.9	mΩ	$I_D = 20 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}	_	40	_	S	$I_D = 20 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	2000	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V},$
Output capacitance	Coss	_	620		pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	150		pF	
Gate Resistance	Rg	_	0.5		Ω	
Total gate charge	Qg	_	25	_	nC	$V_{DD} = 10 \text{ V}, V_{GS} = 10 \text{ V},$
Gate to source charge	Qgs	_	9.0	_	nC	I _D = 40 A
Gate to drain charge	Qgd	_	3.0	_	nC	
Turn-on delay time	t _{d(on)}	_	10	_	ns	$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A},$
Rise time	t _r	_	5.2	_	ns	$V_{DD}\cong 10~V,~R_L=0.5~\Omega,$ $Rg=4.7~\Omega$
Turn-off delay time	t _{d(off)}	_	30	_	ns	
Fall time	t _f	_	6.5	_	ns	
Body-drain diode forward voltage	V_{DF}	_	0.8	1.1	V	$I_F = 40 \text{ A}, V_{GS} = 0 \text{ V}^{\text{Note4}}$
Body-drain diode reverse recovery time	t _{rr}	_	37	_	ns	$I_F = 40 \text{ A}, V_{GS} = 0 \text{ V}$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

Notes: 4. Pulse test

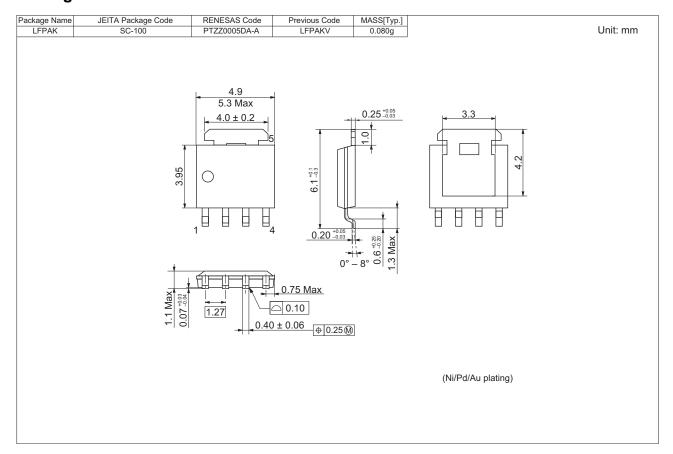
Main Characteristics







Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJK0454DPB-00-J5	2500 pcs	Taping

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