

CR05AS-8

Thyristor

Low Power Use

R07DS0135EJ0500

(Previous: REJ03G0348-0400)

Rev.5.00

Sep 15, 2010

Features

• $I_{T (AV)} : 0.5 A$ V_{DRM} : 400 V I_{GT} : 100 μA

- Non-Insulated Type
- Planar Passivation Type
- Surface Mounted type

Outline

RENESAS Package code: PLZZ0004CA-A (Package name: UPAK)



RENESAS Package code: PLZZ0004CB-A (Package name: SOT-89)





- 1. Cathode
- 2. Anode
- 3. Gate 4. Anode

Applications

Solid state relay, strobe flasher, igniter, and hybrid IC

Maximum Ratings

Parameter	Cumbal	Voltage class	Unit	
Parameter	Symbol	8 (Mark CD)	Unit	
Repetitive peak reverse voltage	V_{RRM}	400	V	
Non-repetitive peak reverse voltage	V_{RSM}	500	V	
DC reverse voltage	V _{R (DC)}	320	V	
Repetitive peak off-state voltage ^{Note1}	V_{DRM}	400	V	
DC off-state voltage ^{Note1}	V _{D (DC)}	320	V	

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T (RMS)}	0.79	Α	
Average on-state current	I _{T (AV)}	0.5	А	Commercial frequency, sine half wave 180° conduction, Ta = 57°C ^{Note2}
Surge on-state current	I _{TSM}	10	А	60Hz sine half wave 1 full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	0.4	A ² s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	P_{GM}	0.1	W	
Average gate power dissipation	P _{G (AV)}	0.01	W	
Peak gate forward voltage	V_{FGM}	6	V	
Peak gate reverse voltage	V_{RGM}	6	V	
Peak gate forward current	I _{FGM}	0.1	Α	
Junction temperature	Tj	- 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	_	50	mg	Typical value

Notes: 1. With gate to cathode resistance $R_{GK} = 1 \text{ k}\Omega$.

Electrical Characteristics

Parameter	Symbol	Rated value			I Imit	Test conditions
		Min.	Тур.	Max.	Unit	rest conditions
Repetitive peak reverse current	I _{RRM}	_	_	0.1	mA	Tj = 125°C, V _{RRM} applied
Repetitive peak off-state current	I _{DRM}	_	_	0.1	mA	$Tj = 125$ °C, V_{DRM} applied, $R_{GK} = 1 \text{ k}\Omega$
On-state voltage	V _{TM}	_	_	1.9	>	Ta = 25°C, I _{TM} = 1.5 A, instantaneous value
Gate trigger voltage	V _{GT}	_	_	0.8	٧	$Tj = 25$ °C, $V_D = 6$ V, $I_T = 0.1$ A ^{Note4}
Gate non-trigger voltage	V_{GD}	0.2	_	_	V	$Tj = 125$ °C, $V_D = 1/2 V_{DRM}$, $R_{GK} = 1 k\Omega$
Gate trigger current	I _{GT}	20	_	100 ^{Note3}	μΑ	$Tj = 25^{\circ}C, V_D = 6 V,$ $I_T = 0.1 A^{Note4}$
Holding current	I _H	_	_	3	mA	$Tj = 25$ °C, $V_D = 12$ V, $R_{GK} = 1$ k Ω
Thermal resistance	R _{th (j-a)}	_	_	70	°C/W	Junction to ambient ^{Note2}

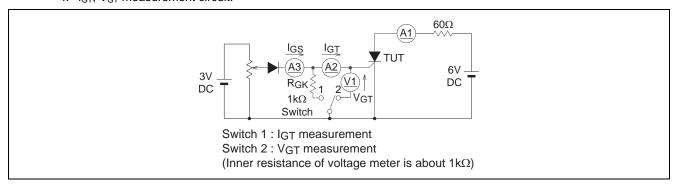
Notes: 2. Soldering with ceramic plate (25 mm \times 25 mm \times t0.7 mm).

3. If special values of I_{GT} are required, choose item E from those listed in the table below if possible.

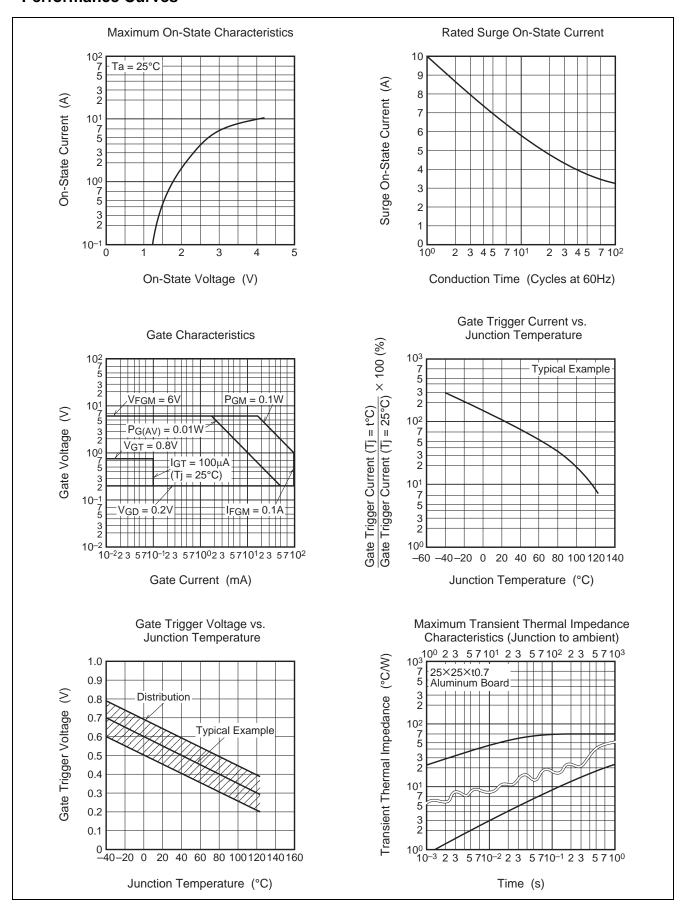
Item	В	E	
I _{GT} (μA)	20 to 50	20 to 100	

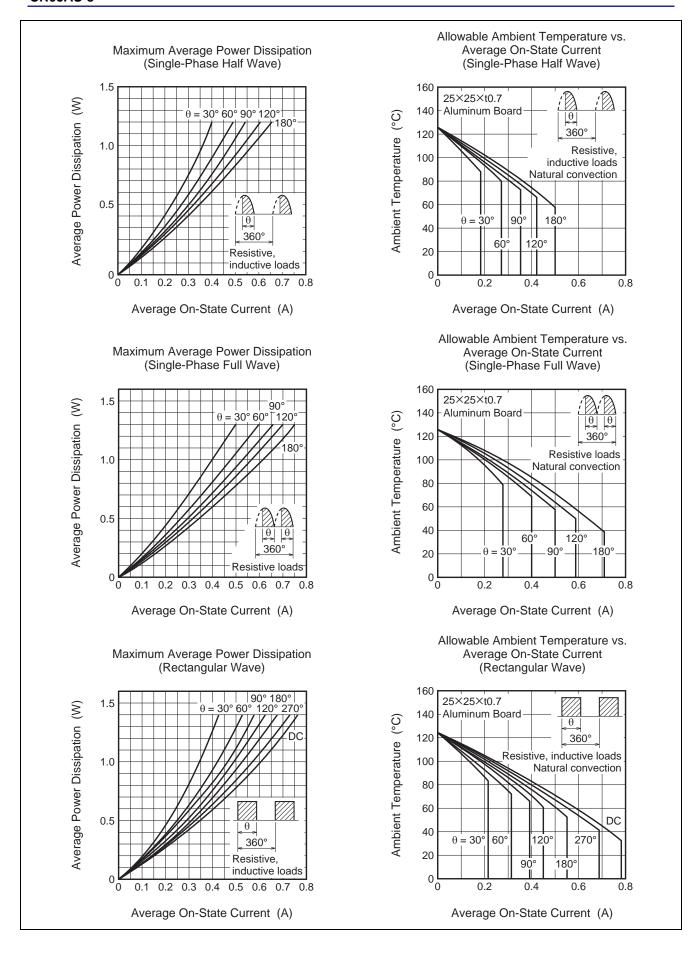
The above values do not include the current flowing through the 1 k Ω resistance between the gate and cathode.

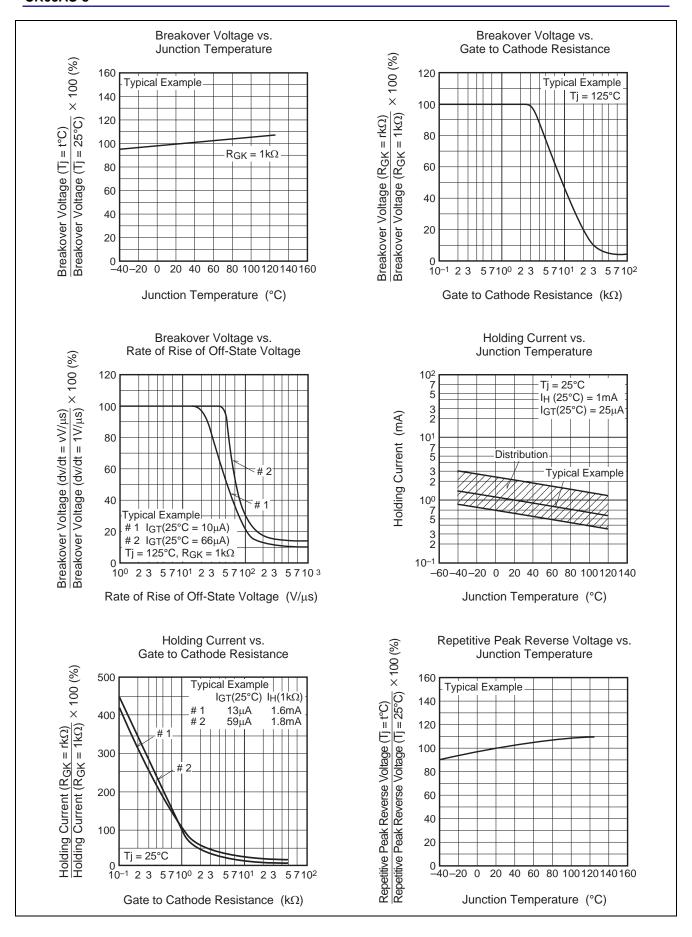
4. I_{GT} , V_{GT} measurement circuit.

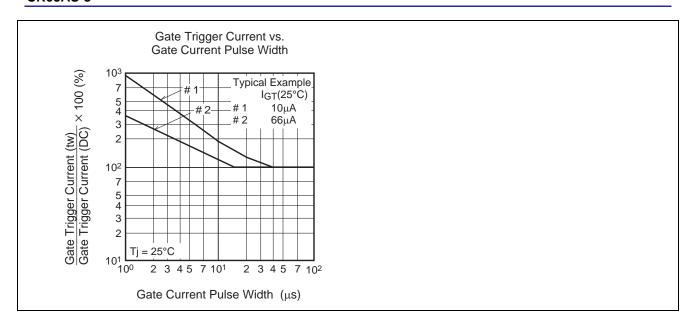


Performance Curves

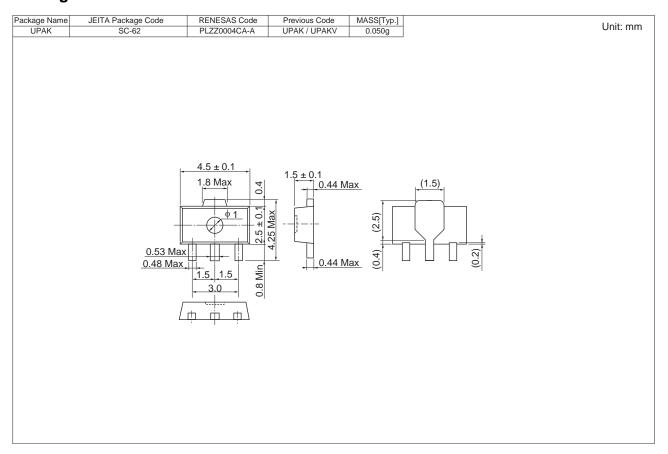


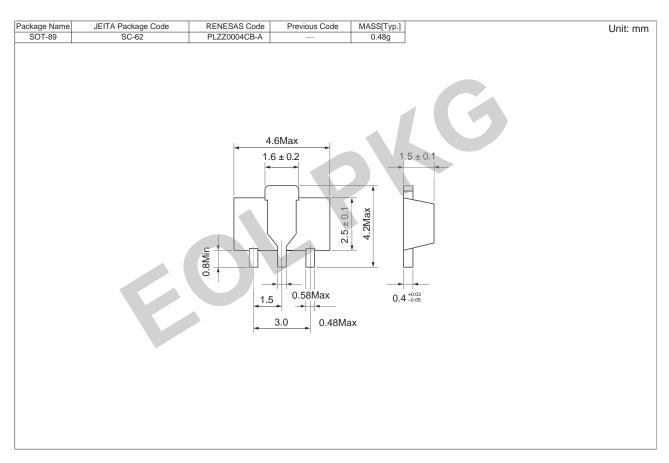






Package Dimensions





Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Surface-mounted type	Taping	4000	Type name – ET +Direction (1 or 2) + 4	CR05AS-8-ET14

Note: Please confirm the specification about the shipping in detail.

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Renesas Electronics America Inc. 2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A. Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited 1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K
Tel: +44-1628-585-100, Fax: +44-1628-585-900

Renesas Electronics Europe GmbH Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China
Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China Tel: +86-21-5877-1818, Fax: +86-21-6887-7858 / -7898

เพลายอย อเชียงเทเชง **ทยายู nong Limited** Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong Tel: +852-2866-9318, Fax: +852-2866-9022/9044

Renesas Electronics Taiwan Co., Ltd.

7F, No. 363 Fu Shing North Road Taipei, Taiwar Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd.

1 harbourFront Avenue, #06-10, keppel Bay Tower, Singapore 098632
Tel: +65-6273-0200, Fax: +65-6278-8019
Renesas Electronics Malaysia Sdn.Bhd. เพราะสอน เมราะเพราะเพราะสามารถ งสท.**ษกด.** Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

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