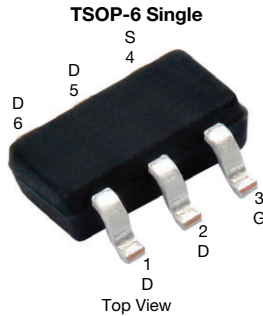


P-Channel 30 V (D-S) MOSFET



FEATURES

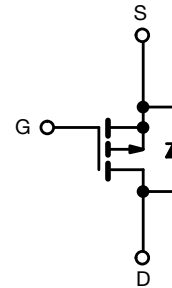
- TrenchFET® Gen IV p-channel power MOSFET
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

- Load switch



P-Channel MOSFET

Marking code: BT

PRODUCT SUMMARY	
V _{DS} (V)	-30
R _{DS(on)} max. (Ω) at V _{GS} = -10 V	0.0312
R _{DS(on)} max. (Ω) at V _{GS} = -4.5 V	0.0513
Q _g typ. (nC)	4.5
I _D (A) ^{a, d}	-8
Configuration	Single

ORDERING INFORMATION	
Package	TSOP-6
Lead (Pb)-free and halogen-free	Si3483DDV-T1-GE3

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)				
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-source voltage	V _{DS}	-30	V	
Gate-source voltage	V _{GS}	-20 / +16		
Continuous drain current (T _J = 150 °C)	I _D	T _C = 25 °C	A	
		T _C = 70 °C		
		T _A = 25 °C		
		T _A = 70 °C		
Pulsed drain current (t = 100 μs)	I _{DM}	-30		
Continuous source-drain diode current	I _S	T _C = 25 °C	-2.5	
		T _A = 25 °C		
Maximum power dissipation	P _D	T _C = 25 °C	W	
		T _C = 70 °C		
		T _A = 25 °C		
		T _A = 70 °C		
Operating junction and storage temperature range	T _J , T _{stg}	-55 to +150	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient ^b	R _{thJA}	52	62.5	°C/W
Maximum junction-to-case (drain)	R _{thJC}	34	41	

Notes

- Package limited
- Surface mounted on 1" x 1" FR4 board
- t = 5 s
- Maximum under steady state conditions is 110 °C/W



SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-source breakdown voltage	V_{DS}	$V_{GS} = 0\text{ V}$, $I_D = -250\text{ }\mu\text{A}$	-30	-	-	V
V_{DS} temperature coefficient	$\Delta V_{DS}/T_J$	$I_D = -250\text{ }\mu\text{A}$	-	-17.6	-	mV/ $^\circ\text{C}$
$V_{GS(th)}$ temperature coefficient	$\Delta V_{GS(th)}/T_J$		-	5	-	
Gate-source threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = -250\text{ }\mu\text{A}$	-1	-	-2.2	V
Gate-source leakage	I_{GSS}	$V_{DS} = 0\text{ V}$, $V_{GS} = -20\text{ V} / +16\text{ V}$	-	-	± 100	nA
Zero gate voltage drain current	I_{DSS}	$V_{DS} = -30\text{ V}$, $V_{GS} = 0\text{ V}$	-	-	-1	μA
		$V_{DS} = -30\text{ V}$, $V_{GS} = 0\text{ V}$, $T_J = 70\text{ }^\circ\text{C}$	-	-	-10	
On-state drain current ^a	$I_{D(on)}$	$V_{DS} \geq -10\text{ V}$, $V_{GS} = -10\text{ V}$	-5	-	-	A
Drain-source on-state resistance ^a	$R_{DS(on)}$	$V_{GS} = -10\text{ V}$, $I_D = -5\text{ A}$	-	0.0260	0.0312	Ω
		$V_{GS} = -4.5\text{ V}$, $I_D = -3\text{ A}$	-	0.0410	0.0513	
Forward transconductance ^a	g_{fs}	$V_{DS} = -10\text{ V}$, $I_D = -5\text{ A}$	-	30	-	S
Dynamic ^b						
Input capacitance	C_{ISS}	$V_{DS} = -15\text{ V}$, $V_{GS} = 0\text{ V}$, $f = 1\text{ MHz}$	-	580	-	pF
Output capacitance	C_{OSS}		-	245	-	
Reverse transfer capacitance	C_{RSS}		-	35	-	
Total gate charge	Q_g	$V_{DS} = -15\text{ V}$, $V_{GS} = -10\text{ V}$, $I_D = -6.4\text{ A}$	-	9.5	14.5	nC
		$V_{DS} = -15\text{ V}$, $V_{GS} = -4.5\text{ V}$, $I_D = -6.4\text{ A}$	-	4.5	9	
Gate-source charge	Q_{gs}	$V_{DS} = -15\text{ V}$, $V_{GS} = -4.5\text{ V}$, $I_D = -6.4\text{ A}$	-	2.6	-	nC
Gate-drain charge	Q_{gd}		-	1.3	-	
Gate resistance	R_g		$f = 1\text{ MHz}$	3.4	20	
Turn-on delay time	$t_{d(on)}$	$V_{DD} = -15\text{ V}$, $R_L = 2.9\text{ }\Omega$, $I_D \cong -5.2\text{ A}$, $V_{GEN} = -10\text{ V}$, $R_g = 1\text{ }\Omega$	-	15	30	ns
Rise time	t_r		-	33	66	
Turn-off delay time	$t_{d(off)}$		-	30	60	
Fall time	t_f		-	40	60	
Turn-on delay time	$t_{d(on)}$	$V_{DD} = -15\text{ V}$, $R_L = 2.9\text{ }\Omega$, $I_D \cong -5.2\text{ A}$, $V_{GEN} = -4.5\text{ V}$, $R_g = 1\text{ }\Omega$	-	26	52	ns
Rise time	t_r		-	140	280	
Turn-off delay time	$t_{d(off)}$		-	26	52	
Fall time	t_f		-	42	84	
Drain-Source Body Diode Characteristics						
Continuous source-drain diode current	I_S	$T_C = 25\text{ }^\circ\text{C}$	-	-	-2.5	A
Pulse diode forward current	I_{SM}		-	-	-30	
Body diode voltage	V_{SD}	$I_S = -5.2\text{ A}$, $V_{GS} = 0\text{ V}$	-	-0.8	-1.2	V
Body diode reverse recovery time	t_{rr}	$I_F = -5.2\text{ A}$, $di/dt = 100\text{ A}/\mu\text{s}$, $T_J = 25\text{ }^\circ\text{C}$	-	21	32	ns
Body diode reverse recovery charge	Q_{rr}		-	9	18	nC
Reverse recovery fall time	t_a		-	9	-	ns
Reverse recovery rise time	t_b		-	12	-	

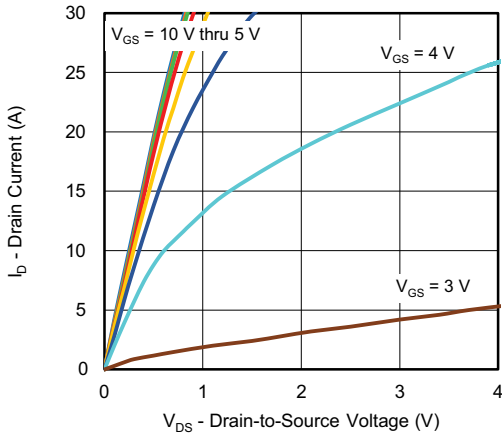
Notes

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$
b. Guaranteed by design, not subject to production testing

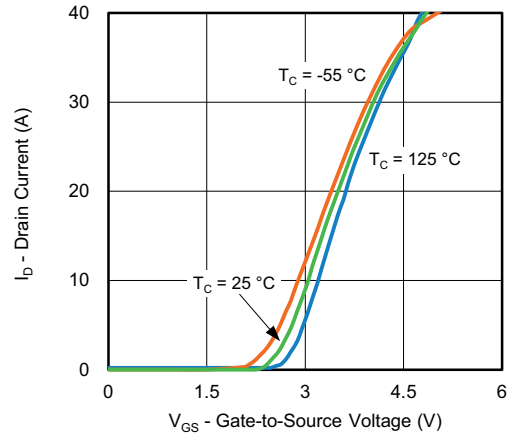
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



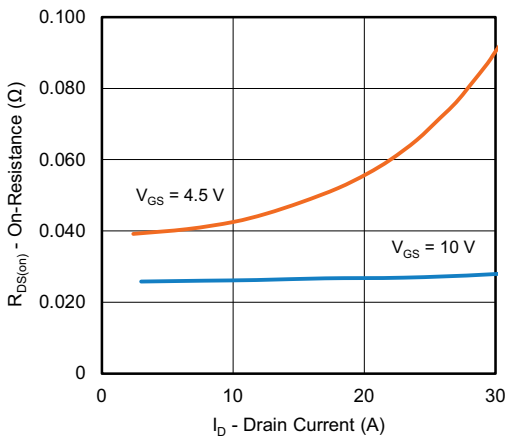
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



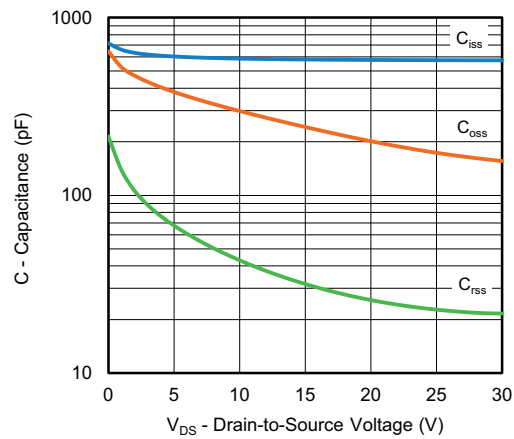
Output Characteristics



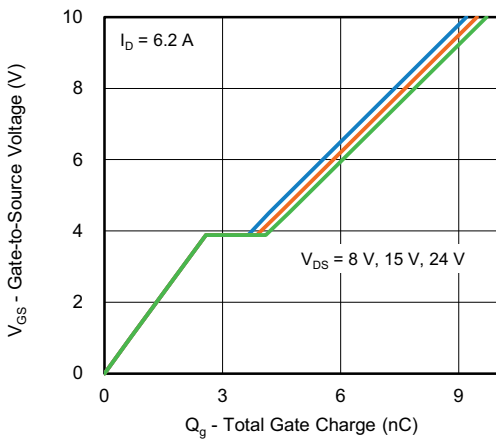
Transfer Characteristics



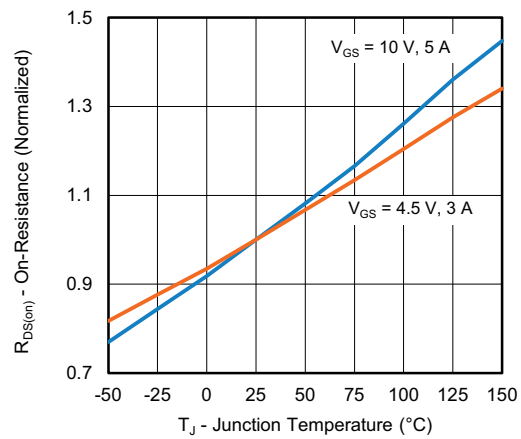
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



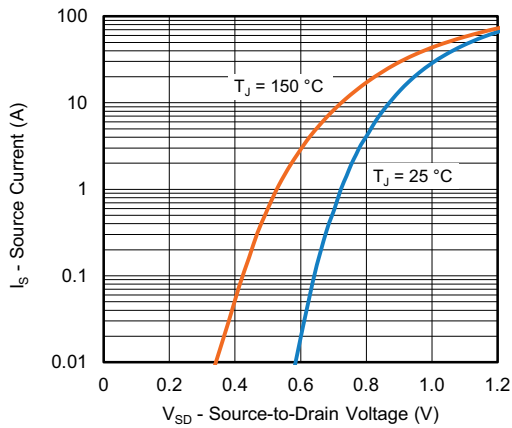
Gate Charge



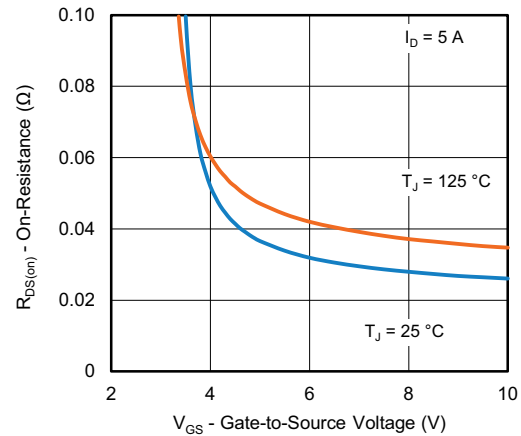
On-Resistance vs. Junction Temperature



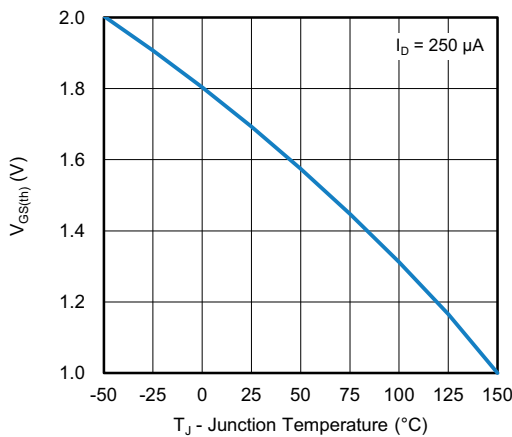
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



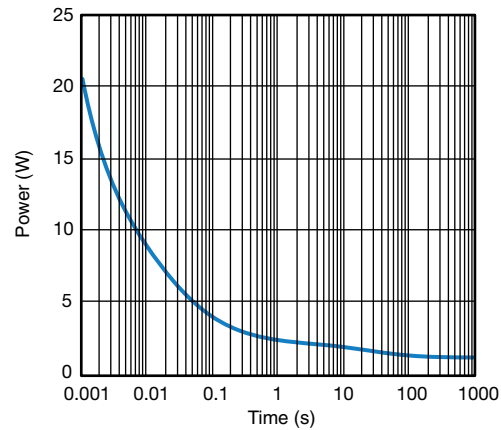
Source-Drain Diode Forward Voltage



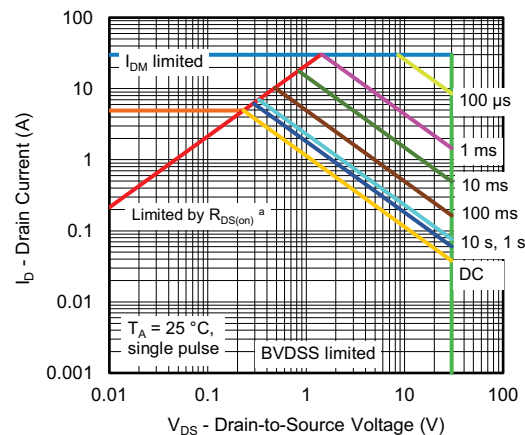
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Single Pulse Power, Junction-to-Ambient



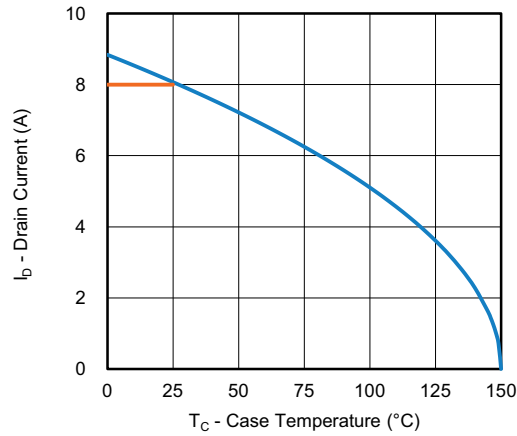
Safe Operating Area, Junction-to-Ambient

Note

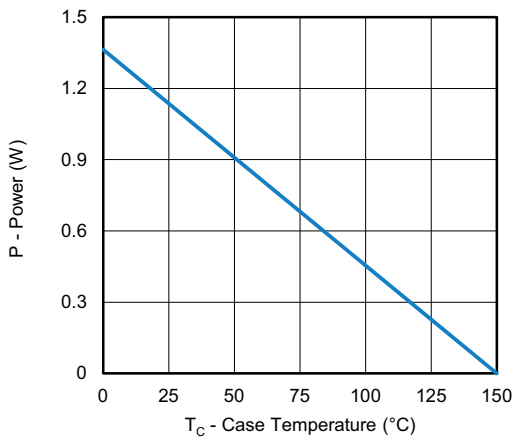
a. $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified



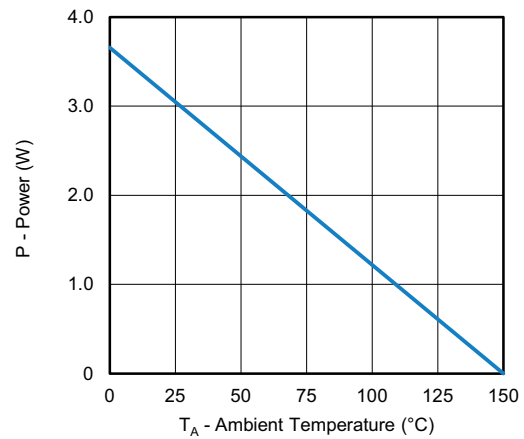
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating ^a



Power, Junction-to-Case



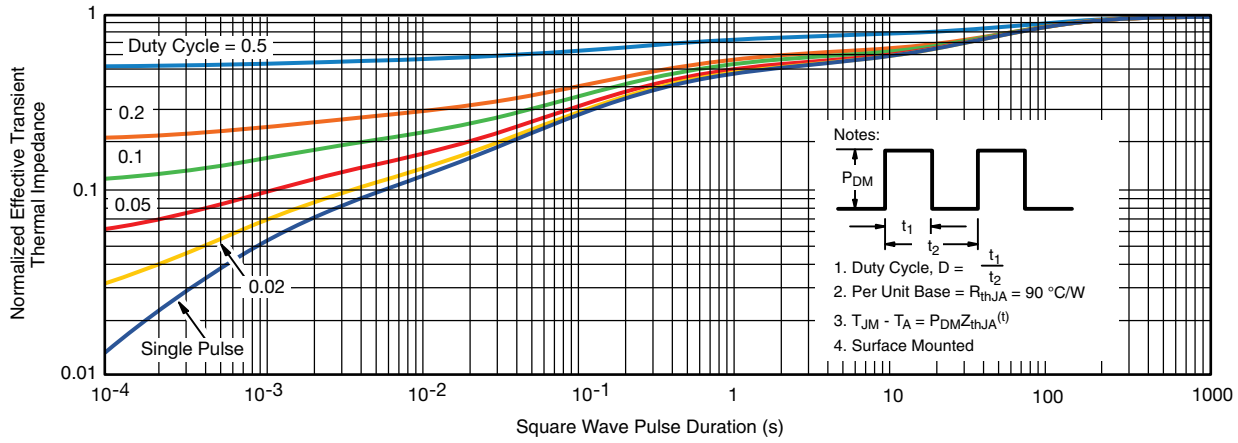
Power, Junction-to-Ambient

Note

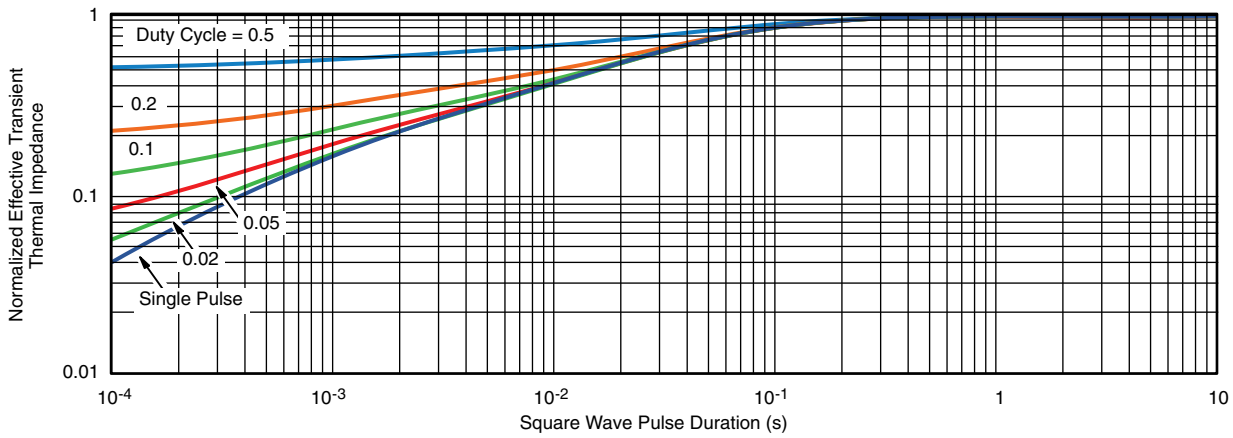
- a. The power dissipation P_D is based on $T_{J \text{ max.}} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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TSOP: 5/6-LEAD

JEDEC Part Number: MO-193C



5-LEAD TSOP



6-LEAD TSOP



Dim	MILLIMETERS			INCHES		
	Min	Nom	Max	Min	Nom	Max
A	0.91	-	1.10	0.036	-	0.043
A₁	0.01	-	0.10	0.0004	-	0.004
A₂	0.90	-	1.00	0.035	0.038	0.039
b	0.30	0.32	0.45	0.012	0.013	0.018
c	0.10	0.15	0.20	0.004	0.006	0.008
D	2.95	3.05	3.10	0.116	0.120	0.122
E	2.70	2.85	2.98	0.106	0.112	0.117
E₁	1.55	1.65	1.70	0.061	0.065	0.067
e	0.95 BSC			0.0374 BSC		
e₁	1.80	1.90	2.00	0.071	0.075	0.079
L	0.32	-	0.50	0.012	-	0.020
L₁	0.60 Ref			0.024 Ref		
L₂	0.25 BSC			0.010 BSC		
R	0.10	-	-	0.004	-	-
θ	0°	4°	8°	0°	4°	8°
θ₁	7° Nom			7° Nom		
ECN: C-06593-Rev. I, 18-Dec-06						
DWG: 5540						

RECOMMENDED MINIMUM PADS FOR TSOP-6



Recommended Minimum Pads
Dimensions in Inches/(mm)

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