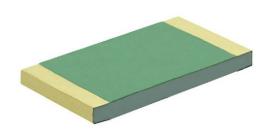


High Precision Wraparound - Power Enhanced Thin Film Chip Resistors



LINKS TO ADDITIONAL RESOURCES



INTRODUCTION

PEP series chip resistors are designed for high power applications, low noise, superior stability, low temperature coefficient of resistance, and low voltage coefficient. The resistive thin film layer can withstand an established temperature as high as 250 °C: hence, the restrictions are mainly due to the robustness of terminations and solder joints.

PEP series is recommended for customers who need to switch to lower size devices, with the same power limits.

FEATURES

- Load life stability: 0.1 % typical (0.35 % max.) at 2000 h / P_n / 70° C
- Very low noise < -35 dB and voltage coefficient < 0.01 ppm/V
- Wide resistance range:
 39 Ω to 900 kΩ depending on size
- Tolerances down to ± 0.05 %
- · Termination: thin film technology
- P_n: up to 1 W for 1206 size, without cooling under PCB required
- Sulfur resistant (per ASTM B809-95 humid vapor test)
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



RoHS COMPLIANT
HALOGEN
FREE
GREEN
(5.2008)

STANDA	STANDARD ELECTRICAL SPECIFICATIONS						
MODEL SIZE RANGE POWER W POWER W VOLTAGE TOLERANCE COE		TEMPERATURE COEFFICIENT ⁽²⁾ ± ppm/°C					
PEP0402	0402	39 to 50K	0.125	0.063	50	0.05, 0.1, 0.5, 1	5, 10, 25, 50
PEP0603	0603	39 to 108K	0.320	0.125	75	0.05, 0.1, 0.5, 1	5, 10, 25, 50
PEP0805	0805	39 to 240K	0.500	0.200	150	0.05, 0.1, 0.5, 1	5, 10, 25, 50
PEP1206	1206	39 to 900K	0.660 (3)	0.330	200	0.05, 0.1, 0.5, 1	5, 10, 25, 50

Notes

- (1) P_n = nominal power: P_d = derated power intended to improve stability
- (2) For ohmic range versus tolerance and TCR, see Best Tolerance and TCR vs. Ohmic Value" table
- (3) $P_n = 1 \text{ W if PEP1206}$ is mounted on <u>alumina board</u>

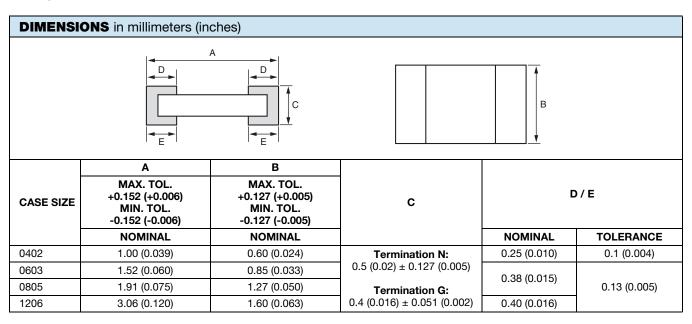
CLIMATIC SPECIFICATIONS			
Operating temperature range -55 °C; +155 °C			

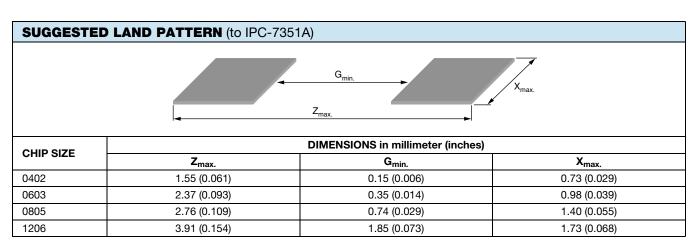
PERFORMANCE VS. HUMID SULFUR VAPOR				
Test conditions	50 °C ± 2 °C, 85 % ± 4 % RH, exposure time 500 h			
Test results	Resistance drift $<$ (0.05 % R + 0.05 Ω), no corrosion products observed			

MECHANICAL SPECIFICATIONS				
Substrate	Substrate Alumina			
Technology	Thin film			
Film	Nickel chromium based alloy with mineral passivation			
Protection	Epoxy + silicone			
Terminations	N type: tin silver over nickel barrier G type: gold over nickel barrier			

ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000

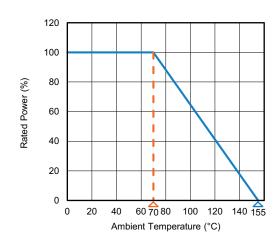






TEMPERATURE COEFFICIENT			
TCR (ppm/°C) CODE (TEMPERATURE RANGE)			
± 5	Z (0 °C; +70 °C)		
± 10	Y (-55 °C; +155 °C)		
± 25	E (-55 °C; +155 °C)		
± 50	H (-55 °C; +155 °C)		

POWER DERATING CURVE







BEST TOLERANCE AND TCR VS. OHMIC VALUE				
STYLE	RANGE (Ω)	TOLERANCE (± %)	TCR CODE	
0402	39 to < 50	0.1, 0.5, 1	Z; Y; E; H	
0402	50 to 50K	0.05, 0.1, 0.5, 1	Z; Y; E; H	
0603	39 to < 50	0.1, 0.5, 1	Z; Y; E; H	
0003	50 to 108K	0.05, 0.1, 0.5, 1	Z; Y; E; H	
0805	39 to < 50	0.1, 0.5, 1	Z; Y; E; H	
0603	250 to 240K	0.05, 0.1, 0.5, 1	Z; Y; E; H	
1206	39 to < 50	0.1, 0.5, 1	Z; Y; E; H	
1200	250 to 900K	0.05, 0.1, 0.5, 1	Z; Y; E; H	

POPULAR OPTIONS

For any option it is recommended to consult Vishay Sfernice for availability first.

Option: Marking

Option to order 0013:

Marking of ohmic value and tolerance:

0805 size: 3 digits marking (according to EIA-96)

1206 size: 4 digits marking (same codification than in the

ordering procedure)

Tolerance indicated by a color dot.

Option to order 0014:

Marking of ohmic value:

0805 size: 3 digits marking (according to EIA-96)

1206 size: 4 digits marking (same codification than in the

ordering procedure)

No standard marking available for smaller sizes.

A price adder will apply to the unit price of the parts for options 0013 and 0014.

PACKAGING

ESD packaging available: waffle-pack, plastic tape and reel (low conductivity), and paper tape and reel.

		NUMBER OF PIE			
SIZE	MOQ	WAFFLE PACK	TAPE AND REEL		TAPE WIDTH
		2" × 2"	MIN.	MAX.	
0402		340	100	5000	8 mm
0603	100	100			
0805	100	100			
1206		140		4000	

PACKAGING RULES

Waffle Pack

Can be filled up to maximum quantity indicated in the table here above, taking into account the minimum order quantity. When quantity ordered exceeds maximum quantity of a single waffle pack, the waffle packs are stacked up on the top of each other and closed by one single cover.

To get "not stacked up" waffle pack in case of ordered quantity > maximum number of pieces per package: Please consult Vishay Sfernice for specific ordering code.

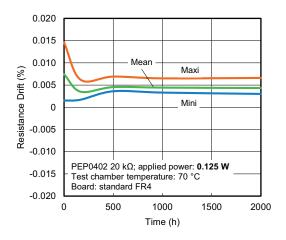
Tape and Reel

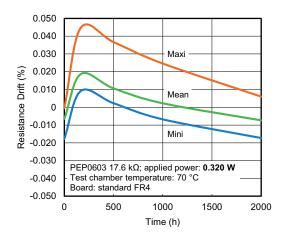
See Global Part Number Information to get the quantity desired by tape.

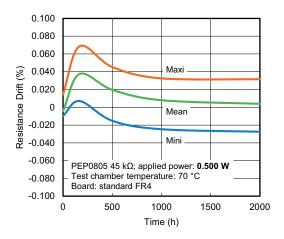
PERFORMANCES				
TESTS	CONDITIONS	MIL OR CECC REQUIREMENTS	TYPICAL VISHAY PERFORMANCES	
Thermal shock	MIL-PRF-55342G MIL-STD-202 F-Method 107 F	± 0.05 %	± 0.02 %	
Short time overload	MIL-PRF-55342G PARA 3.10.4.7.5	± 0.05 %	± 0.01 %	
Resistance to solder heat	MIL-PRF-55342G PARA 3.12, 4.7.7, 4.7.1.2	± 0.05 %	± 0.03 %	
Resistance of terminations (bending test)	CECC	± 0.05 %	± 0.01 %	
Load life	MIL-PRF-55342G 2000 h P _n at 70 °C MIL-STD-202 F-Method 108 A	± 0.5 %	± 0.1 %	

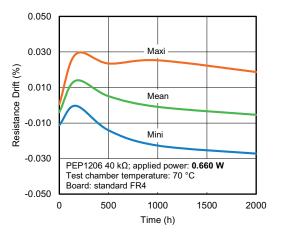


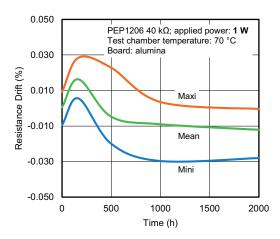
STABILITY TEST RESULTS







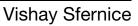




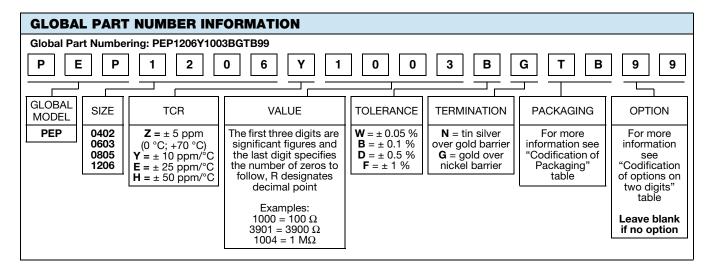
Note

• Note about stability test results: all parts reported by reflow with solder paste lead (Pb)-free SAC305 (Sn 96.5 % / Ag 3 % / Cu 0.5 %)









CODIFICATION OF OP	TIONS ON TWO DIGITS
OPTION	OPTION 2 DIGITS
0099	99
0100	0A
0101	0A
0102	0C
0103	0D
0104	0E
0105	0F
0124	0Y
0125	0Z
0126	1A
0127	1B
0128	1C
0320	8M
0321	8N
0322	80
0323	8P
0324	8Q
0325	8R

CODIFICATION OF SIZES		
CODE 18	CODE 40	
9	0402	
С	0603	
D	0805	
Н	1206	

CODIFICATION OF PACKAGING				
CODE 18	PACKAGING			
WAFFLE PACK				
W	100 min., 1 mult.			
WA	100 min., 100 mult. (available only on size 1206)			
PLASTIC TAPE (in standard for	or all sizes)			
Т	100 min., 1 mult.			
TA	100 min., 100 mult.			
ТВ	250 min., 250 mult.			
TC	500 min., 500 mult.			
TD	1000 min., 1000 mult.			
TE	2500 min., 2500 mult.			
TF	Full tape (quantity depending on size of chips)			



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 PEP1206Y2003WNTA
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