High Performance Schottky Rectifier, 1.0 A



Cathode		Anode
۰	◄	o

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	1.0 A			
V <sub>R</sub>	40 V			
V <sub>F</sub> at I <sub>F</sub>	0.53 V			
I <sub>RM</sub> max.	4.0 mA at 150 °C			
E <sub>AS</sub>	3.0 mJ			
T <sub>J</sub> max.	150 °C			
Package	SMB			
Circuit configuration	Single			

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#### **FEATURES**

- · Small foot print, surface mountable
- · Low forward voltage drop
- High frequency operation
- HALOGEN • Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### DESCRIPTION

The VS-MBRS140-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I <sub>F(AV)</sub>	Rectangular waveform	1.0	A	
V <sub>RRM</sub>		40	V	
I <sub>FSM</sub>	$t_p = 5 \ \mu s \ sine$	380	А	
V <sub>F</sub>	1.0 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.53	V	
TJ	Range	-55 to +150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-MBRS140-M3	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	40	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	40	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDI	TIONS	VALUES	UNITS
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at $T_L$ = 119 °C, rectangular waveform		1.0	
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated	380	А
non-repetitive surge current	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	load condition and with rated V <sub>RRM</sub> applied	40	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 6 mH		3.0	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical 1.0		А	

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COMPLIANT

FREE



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	NDITIONS	TYP.	MAX.	UNITS
	V <sub>FM</sub> <sup>(1)</sup>	1 A	T <sub>J</sub> = 25 °C	0.52	0.6	v
Maximum forward voltage drep		2 A		0.70	0.77	
Maximum forward voltage drop		1 A	T <sub>J</sub> = 125 °C	0.48	0.53	
		2 A		0.63	0.71	
	-+ (1)	T <sub>J</sub> = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	-	0.1	mA
Maximum reverse leakage current	T <sub>J</sub> = 125 °C	VR - naleu VR	-	4.0	ША	
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		-	80	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		-	2.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		-	10 000	V/µs

Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

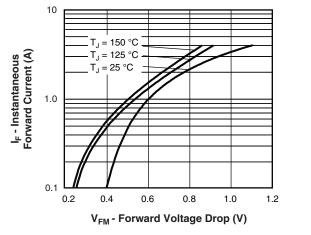
THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		-55 to +150	°C
Maximum thermal resistance, junction to lead	R <sub>thJL</sub> <sup>(2)</sup>	DC operation See fig. 4	36	°C/W
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	80	C/W
Approximate weight			0.10	g
Approximate weight			0.003	oz.
Marking device		Case style SMB (similar to DO-214AA)	1	4

#### Notes

(1)  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink

<sup>(2)</sup> Mounted 1" square PCB





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Fig. 1 - Maximum Forward Voltage Drop Characteristics

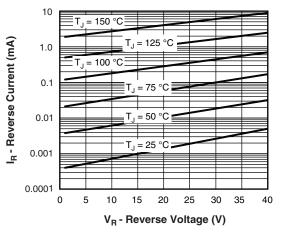


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

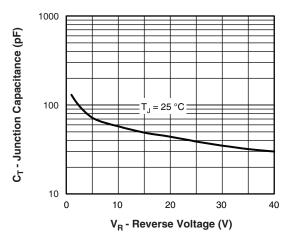
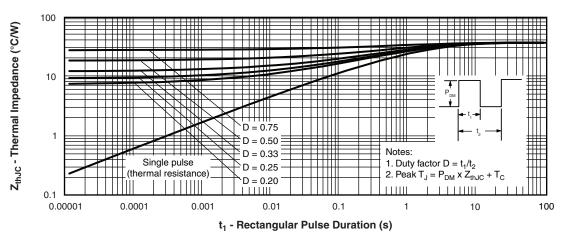
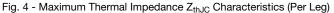


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



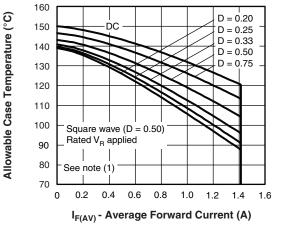


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Fig. 5 - Maximum Average Forward Current vs. Allowable Lead Temperature

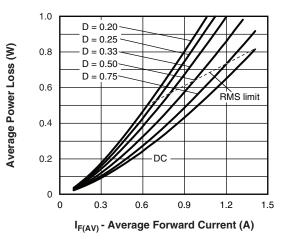


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

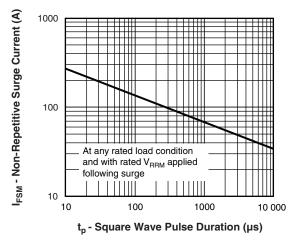


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

#### Note

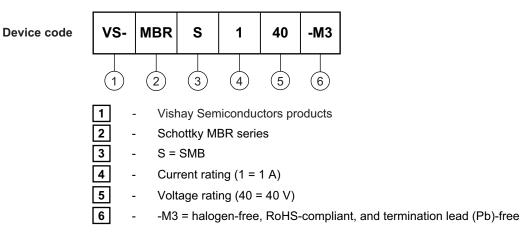
- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$ ;

  - $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$





#### **ORDERING INFORMATION TABLE**



ORDERING INFORMATION (Example)					
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-MBRS140-M3/5BT	5BT	3200	13" diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS		
Dimensions www.vishay.com/doc?95401		
Part marking information	www.vishay.com/doc?95403	
Packaging information	www.vishay.com/doc?95404	
SPICE model	www.vishay.com/doc?95299	

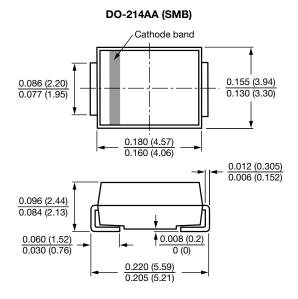


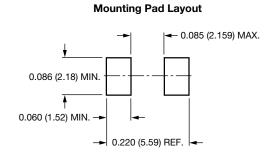
## **Outline Dimensions**

**Vishay Semiconductors** 

**SMB** 

#### **DIMENSIONS** in inches (millimeters)







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