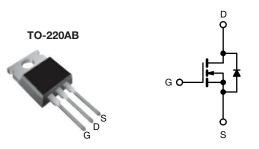


Power MOSFET



N-Channel MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	60			
R _{DS(on)} (Ω)	V _{GS} = 10 V 0.018			
Q _g (Max.) (nC)	110			
Q _{gs} (nC)	29			
Q _{gd} (nC)	36			
Configuration	Single			

FEATURES

- Dynamic dV/dt rating
- Repetitive avalanche rated
- Ultra low on-resistance
- · Very low thermal resistance
- 175 °C operating temperature
- · Fast switching
- · Ease of paralleling
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Note

* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

DESCRIPTION

Third generation power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.

ORDERING INFORMATION	
Package	TO-220AB
Lead (Pb)-free	IBFZ48PbF

ABSOLUTE MAXIMUM RATINGS (T _C	= 25 °C, unless otherwis	se noted)			
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V_{DS}	60	V	
Gate-source voltage		V _{GS}	± 20	7 v	
Continuous drain current	V_{GS} at 10 V $T_{C} = 25 ^{\circ}C$ $T_{C} = 100 ^{\circ}C$	I _D	50		
Continuous drain current	$T_C = 100 ^{\circ}$ C		50	Α	
Pulsed drain current a		I _{DM}	290	1	
Linear derating factor			1.3	W/°C	
Single pulse avalanche energy ^b		E _{AS}	100	mJ	
Repetitive avalanche current a		I _{AR}	50	Α	
Repetitive avalanche energy a		E _{AR}	19	mJ	
Maximum power dissipation $T_C = 25$ °C		P _D	190	W	
Peak diode recovery dV/dt ^c		dV/dt	4.5	V/ns	
Operating junction and storage temperature range		T _J , T _{stg}	- 55 to + 175	°C	
Soldering recommendations (peak temperature) d For 10 s			300		
Mary all and a second	6-32 or M3 screw		10	lbf ⋅ in	
Mounting torque	0-32 OF IVIS SCIEW		1.1	N⋅m	

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)
- b. V_{DD} = 25 V, starting T_J = 25 °C, L = 22 μ H, R_g = 25 Ω I_{AS} = 72 A (see fig. 12)
- c. $I_{SD} \le 72$ A, $dI/dt \le 200$ A/µs, $V_{DD} \le V_{DS}$, $T_{J} \le 175$ °C
- d. 1.6 mm from case
- e. Current limited by the package, (die current = 72 A)



Vishay Siliconix

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum junction-to-ambient	R _{thJA}	-	62	
Case-to-sink, flat, greased surface	R _{thCS}	0.50	-	°C/W
Maximum junction-to-case (drain)	R _{thJC}	-	0.80	

PARAMETER	SYMBOL	TEST	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static							
Drain-source breakdown voltage	V_{DS}	$V_{GS} = 0$	V, I _D = 250 μA	60	-	-	V
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	Reference t	o 25 °C, I _D = 1 mA	-	0.060	-	V/°C
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_0$	_{GS} , I _D = 250 μA	2.0	-	4.0	V
Gate-source leakage	I _{GSS}	Vo	_{GS} = ± 20	-	-	± 100	nA
Zoro gato voltago droin ourrent		V _{DS} = 6	V _{DS} = 60 V, V _{GS} = 0 V		-	25	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 48 V, V ₀	_{SS} = 0 V, T _J = 150 °C	-	-	250	μA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 43 A ^b	-	-	0.018	Ω
Forward transconductance	9 _{fs}	V _{DS} = 2	5 V, I _D = 43 A ^b	27	-	-	S
Dynamic							
Input capacitance	C _{iss}	V	_{3S} = 0 V,	-	2400	-	pF
Output capacitance	C _{oss}	V _C	$_{0S} = 25 \text{ V},$	-	1300	-	
Reverse transfer capacitance	C _{rss}	f = 1.0 ľ	MHz, see fig. 5	-	190	-	
Total gate charge	Q_g			-	-	110	
Gate-source charge	Q _{gs}	V _{GS} = 10 V	$I_D = 72 \text{ A}, V_{DS} = 48 \text{ V},$ see fig. 6 and 13 ^b	-	-	29	nC
Gate-drain charge	Q _{gd}		gr c and re	-	-	36	
Turn-on delay time	t _{d(on)}			-	8.1	-	
Rise time	t _r	$V_{DD} = 30 \text{ V}, I_D = 72 \text{ A},$ $R_g = 9.1 \ \Omega, R_D = 0.34 \ \Omega, \text{ see fig. } 10^{\text{b}}$		-	250	-	ns
Turn-off delay time	t _{d(off)}			-	210	-	
Fall time	t _f			-	250	-	
Internal drain inductance	L _D	Between lead, 6 mm (0.25") from package and center of die contact - 4.5 - 7.5		-	11		
Internal source inductance	L _S			-	7.5	-	- nH
Drain-Source Body Diode Characteristic	s						
Continuous source-drain diode current	I _S	MOSFET symbo	MOSFET symbol showing the		-	50°	Α
Pulsed diode forward current ^a	I _{SM}	integral reverse p - n junction did	ode	-	-	290	
Body diode voltage	V_{SD}	T _J = 25 °C, I _S	_S = 72 A, V _{GS} = 0 V ^b	-	-	2.0	V
Body diode reverse recovery time	t _{rr}	T 0500 L 70 L 11/11 100 L 1		-	120	180	ns
Body diode reverse recovery charge	Q _{rr}	$J = 25 \text{ °C, I}_{F} = 1.5 \text{ °C}$	72 A, dl/dt = 100 A/µs ^b	-	0.50	0.80	μC
Forward turn-on time	t _{on}	Intrinsic turn-	on time is negligible (turn	-on is do	minated b	y L _S and	L _D)

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)
- b. Pulse width \leq 300 μ s; duty cycle \leq 2 %
- c. Current limited by the package, (die current = 72 A)



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

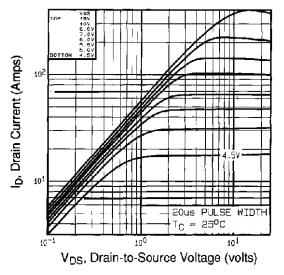


Fig. 1 - Typical Output Characteristics, T_C = 25 °C

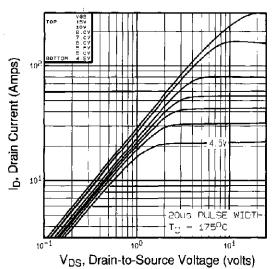
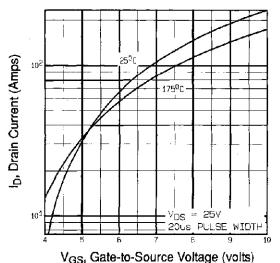


Fig. 2 - Typical Output Characteristics, T_C = 175 °C



. G3, date to octave towage (verta)

Fig. 3 - Typical Transfer Characteristics

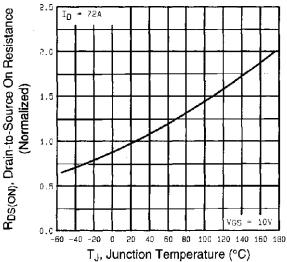


Fig. 4 - Normalized On-Resistance vs. Temperature

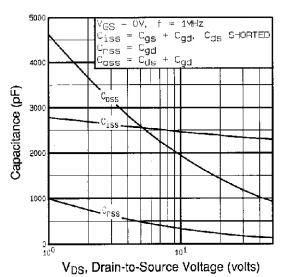


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

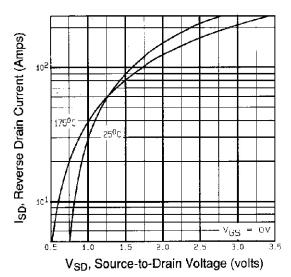


Fig. 7 - Typical Source-Drain Diode Forward Voltage

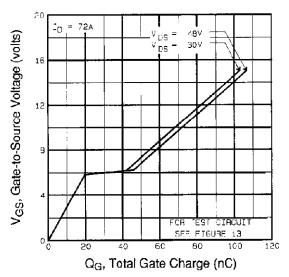


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

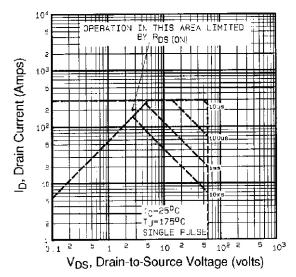


Fig. 8 - Maximum Safe Operating Area



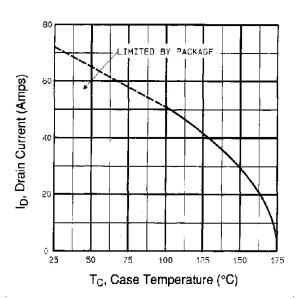


Fig. 9 - Maximum Drain Current vs. Case Temperature

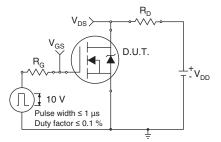


Fig. 10a - Switching Time Test Circuit

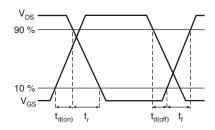


Fig. 10b - Switching Time Waveforms

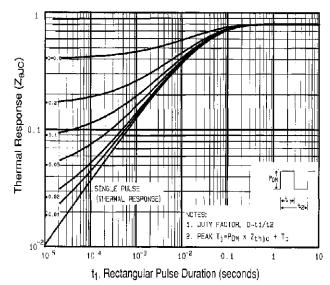
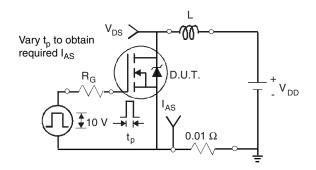


Fig. 10 - Maximum Effective Transient Thermal Impedance, Junction-to-Case





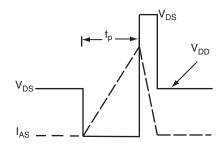


Fig. 12a - Unclamped Inductive Test Circuit

Fig. 12b - Unclamped Inductive Waveforms

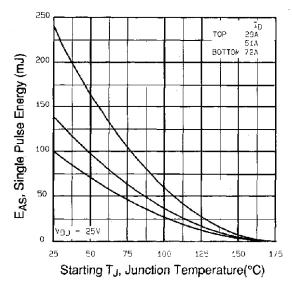


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

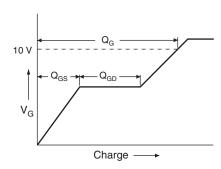


Fig. 13a - Basic Gate Charge Waveform

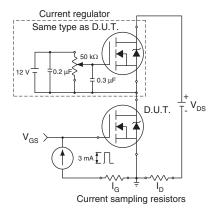
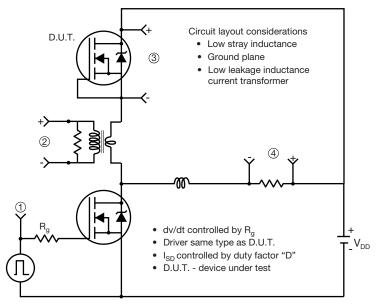


Fig. 13b - Gate Charge Test Circuit



Peak Diode Recovery dv/dt Test Circuit



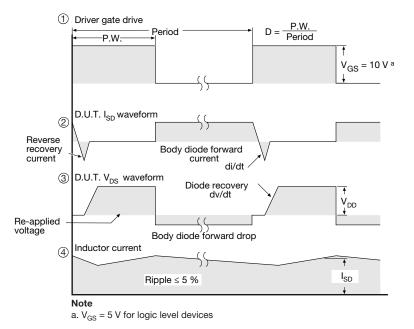


Fig. 14 - For N-Channel

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TO-220-1



DIM.	MILLIN	METERS	INCHES		
	MIN.	MAX.	MIN.	MAX.	
Α	4.24	4.65	0.167	0.183	
b	0.69	1.02	0.027	0.040	
b(1)	1.14	1.78	0.045	0.070	
С	0.36	0.61	0.014	0.024	
D	14.33	15.85	0.564	0.624	
Е	9.96	10.52	0.392	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.10	6.71	0.240	0.264	
J(1)	2.41	2.92	0.095	0.115	
L	13.36	14.40	0.526	0.567	
L(1)	3.33	4.04	0.131	0.159	
ØР	3.53	3.94	0.139	0.155	
Q	2.54	3.00	0.100	0.118	

Note

 \bullet $M^{\star}=0.052$ inches to 0.064 inches (dimension including protrusion), heatsink hole for HVM



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