COMPLIANT

HALOGEN

FREE



Vishay Semiconductors

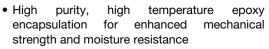
High Performance Schottky Rectifier, 2 x 20 A

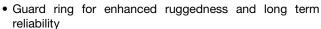


DRIMARY QUARACTERISTICS									
PRIMARY CHARACTERISTICS									
I _{F(AV)}	2 x 20 A								
V_R	45 V								
V _F at I _F	0.48 V								
I _{RM} typ.	115 mA at 125 °C								
T _J max.	150 °C								
E _{AS}	20 mJ								
Package	3L TO-220AB								
Circuit configuration	Common cathode								

FEATURES

- 150 °C T_J operation
- · Very low forward voltage drop
- High frequency operation





- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	40	Α						
V _{RRM}		45	V						
I _{FSM}	$t_p = 5 \mu s sine$	1240	Α						
V _F	20 A _{pk} , T _J = 125 °C (per leg)	0.48	V						
T _J	Range	-55 to +150	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-40CTQ045-M3	UNITS					
Maximum DC reverse voltage	V _R	45						
Maximum working peak reverse voltage	V _{RWM}	45	V					

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS					
Maximum average forward current per leg		50 % duty avala at T = 116 °	20						
See fig. 5 per device	I _{F(AV)}	50 % duty cycle at T_C = 116 °C, rectangular waveform			40				
Maximum peak one cycle non-repetitive surge current per leg		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1240	A				
See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	350					
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 3 \text{A}, L = 4.4 \text{r}$	20	mJ					
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		3	Α				



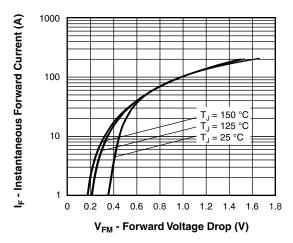
ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST COI	VALUES	UNITS					
		20 A	T _ 05 °C	0.53					
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	40 A	T _J = 25 °C	0.68					
See fig. 1	V _{FM} ('')	20 A	T _ 105 °C	0.48	V				
		40 A	T _J = 125 °C	0.67					
Maximum rayaraa laakaga ayrrant nar lag	I _{RM} ⁽¹⁾	T _J = 25 °C	V Pated V-	3	mΛ				
Maximum reverse leakage current per leg		T _J = 125 °C	V _R = Rated V _R	150	mA				
Typical reverse leakage current	I _{RM} ⁽¹⁾	T _J = 125 °C	V_R = Rated V_R	115	mA				
Threshold voltage	V _{F(TO)}	T T mayimayim		0.27	V				
Forward slope resistance	r _t	$T_{J} = T_{J}$ maximum		8.72	mΩ				
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range	2800	pF					
Typical series inductance per leg	L _S	Measured lead to lead 5 m	8.0	nH					
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs					

Note

 $^{(1)}\,$ Pulse width $<300~\mu s,$ duty cycle <2~%

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +150	°C				
Maximum thermal resistance, junction to case per leg Maximum thermal resistance, junction to case per package		В	DC operation	2.0					
		R _{thJC}	DC operation	1.0	°C/W				
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth, and greased	0.50					
Approximate weight				2	g				
Approximate weight				0.07	OZ.				
Mounting torque	minimum			6 (5)	kgf · cm				
Mounting torque —	maximum			12 (10)	(lbf ⋅ in)				
Marking device			Case style 3L TO-220AB	40CTQ045					





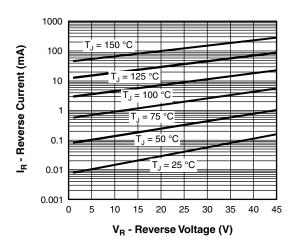


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

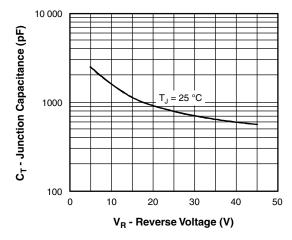


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

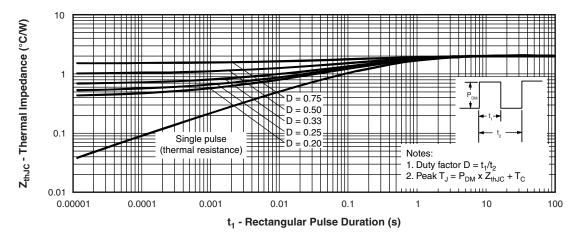


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)



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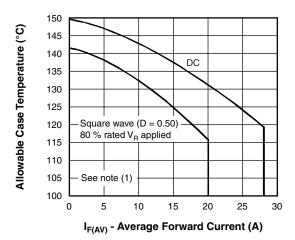


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

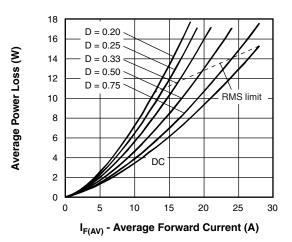


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

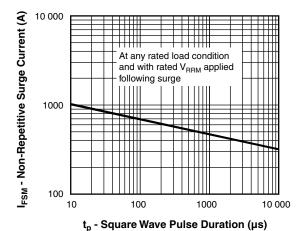


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

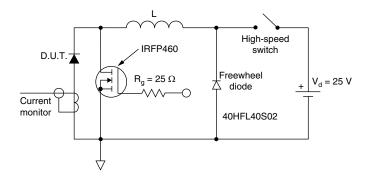


Fig. 8 - Unclamped Inductive Test Circuit

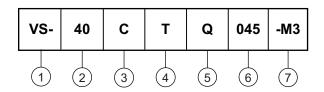
Note

¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = forward power loss = I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 10 \text{ V}$



ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Current rating (40 = 40 A)
- 3 Circuit configuration:

C = Common cathode

4 - Package:

T = TO-220

- 5 Schottky "Q" series
- 6 Voltage rating (045 = 45 V)
- 7 Environmental digit

-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

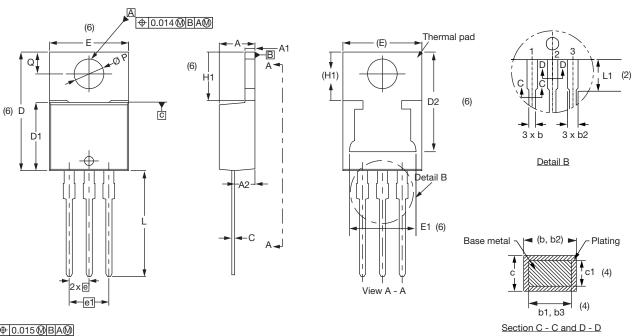
ORDERING INFORMATION (Example)									
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION									
VS-40CTQ045-M3	50	1000	Antistatic plastic tube						

LINKS TO RELATED DOCUMENTS							
Dimensions <u>www.vishay.com/doc?96154</u>							
Part marking information	www.vishay.com/doc?95028						

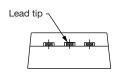


3L TO-220AB

DIMENSIONS in millimeters and inches



⊕ 0.015 **M** B A **M**



Conforms to JEDEC® outline TO-220AB

SYMBOL	MILLIMETERS		INC	HES	NOTES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	JIES	STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183			D2	11.68	13.30	0.460	0.524	6, 7
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105	
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208	
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552	
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2
c1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154	
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118	
D1	8.38	9.02	0.330	0.355		1		•			•	

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- (7) Outline conforms to JEDEC® TO-220, except D2



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