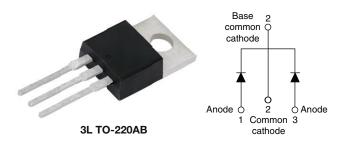


High Performance Schottky Rectifier, 2 x 20 A



PRIMARY CHARACTERISTICS								
I _{F(AV)}	2 x 20 A							
V_{R}	100 V							
V _F at I _F	0.67 V							
I _{RM} max.	11 mA at 125 °C							
T _J max.	175 °C							
E _{AS}	7.50 mJ							
Package	3L TO-220AB							
Circuit configuration	Common cathode							

FEATURES

- 175 °C T_J operation
- Low forward voltage drop



- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I _{F(AV)}	Rectangular waveform	40	А				
V _{RRM}		100	V				
I _{FSM}	t _p = 5 µs sine	850	А				
V _F	20 A _{pk} , T _J = 125 °C (per leg)	0.67	V				
TJ	Range	-55 to +175	°C				

VOLTAGE RATINGS							
PARAMETER	SYMBOL	VS-43CTQ100-M3	UNITS				
Maximum DC reverse voltage	V_R	100	V				
Maximum working peak reverse voltage	V_{RWM}	100	v				

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average forward per le		_	50 % duty cycle at T _C = 135 °C, rectangular waveform		20			
current, see fig. 5	per device	I _{F(AV)}	50 % duty cycle at 1 _C = 135 °C	o, rectangular wavelonn	40			
Maximum peak one cycle non-repetitive surge current per leg, see fig. 7			5 μs sine or 3 μs rect. pulse	Following any rated load condition and	850	A		
		I _{FSM}	10 ms sine or 6 ms rect. pulse	with rated V _{RRM} applied	275			
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 0.50 A, L = 60 mH		7.50	mJ		
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.50	Α		



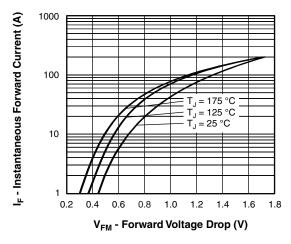
ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS				
		20 A	T _{.1} = 25 °C	0.81	V			
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	40 A	11 = 23 0	0.98				
See fig. 1	VFM (1)	20 A	T 105 °C	0.67				
		40 A	- T _J = 125 °C	0.81				
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	1	mA			
See fig. 2	'RM \''	T _J = 125 °C	v _R = nateu v _R	11				
Threshold voltage	V _{F(TO)}	T T mayimum		0.71	V			
Forward slope resistance	r _t	$T_J = T_J$ maximum		0.43	mΩ			
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal ran	1480	pF				
Typical series inductance per leg	L _S	Measured lead to lead 5 m	8.0	nΗ				
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs				

Note

 $^{(1)}\,$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C			
Maximum thermal resistance, junction to case per leg Maximum thermal resistance, junction to case per package		Б	DC energtion	2.0				
		R _{thJC}	DC operation	1.0	°C/W			
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth, and greased	0.50				
Annyayimata wajaht				2	g			
Approximate weight				0.07	OZ.			
Mounting torque	minimum			6 (5)	kgf ⋅ cm			
	maximum			12 (10)	(lbf \cdot in)			
Marking device			Case style 3L TO-220AB	43CTQ100				







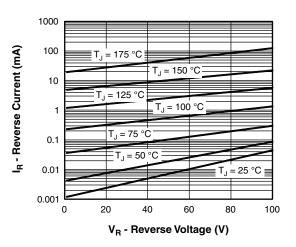


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

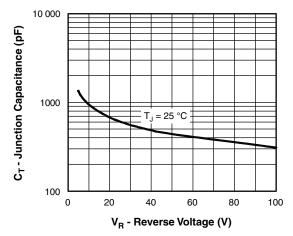


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

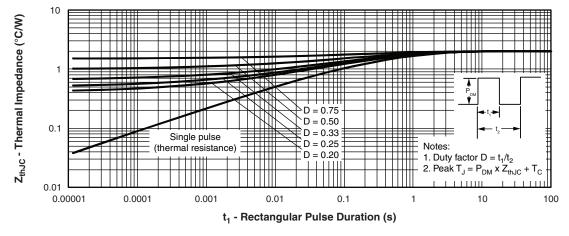
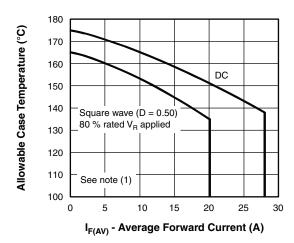
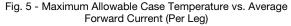


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)







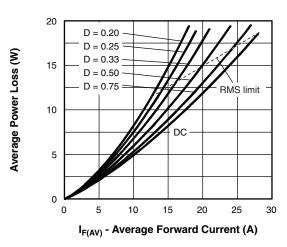


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

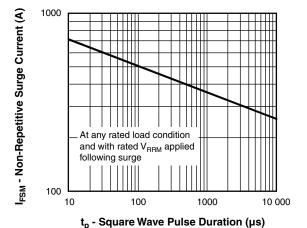


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

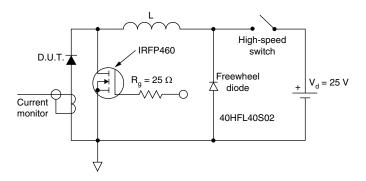


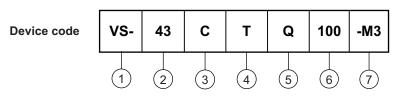
Fig. 8 - Unclamped Inductive Test Circuit

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 10 \text{ V}. \end{array}$



ORDERING INFORMATION TABLE



- 1 Vishay Semiconductors product
- Current rating (40 A)
- 3 Circuit configuration
 - C = common cathode
- 4 Package
 - T = TO-220
- 5 Schottky "Q" series
- 6 Voltage rating (100 = 100 V)
- 7 Environmental digit
 - -M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

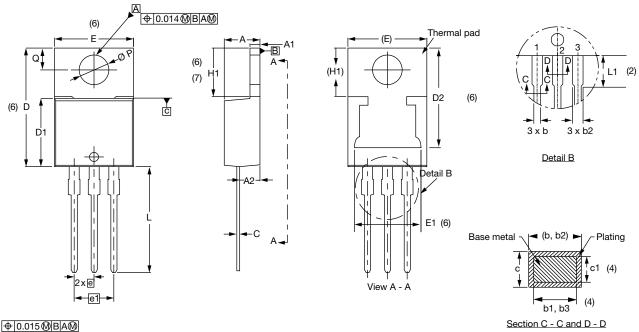
ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-43CTQ100-M3	50	1000	Antistatic plastic tube						

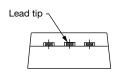
LINKS TO RELATED DOCUMENTS						
Dimensions <u>www.vishay.com/doc?96154</u>						
Part marking information	www.vishay.com/doc?95028					
SPICE model	www.vishay.com/doc?95065					



3L TO-220AB

DIMENSIONS in millimeters and inches





Conforms to JEDEC® outline TO-220AB

SYMBOL	MILLIMETERS		INC	INCHES		NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183			D2	11.68	12.88	0.460	0.507	6
A1	1.14	1.40	0.045	0.055			Е	10.11	10.51	0.398	0.414	3, 6
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105	
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208	
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6, 7
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552	
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2
c1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154	
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118	
D1	8.38	9.02	0.330	0.355								

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- (7) Outline conforms to JEDEC® TO-220, except D2 (minimum)



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Vishay

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