

Type 3560 Series

**Key Features** 

6W@70°C in 4527 size package

Suitable for auto placement

Available from distribution

Terminal finish matte sn over ni barrier

AEC-Q200 Qualified



TE Connectivity is pleased to announce that our 3560 high power Thick Film Chip Resistor is now AEC-Q200 Qualified. This sister to our popular 3522 series is suitable for auto placement in volume and for most applications. Supplied as standard on 7 inch Reels of 1000 pieces per reel.

Power Rating @ 70°C	6W
Resistance Range	1Ω ~ 10ΜΩ
Resistance Tolerance	±1%, ±5%
Temperature Coefficient of Resistance (TCR)	1Ω≦R≤10Ω ≤± 400PPM/°C
	10Ω< R ≦100Ω≤±200PPM/°C
	100Ω< R ≦10MΩ≤±100PPM/°C
Max. Working Voltage	300V
Max. Overload Voltage	600V
Dielectric Withstanding Voltage	600V
Operating Temperature Range	-55℃ <b>~</b> 155℃

### **Characteristics – Electrical**

Resistors shall have a rated direct-current (DC) continuous working voltage or an approximate sine-wave root-mean-square (RMS) alternating-current (AC) continuous working voltage at commercial line frequency and waveform corresponding to the power rating, as determined from the following formula :

 $RCWV = VP \times R$ 

Where the calculated RCWV is greater than the stated Max. Working Voltage, the Max. Working Voltage will apply.

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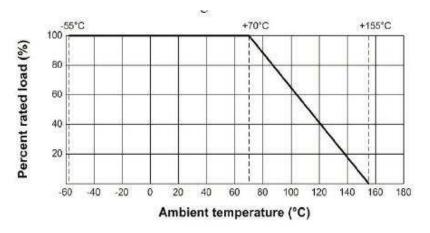
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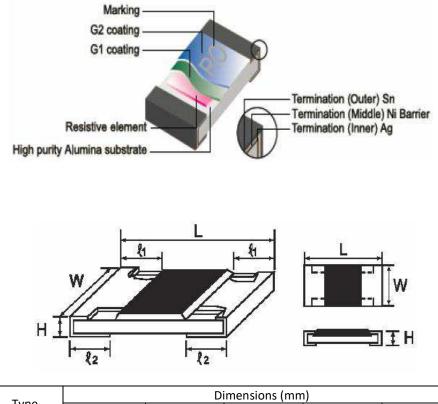


# **Power Rating and Derating**

Resistors shall have a power rating based on continuous load operation at an ambient temperature of 70  $^{\rm o}{\rm C}$ . For temperature in excess of 70  $^{\rm o}{\rm C}$ , The load shall derate as shown in chart below.



### **Construction and Dimensions:**



	Туре	Dimensions (mm)					
		L	W	Н	e1	ይ2	
[	3560	11.6 ± 0.30	6.85 ± 0.25	$1.10 \pm 0.10$	$1.00 \pm 0.20$	2.50 ± 0.20	

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# **Environmental Characteristics**

Characteristics	Limits	Test Methods		
Operational Life	± (1.0% + 0.1Ω) Max	125°C, at 35% of operating power, 1000H hours "ON", 0.5 hour "OFF"). (MIL-STD-202)		
	<100mΩ	Apply to rate current for $0\Omega$		
Electrical Characterization	1Ω≦R≤10Ω ≤± 400PPM/°C 10Ω< R ≦100Ω≤±200PPM/°C 100Ω< R ≦10MΩ≤±100PPM/°C	Parametrically test per lot and sample size requirements, summary to show Min, Max, Mean and standard deviation at room as well as Min and Max operating temperatures. (User spec)		
External Visual	No Mechanical Damage	Electrical test not required. Inspect device construction, marking and workmanship (MIL-STD-883 Method 2009)		
Physical Dimension	Reference Dimension Chart.	Verify physical dimensions to the applicable device detail specification. Note: User(s) and Suppliers spec. Electrical test not required (JESD22 MH Method JB-100)		
Resistance To Solvent	Marking Unsmeared	Note: Add aqueous wash chemical-OKEM Clean or equivalent. Do not use banned solvents. (MIL-STD-202 Method 215)		
Terminal Strength	Not Broken	Force of 1.8kg for 60 seconds. (JIS-C-6429)		
High Temperature ± (1.0% + 0.1Ω) Max Exposure (storage)		1000hrs. @T=155°C. Unpowered. Measurement at 24±2 hours after test conclusion. (MIL-STD-202 Method 108)		
	<50mΩ	Apply to rate current for $0\Omega$		
Temperature Cycling	Resistance change rate is: $\pm (0.5\% + 0.1\Omega)$ Max.	1000 Cycles (-55°C to 155°C). Measure at 24± hours after test conclusion. (JESD22 Method JA-104)		
<50mΩ		Apply to rate current for $0\Omega$ Temp(2) are a star and a star a		
	<50mΩ	Apply to rate current for $0\Omega$		
Biased Humidity	Resistance change rate is: ± (1.0% + 0.1Ω) Max.	10% rated power, 85°C/85%RH, 1000H, Measurement at 24 hours after test conclusion. (MIL-STD-202 Method 103)		
	<100mΩ	Apply to rate current for $0\Omega$		

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# **Environmental Characteristics (cont.)**

Characteristics	Limits	Test Methods	
Mechanical Shock	± (1.0% + 0.1Ω) Max.	Wave Form: Tolerance for half since shock pulse. Peak value is 100g's. Normal duration (D) is 6. (MIL-STD-202 Method 213)	
Vibration	± (1.0% + 0.1Ω) Max.	5g's for 20 min., 12 cycles each of 3 orientations. Note: Use 8"*5"PCB. 031" thick 7 secure points (onone) long side and 2 secure points at corners of opposite sides. Parts mounted within 2' from any secure point. Test from 10-2000Hz.(MIL-STD-202 Method 204)	
Thermal Shock ± (1.0% + 0.1Ω) Max.		-55°C to 155°C. Note: Number of cycles required -300, Maximum transfer time -20 seconds, Dwell time -15 minutes. Air-Air. (MIL-STD-202 Method 107)	
	<50mΩ	Apply to rate current for 0Ω	
ESD	± (10.0% + 0.1Ω) Max.	With the electrometer in direct contact with the discharge tip, verify the voltage setting at levels of $\pm$ 500V, $\pm$ 1KV, $\pm$ 2KV, $\pm$ 4KV, $\pm$ 8KV, The electrometer reading shall be within $\pm$ 10% for voltage from 500V to $\leq$ 800V. (AEC-Q200-002)	
Solderability	95% Coverage Min.	For both leaded and SMD. Electrical test not required. Magnification 50X. Conditions: Method B 4hrs at 155°C dry heat, then dip in bath at 245°C, 5s. Method B: at 215°C, 5s. Method D: at 260°C, 60s. (J-STD-202)	
Flammability	No ignition of the tissue paper or scorching of the pinewood board	V-0 or V-1 are acceptable. Electrical test not required. (UL-94)	
Board Flex	± (1.0% + 0.05Ω) Max. <50mΩ	2mm (Min) (JIS-C-6429) Apply to rate current for 0Ω	
Flame Retardance		Temperature sensing at 500°C, Voltage power subjected to 32VDC current clamped up to 500ADC and decreased in 1.0VDC/hour. (AEC-Q200-001)	
Resistance To Soldering Heat	± (1.0% + 0.05Ω) Max. <50mΩ	Condition B No per-heat of samples. Note: Single Wave Solder-Procedure 2 for SMD and Procedure 1 for Leaded with solder within 1.5mm of device body. (MIL-STD-202 Method 210) Apply to rate current for 0W	

\* Sulfuration test: H2S 3~5PPM 50 ±2 91%~93%RH 1000H

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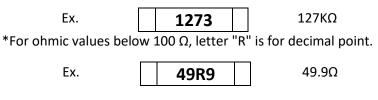
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### Marking

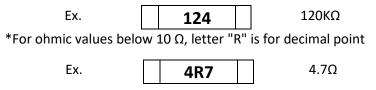
A. 4 digit marking for E-96 series:

\*The first 3 digits are significant figures of resistance and the 4th digit denoted number of zeros.

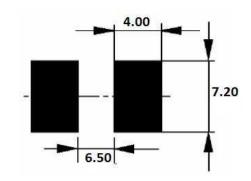


B. 3 digit marking for E-24 series:

\*The first 2 digits are significant figures of resistance and the 3rd digit denoted number of zeros



# Soldering



4 layers PCB specification:

- 1) Outside 2 layers (Top and Bottom) with copper foil thickness at 2oz.
- 2) Inside 2 layers (Middle layers) with copper foil thickness at 4 oz.

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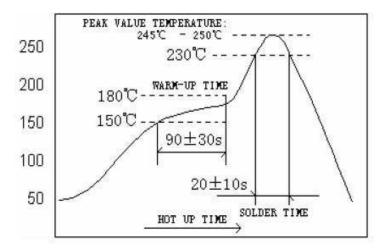
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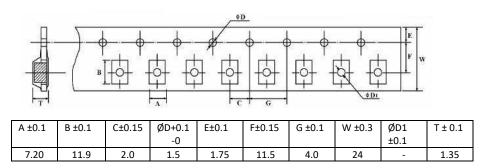
# Soldering (cont.)

Reflow solder profile



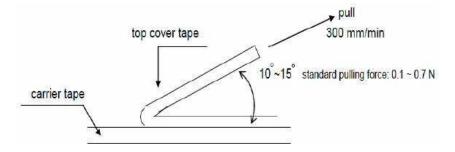
## Packaging

### **Tape and Reel**



### Peeling Strength of Top Cover Tape

Test Condition: 0.1 to 0.7 N at a peel-off speed of 300 mm / min.



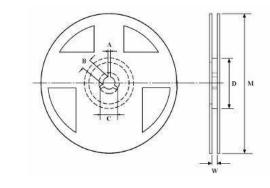
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### **Reel Dimensions**



Qty Reel	A±0.5	B±0.5	C±0.5	D±1	M±2	W±1
1000	2.0	13.0	21	60.0	178	25.5

#### **Environment Related Substance**

This product complies to EU RoHS directive, EU PAHs directive, EU PFOS directive and Halogen free.

Ozone depleting substances are not used in our manufacturing process of this product. This product is not manufactured using Chloro fluorocarbons (CFCs), Hydrochlorofluorocarbons (HCFCs), Hydrobromofluorocarbons (HBFCs) or other ozone depleting substances in any phase of the manufacturing process.

#### **Storage Condition**

The performance of these products, including the solderability, is guaranteed for a year from the date of arrival at your company, provided that they remain packed as they were when delivered and stored at a temperature of  $25^{\circ}$ C ±  $10^{\circ}$ C and a relative humidity of 60%RH ± 10%RH, chemical and dust free atmosphere.

Even within the above guarantee periods, do not store these products in the following conditions:

- In salty air or in air with a high concentration of corrosive gas, such as Cl2, H2S, NH3, SO2, or NO2
- 2. In direct sunlight

#### **How To Order**

3540	1R0	F	Т
Common Part	Resistance Value	Tolerance	Pack Style
3560 – 6W 4527 Resistor	1Ω - 1R0 100Ω - 100R 1KΩ - 1K0	F — 1% J — 5%	T- 1000 per reel

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Authorized Distributor

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