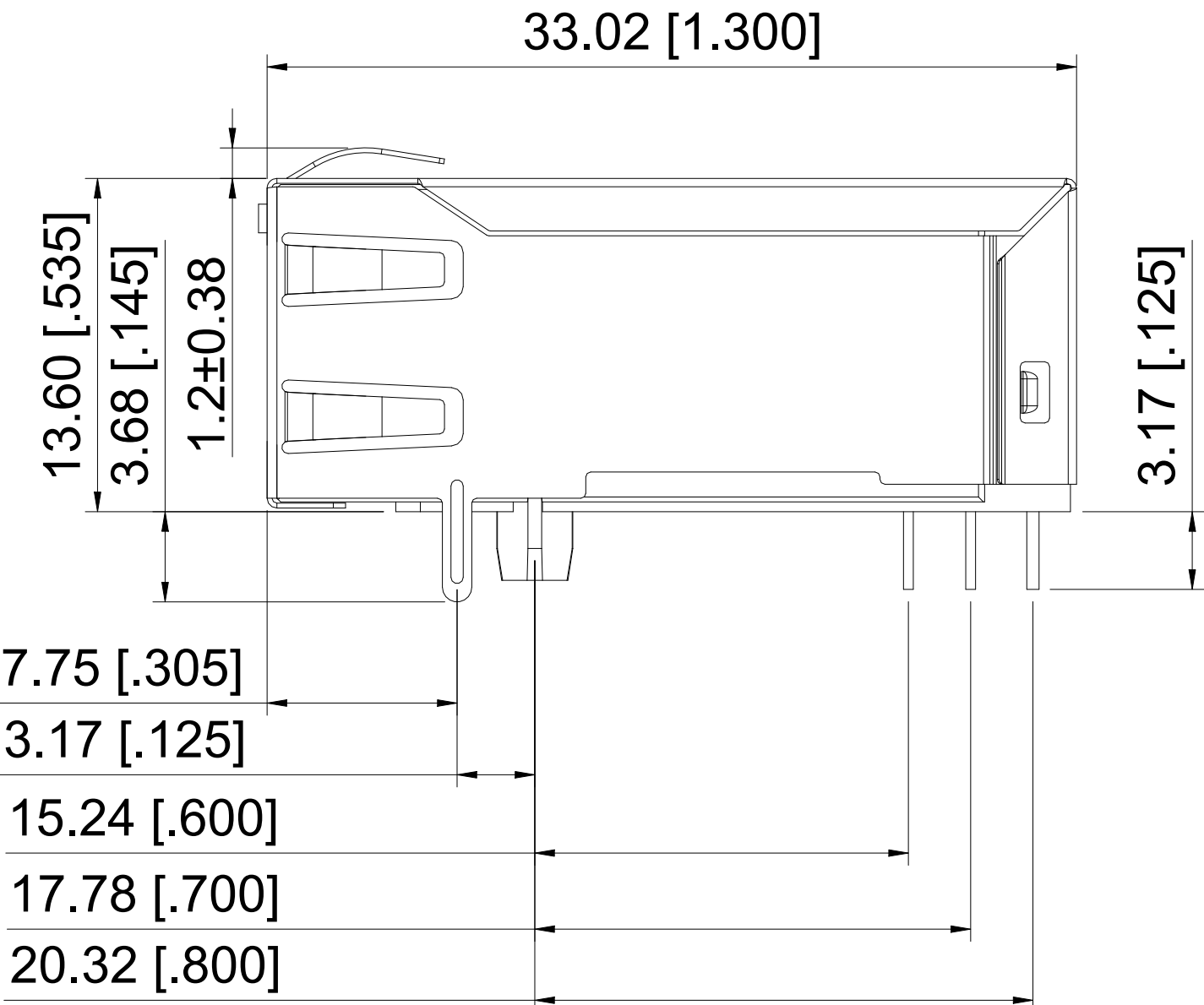
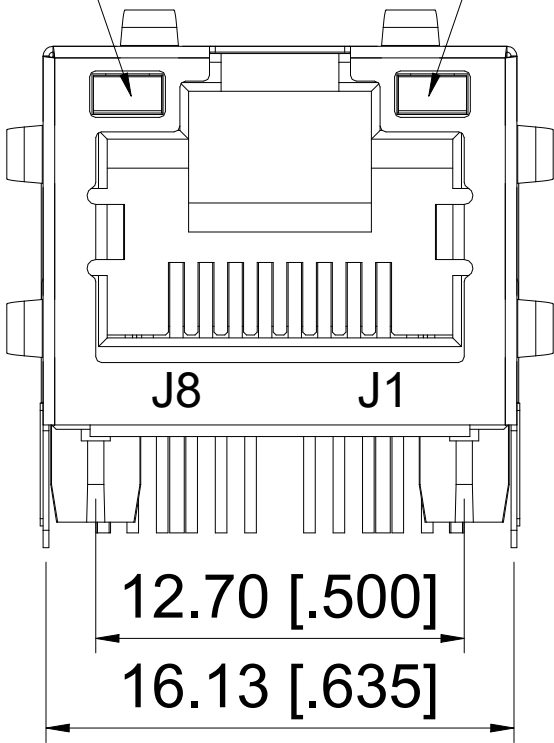


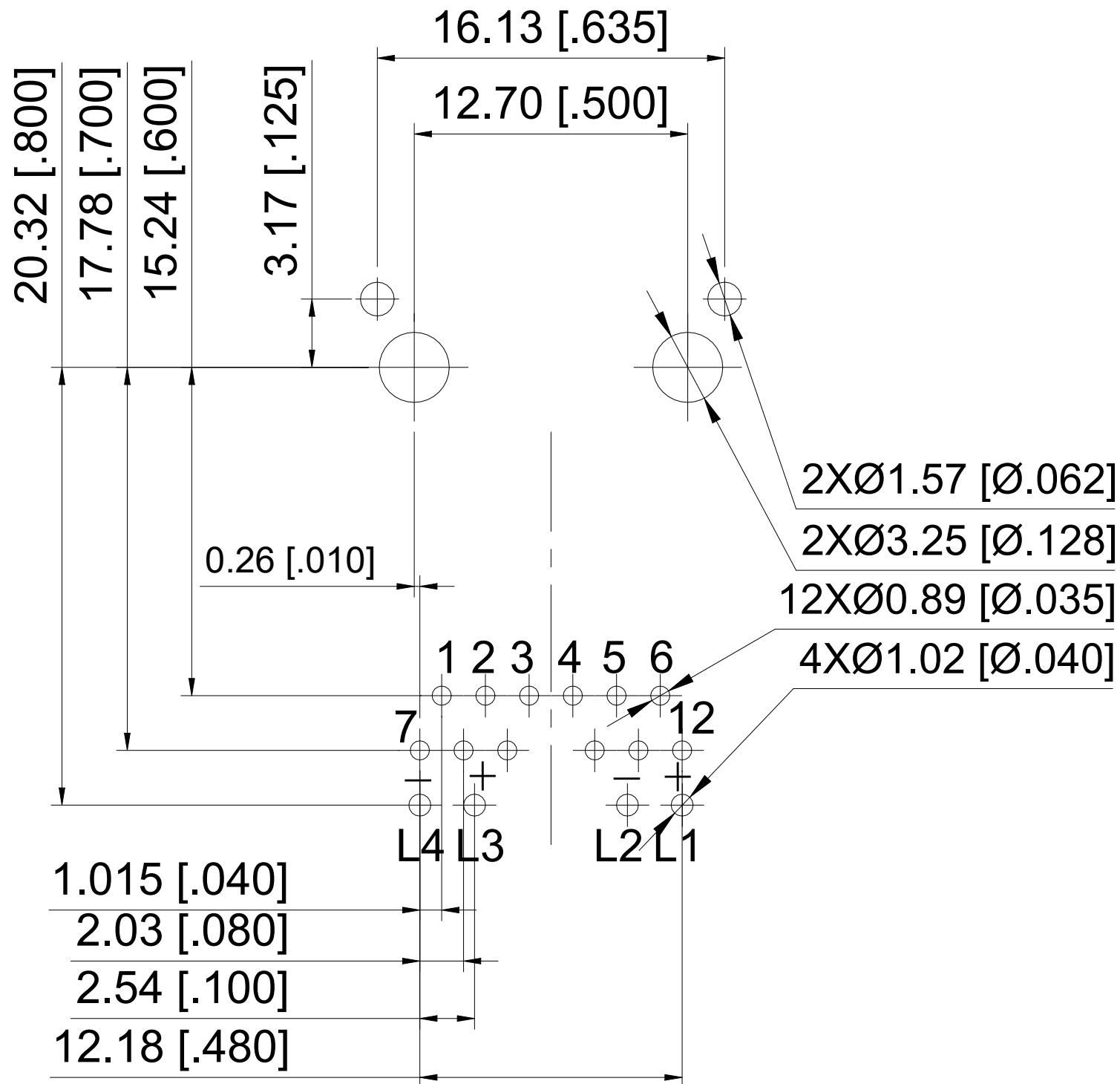
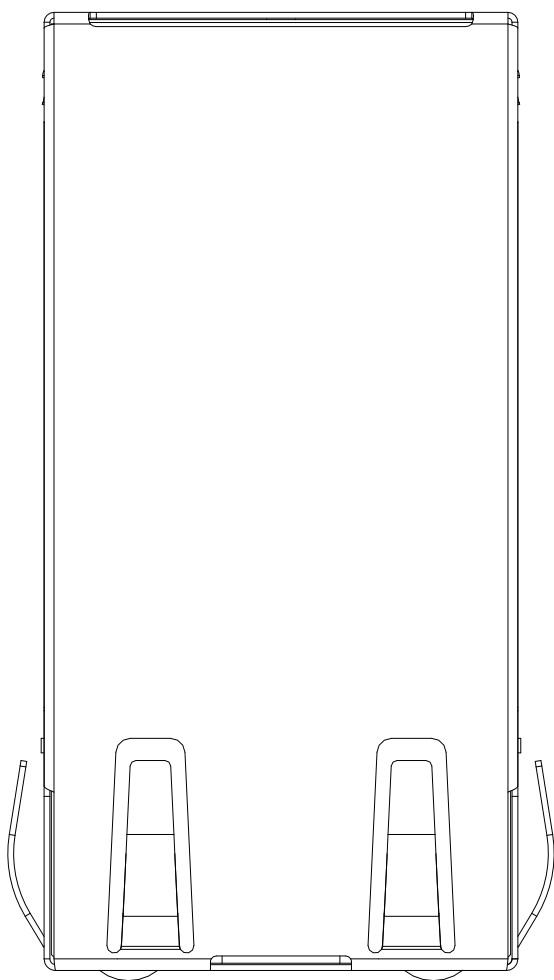
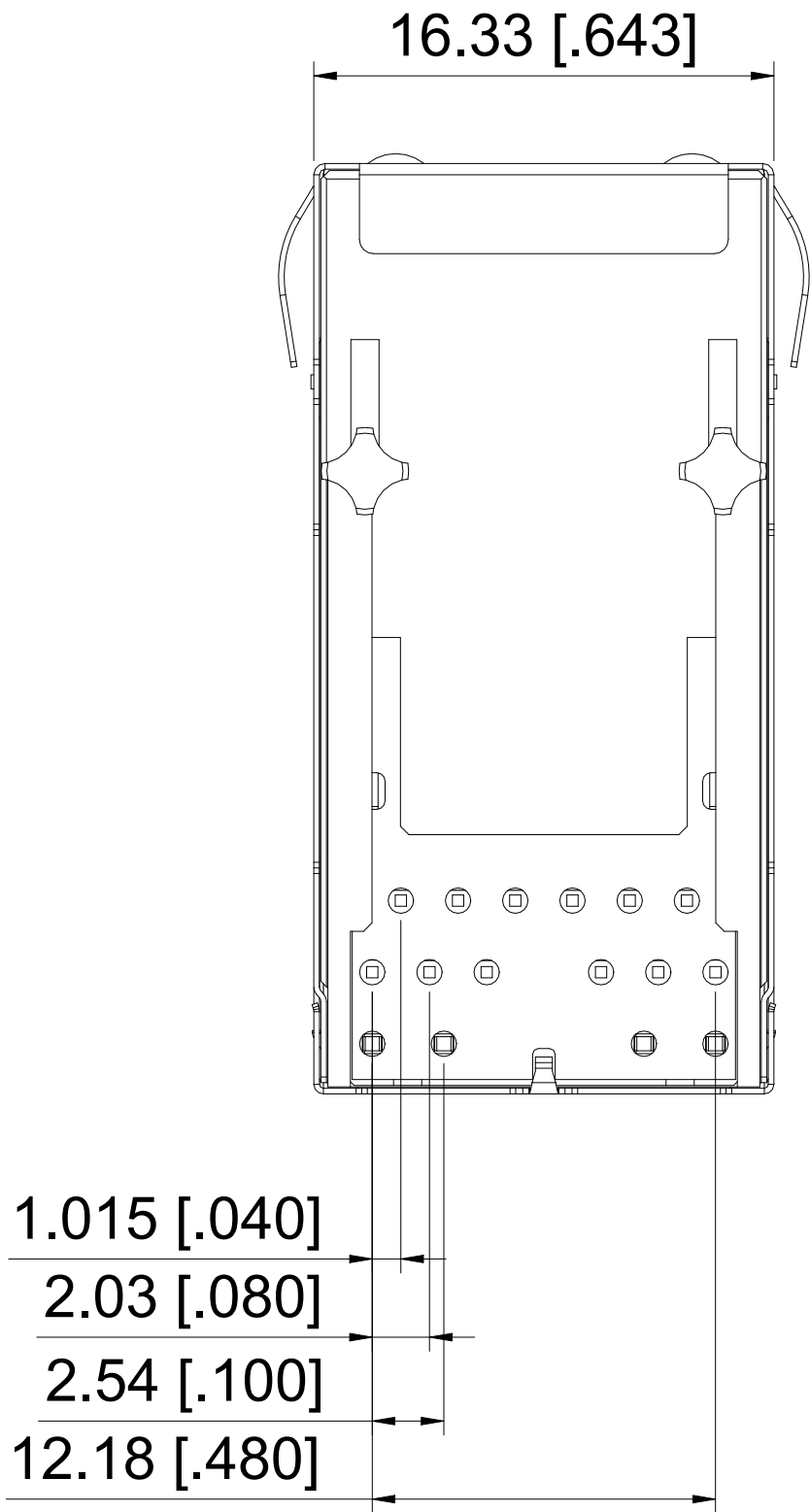
REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
	A	INITIAL RELEASE	11JUL2024	RK	PSS

GreenGreen



NOTES:

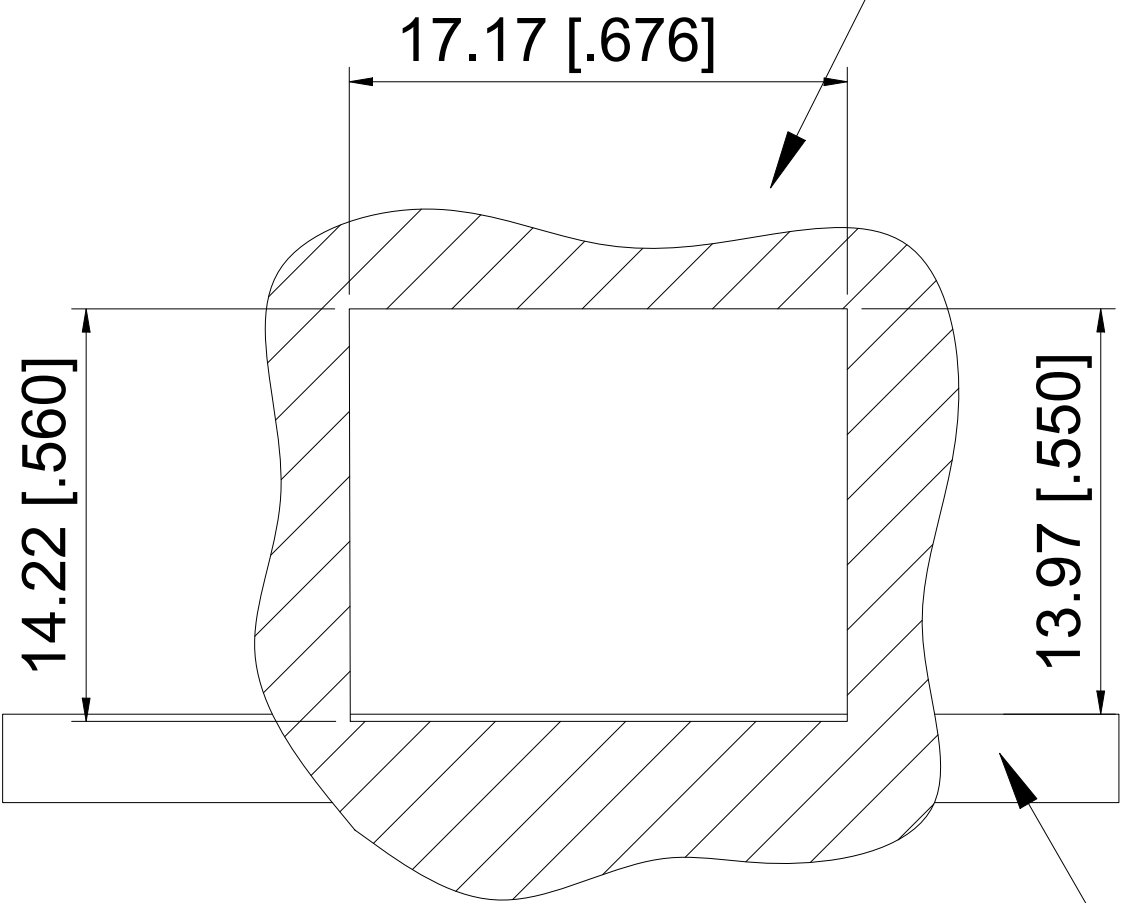
- 1.HOUSING MATERIAL:GLASS FILLED PA46(BLACK) UL94V-0(ROHS)
- 2.CONTACT: PHOSPHOR BRONZE,50 MICROINCHES MIN. OVERALL DUCTILE NICKEL UNDERPLATE WITH SELECTED (REFER P/N TABLE) MICROINCHES GOLD AT MATING INTERFACE
- 3.SHIELD:0.2mm THICKNESS WITH BRASS.
- 4.JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68 SUBPART F
- 5.THE PART IS RECOMMENDDDED FOR WAVE SOLDERING PROCESS PEAK SOLDERING TEMPERATURE IS 260°C MAX,10 SECS MAX.
- 6.OPERATING TEMPERATURE:0°C TO +70°C.
STORAGE TEMPERATURE:-40°C TO +85°C.



METAL PANEL

RECOMMENDED P.C.B LAYOUT
TOP VIEW (COMPONENT SIDE)

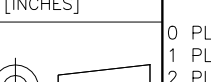
Dimension Tolerance:
All PCB LAYOUT
Tolerance:±0.05[.002]



PANEL CUTOUT

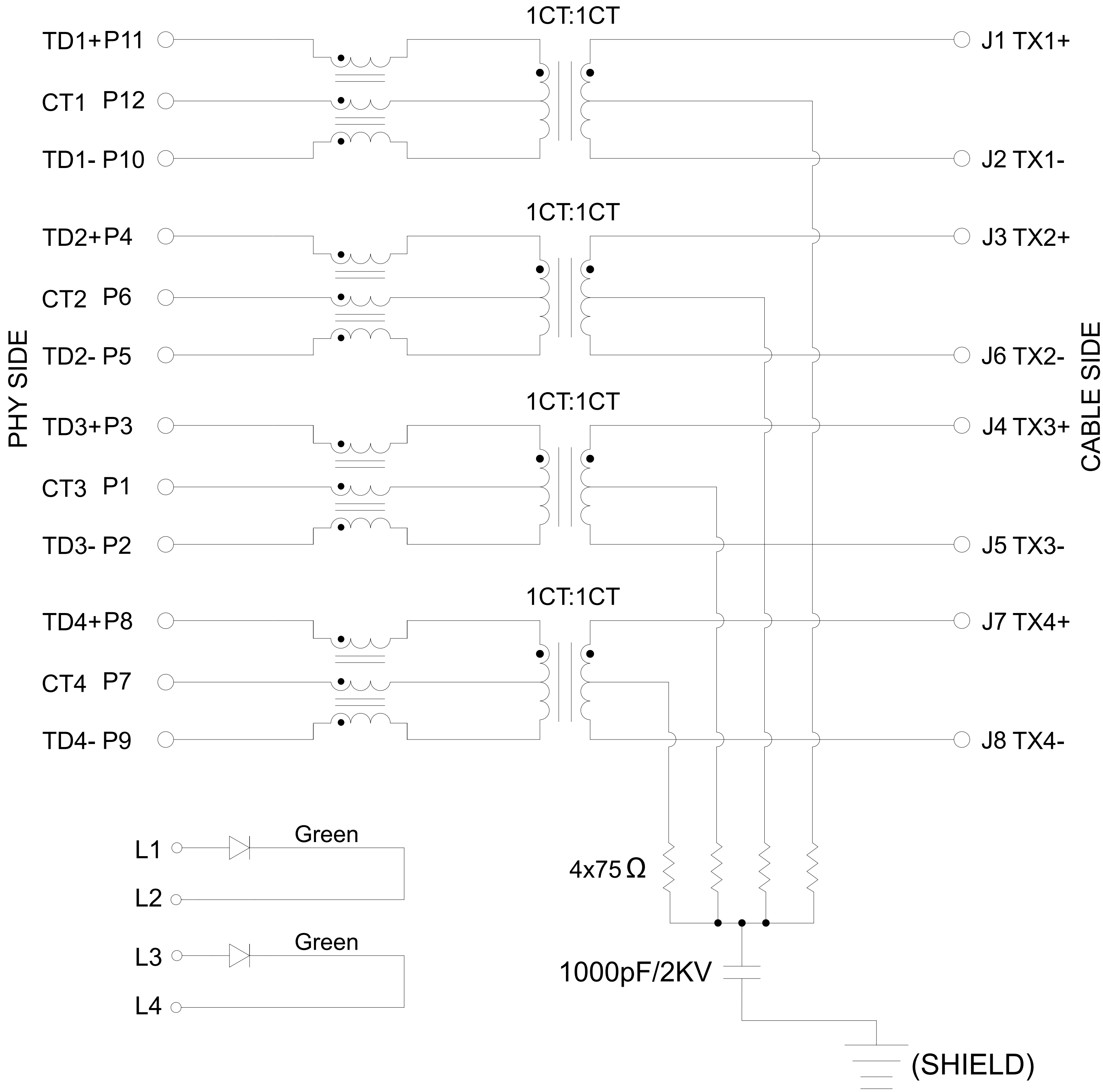
PCB

0.04572 μm [1.8μ"]	2489120-6
0.0762 μm [3μ"]	2489120-5
0.1524 μm [6μ"]	2489120-4
0.381 μm [15μ"]	2489120-3
0.762 μm [30μ"]	2489120-2
1.27 μm [50μ"]	2489120-1
GOLD PLATING THICKNESS PART NUMBER	

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN RAMESH KIVADER 13JUN2024	 TE Connectivity				
DIMENSIONS: mm [INCHES]		CHK RAMESH KIVADER 13JUN2024					
	TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD P.S. SATHISH 13JUN2024	NAME			
	0. PLC ± 0.10		PRODUCT SPEC				
	1. PLC ± 0.25		RJ45 1X1 Tab_Up/Through Hole 10/100/1000Base-T W/LED —				
	2. PLC ± 0.25						
	3. PLC ± 0.25						
4. PLC ± 0.25		APPLICATION SPEC					
ANGLES ± 0.25		WEIGHT		SIZE	CAGE CODE	DRAWING NO	RESTRICTED TO
FINISH		0		A1	00779	C=2489120	—
MATERIAL		CUSTOMER DRAWING		SCALE		SHEET	REV
—		—		1:1		1 OF 2	A

REVISIONS					
P	T	LTR	DESCRIPTION	DATE	DWN
—	—	—	SEE SHEET 1	—	—

Schematic



PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	0°C To +70°C
TURNS RATIO	1:1±2%
INDUCTANCE(OCL)	350uH MIN@100KHz/100mV With 8mA DC Bias
INSERTION LOSS	-1.0dB MAX@0.3MHz-100MHz;
RETURN LOSS (Z out=100 OHM)	-18dB MIN@1MHz-30MHz; -16dB MIN@30MHz-60MHz; -12dB MIN@60MHz-80MHz; -10dB MIN@80MHz-100MHz;
CROSSTALK (ADJACENT CHANNELS)	-30dB MIN@1MHz-100MHz;
COMMON MODE REJECTION RATIO	-30dB MIN@1MHz-100MHz;
HI-POT	2250 VDC@60 SECONDS

LED SPECIFICATION			
STANDARD LED	WAVELENGTH	Farward V(max)	TYP
GREEN	570nm	2.4V	2.2V

*WITH A FORWARD CURRENT OF 20mA

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN RAMESH_KIVADER 13JUN2024	TE Connectivity
		CHK RAMESH_KIVADER 13JUN2024	
DIMENSIONS: mm [INCHES]	TOLERANCES UNLESS OTHERWISE SPECIFIED:	APVD P.S. SATHISH 13JUN2024	NAME
	0. PLC ± 0.10 1. PLC ± 0.25 2. PLC ± 0.25 3. PLC ± — 4. PLC ± — ANGLES ± —	PRODUCT SPEC	RJ45 1X1 Tab_Up/Through Hole 10/100/1000Base-T W/LED
	FINISH —	APPLICATION SPEC	—
MATERIAL —		WEIGHT —	SIZE CAGE CODE DRAWING NO. RESTRICTED TO
		CUSTOMER DRAWING	A1 00779 C=2489120 —
		SCALE 1:1	SHEET 2 OF 2 REV A

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[TE Connectivity:](#)

[2489120-1](#)