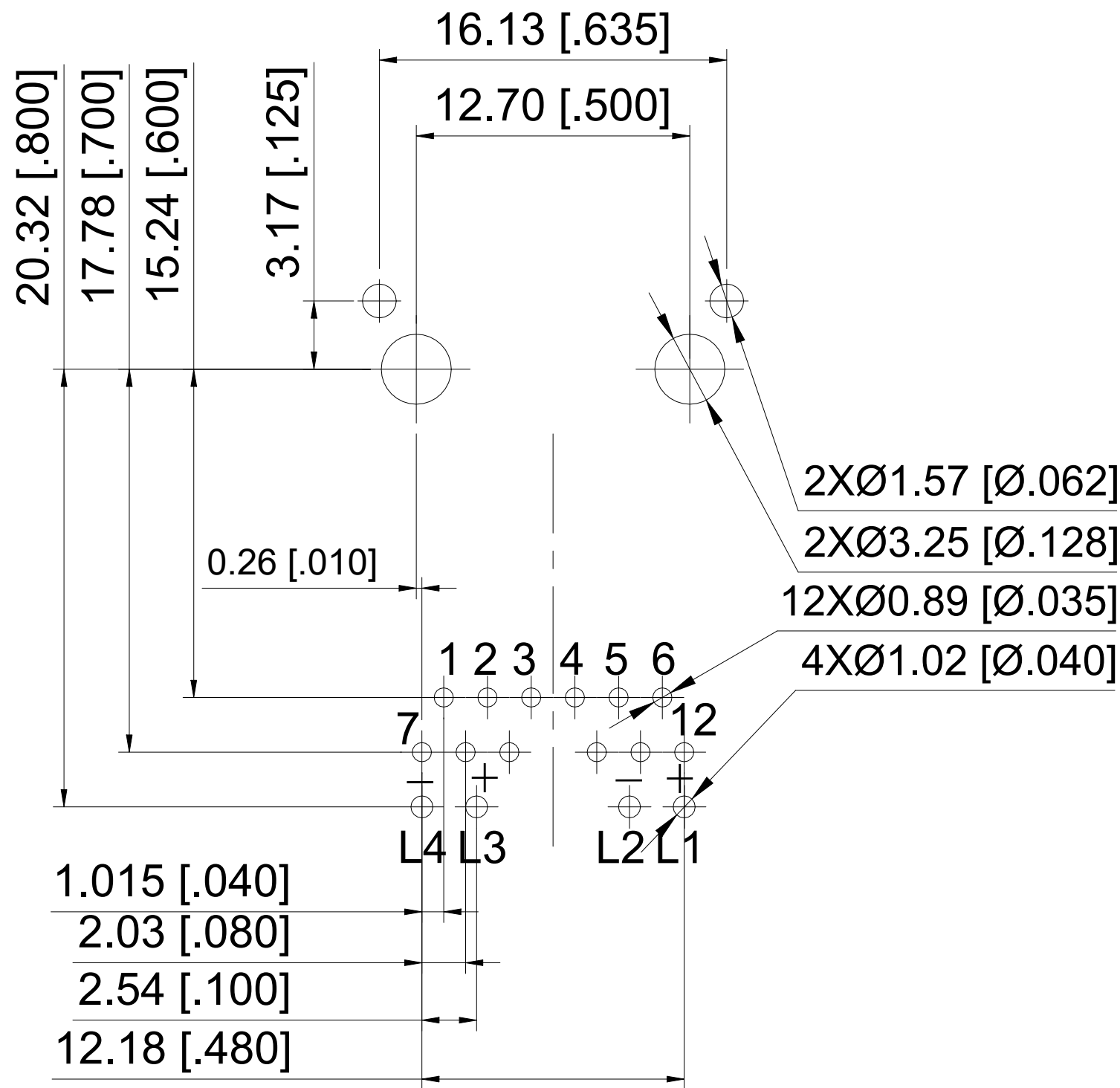
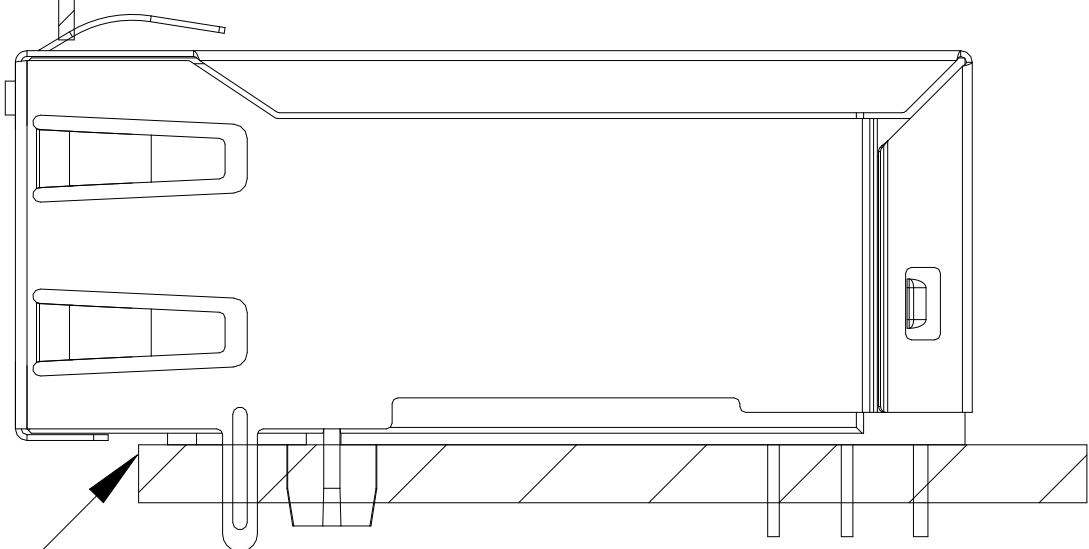
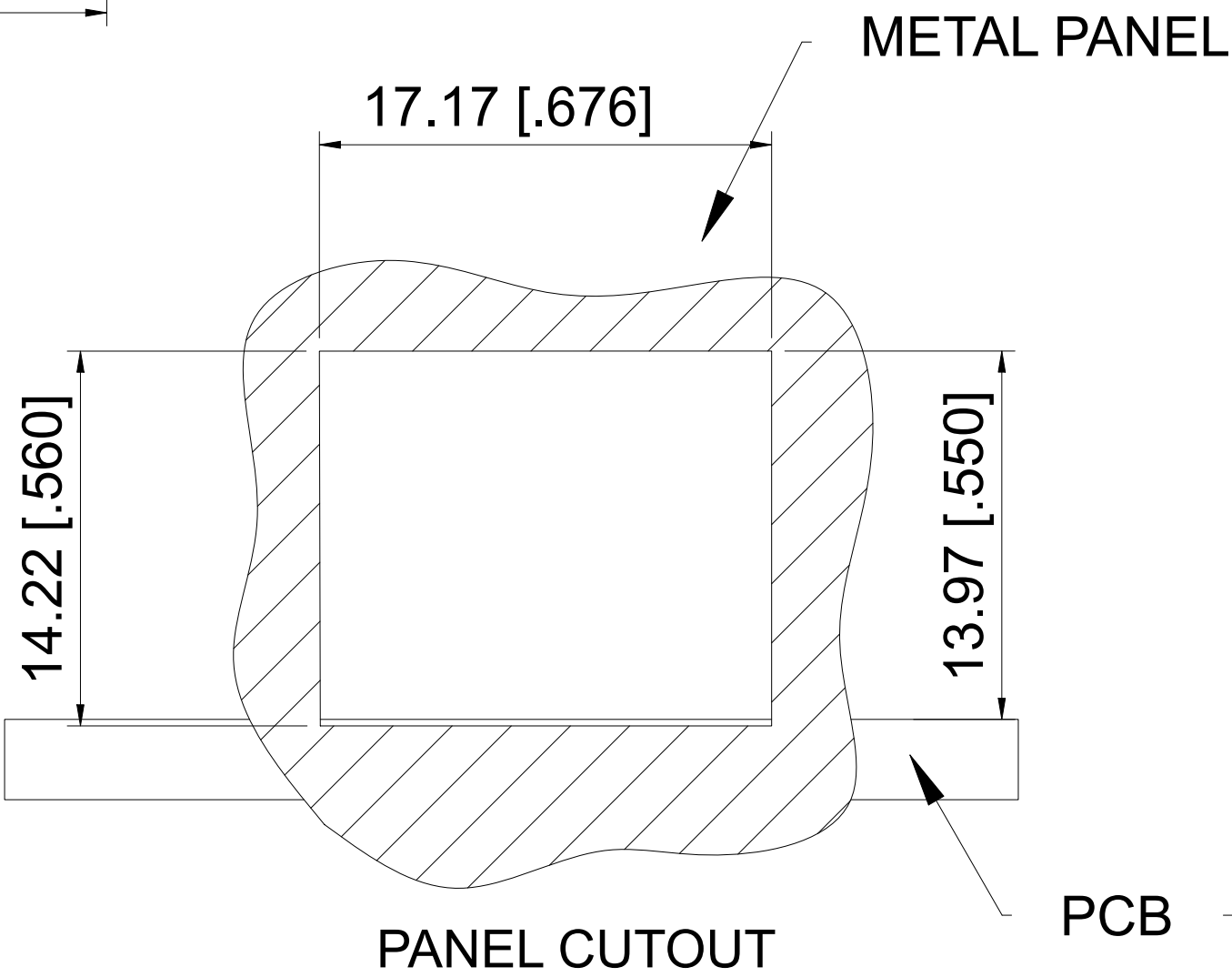
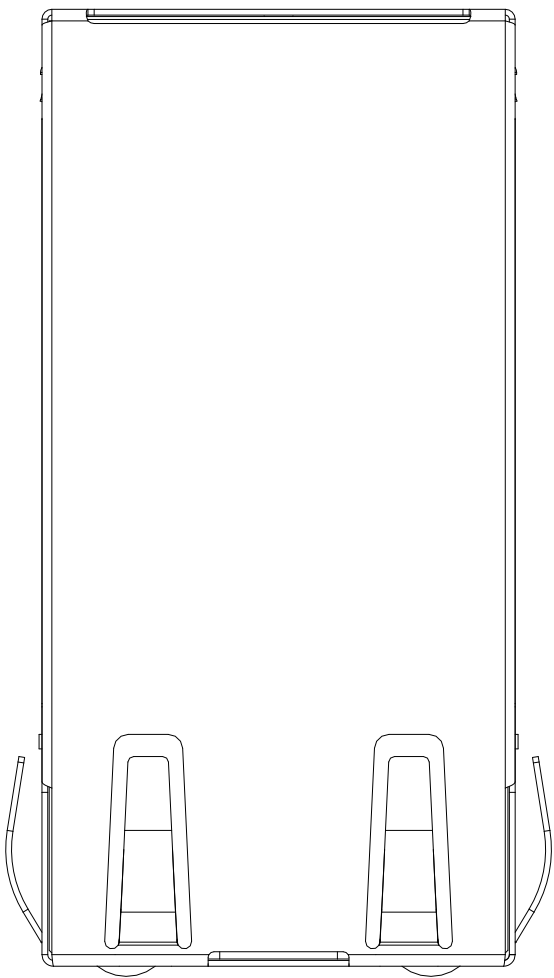
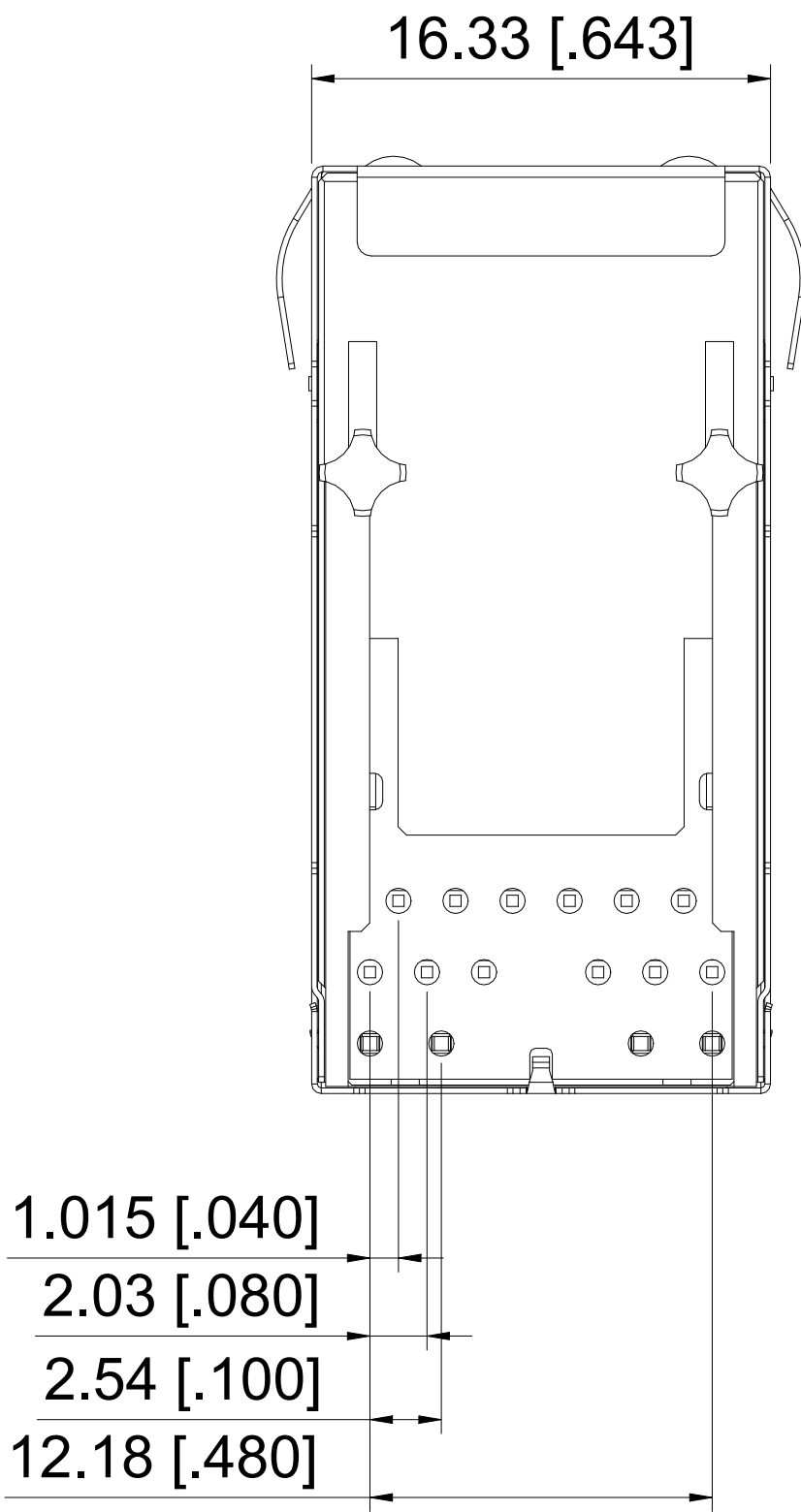
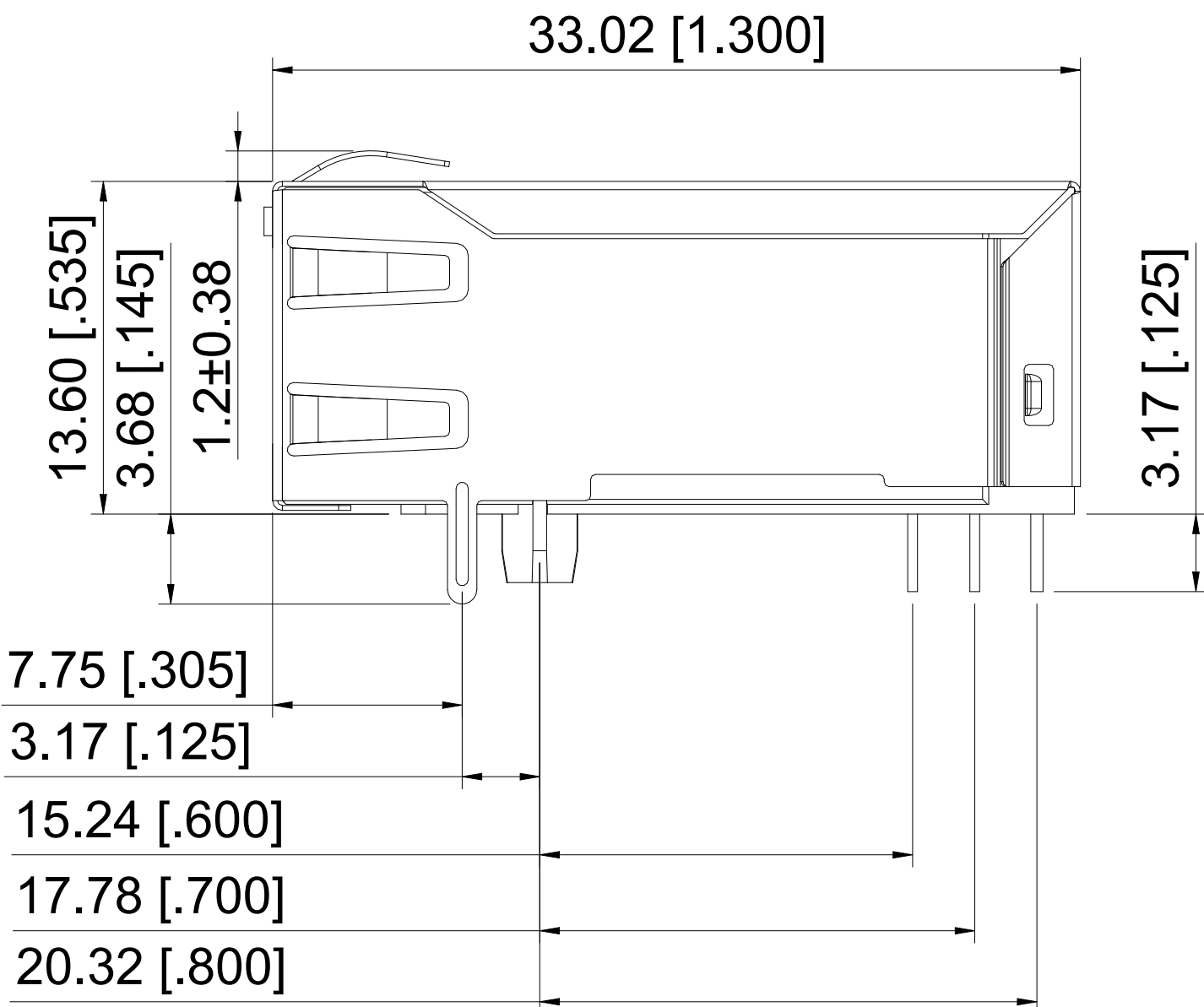
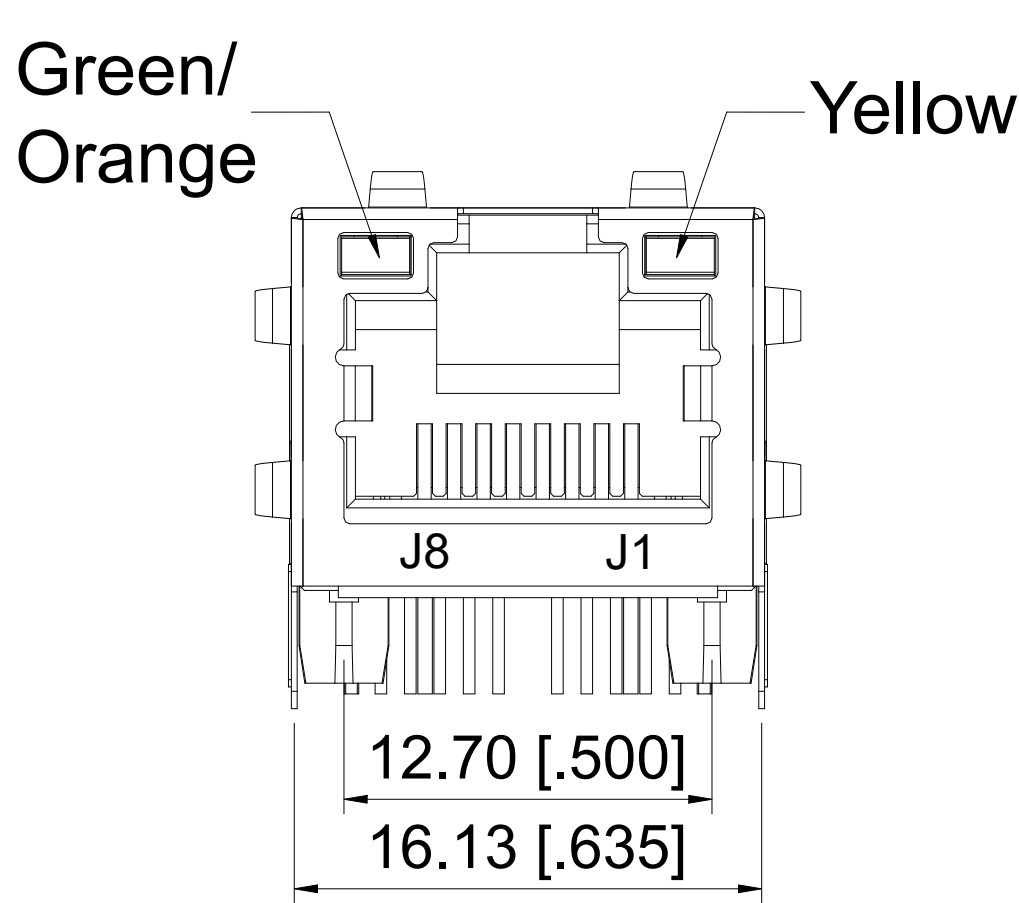


REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
A		INITIAL RELEASE	11JUL2024	RK	PSS

NOTES:

- 1.HOUSING MATERIAL:GLASS FILLED PA46(BLACK) UL94V-0(ROHS)
- 2.CONTACT: PHOSPHOR BRONZE,50 MICROINCHES MIN. OVERALL DUCTILE NICKEL UNDERPLATE WITH SELECTED (REFER P/N TABLE) MICROINCHES GOLD AT MATING INTERFACE
- 3.SHIELD:0.2mm THICKNESS WITH BRASS.
- 4.JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68 SUBPART F
- 5.THE PART IS RECOMMENDDDED FOR WAVE SOLDERING PROCESS PEAK SOLDERING TEMPERATURE IS 260°C MAX,10 SECS MAX.
- 6.OPERATING TEMPERATURE:-40°C TO +85°C.
STORAGE TEMPERATURE:-40°C TO +85°C.



RECOMMENDED P.C.B LAYOUT
TOP VIEW (COMPONENT SIDE)

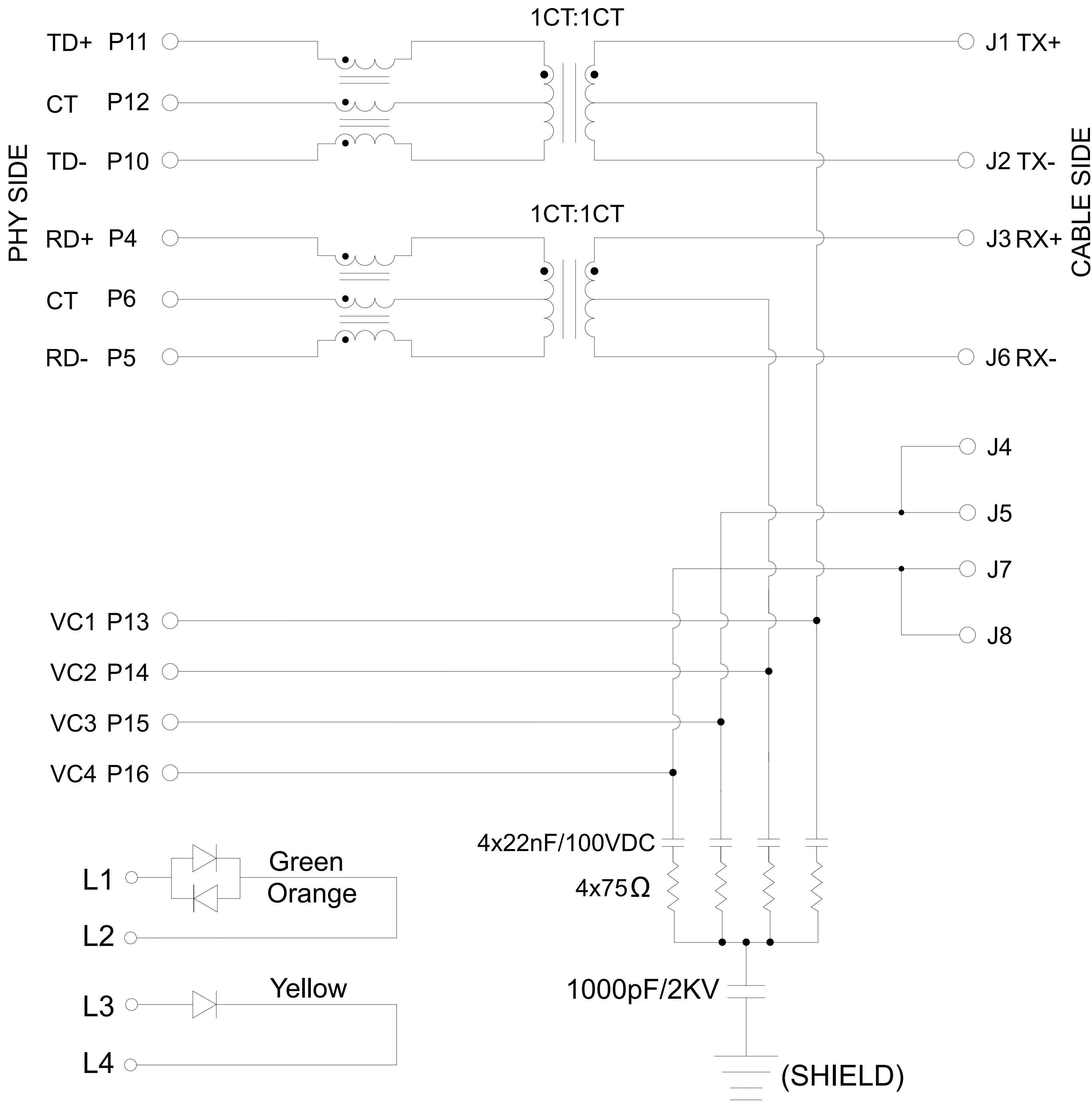
Dimension Tolerance:
All PCB LAYOUT
Tolerance:±0.05[.002]

0.04572 μm [1.8μ"]	2489011-6
0.0762 μm [3μ"]	2489011-5
0.1524 μm [6μ"]	2489011-4
0.381 μm [15μ"]	2489011-3
0.762 μm [30μ"]	2489011-2
1.27 μm [50μ"]	2489011-1
GOLD PLATING THICKNESS PART NUMBER	

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN RAMESH KIVADER 13JUN2024	 TE Connectivity				
		CHK RAMESH KIVADER 13JUN2024					
DIMENSIONS: mm [INCHES]		APVD PS SATHISH 13JUN2024	NAME RJ45 1X1 Tab_Up/Through Hole/ PoE+ 10/100Base-T W/LED —	SIZE A1	CAGE CODE 00779	DRAWING NO C2489011	RESTRICTED TO —
		PRODUCT SPEC					
TOLERANCES UNLESS OTHERWISE SPECIFIED: 0. PLC ± 0.10 1. PLC ± 0.25 2. PLC ± 0.25 3. PLC ± — 4. PLC ± — ANGLES ± — FINISH		APPLICATION SPEC					
MATERIAL —		WEIGHT 0					
CUSTOMER DRAWING			SCALE 1:1		SHEET 1 OF 2		REV A

REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
	—	SEE SHEET 1	—	—	—

Schematic



PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	-40°C To +85°C
TURNS RATIO	1:1±2%
OPEN CIRCUIT INDUCTANCE(OCL)	220uH MIN@100KHz/100mV With 15mA DC Bias For(CHANNEL1,2) 350uH MIN@100KHz/100mV With 8mA DC Bias For(CHANNEL1,2)
INSERTION LOSS(IL)	-1.2dB MAX@1MHz-100MHz;
RETURN LOSS(RL) (Z out=100 OHM)	-18dB MIN@1MHz-30MHz; -16dB MIN@30MHz-60MHz; -12dB MIN@60MHz-80MHz;-10dB MIN@80MHz-100MHz;
CROSSTALK (ADJACENT CHANNELS)	-30dB MIN@1MHz-100MHz;
COMMON MODE REJECTION RATIO(CMRR)	-30dB MIN@1MHz-100MHz;
DC CURRENT/VOLTAGE RATING-PSE PINS	720mA MAXIMUM@57VDC(CONTINUOUS) For PSE Channel (P13 & P14, P15 & P16)
HI-POT	2250 VDC@60 SECONDS

LED SPECIFICATION			
STANDARD LED	WAVELENGTH	Farward V(max)	TYP
GREEN	565nm	2.4V	2.2V
YELLOW	590nm	2.5V	2.1V
ORANGE	605nm	2.5V	2.1V

*WITH A FORWARD CURRENT OF 20mA

THIS DRAWING IS A CONTROLLED DOCUMENT.

DIMENSIONS:
mm (INCHES)

MATERIAL
—

TOLERANCES UNLESS OTHERWISE SPECIFIED:

0. PLC ± 0.10

1. PLC ± 0.25

2. PLC ± 0.25

3. PLC ± 0.25

4. PLC ± 0.25

ANGLES ± 2

FINISH —

DWN
RAMESH KIVADER
13JUN2024

CHK
RAMESH KIVADER
13JUN2024

APVD
P.S. SATHISH
13JUN2024

PRODUCT SPEC

APPLICATION SPEC

WEIGHT
—

CUSTOMER DRAWING

TE Connectivity

NAME
RJ45 1X1 Tab_Up/Through Hole/ PoE+
10/100Base-T W/LED
—

SIZE
A1

CAGE CODE
00779

DRAWING NO
2489011

RESTRICTED TO
—

SCALE
1:1

SHEET
2

OF
2

REV
A

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[TE Connectivity:](#)

[2489011-1](#)