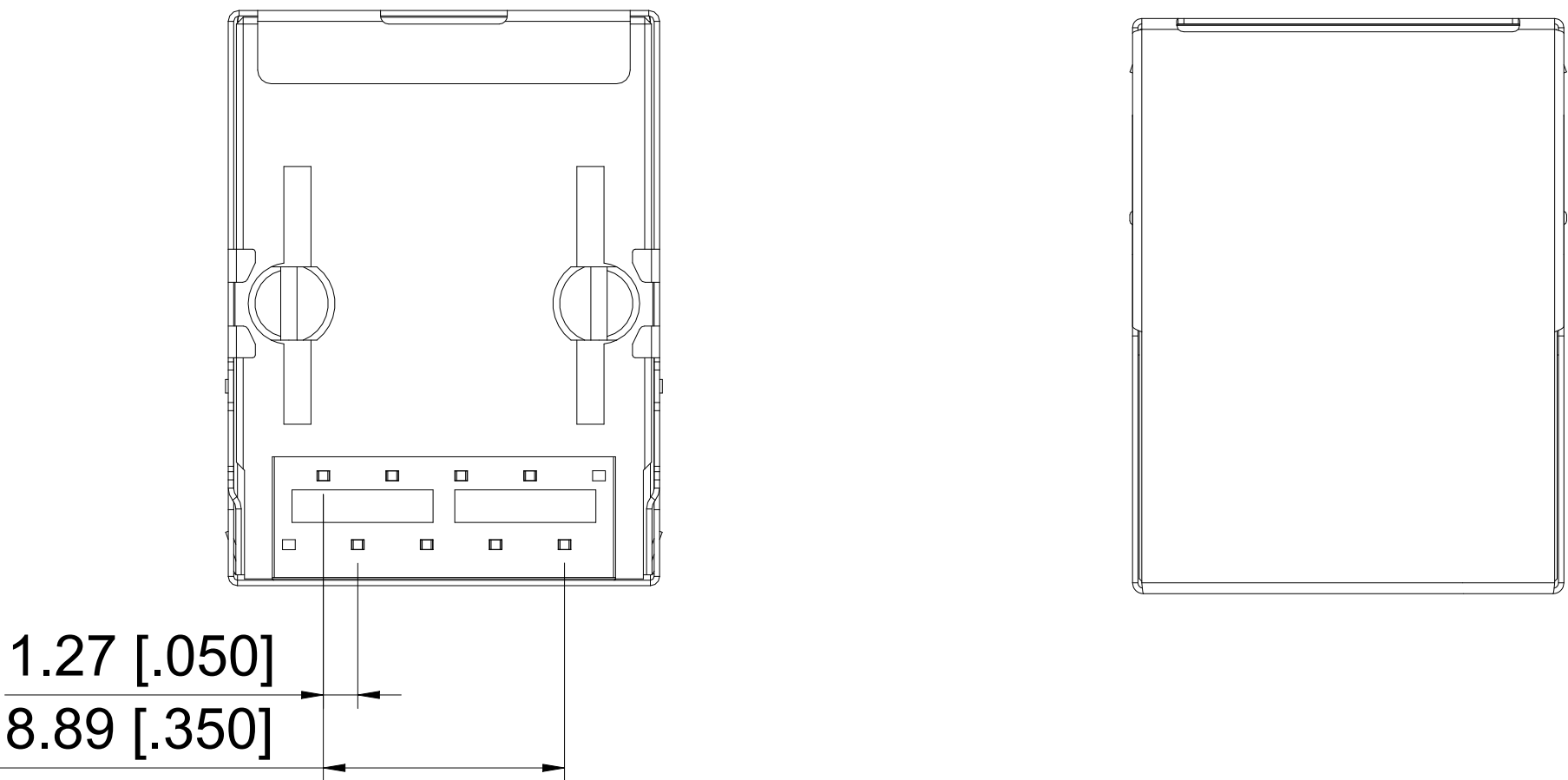
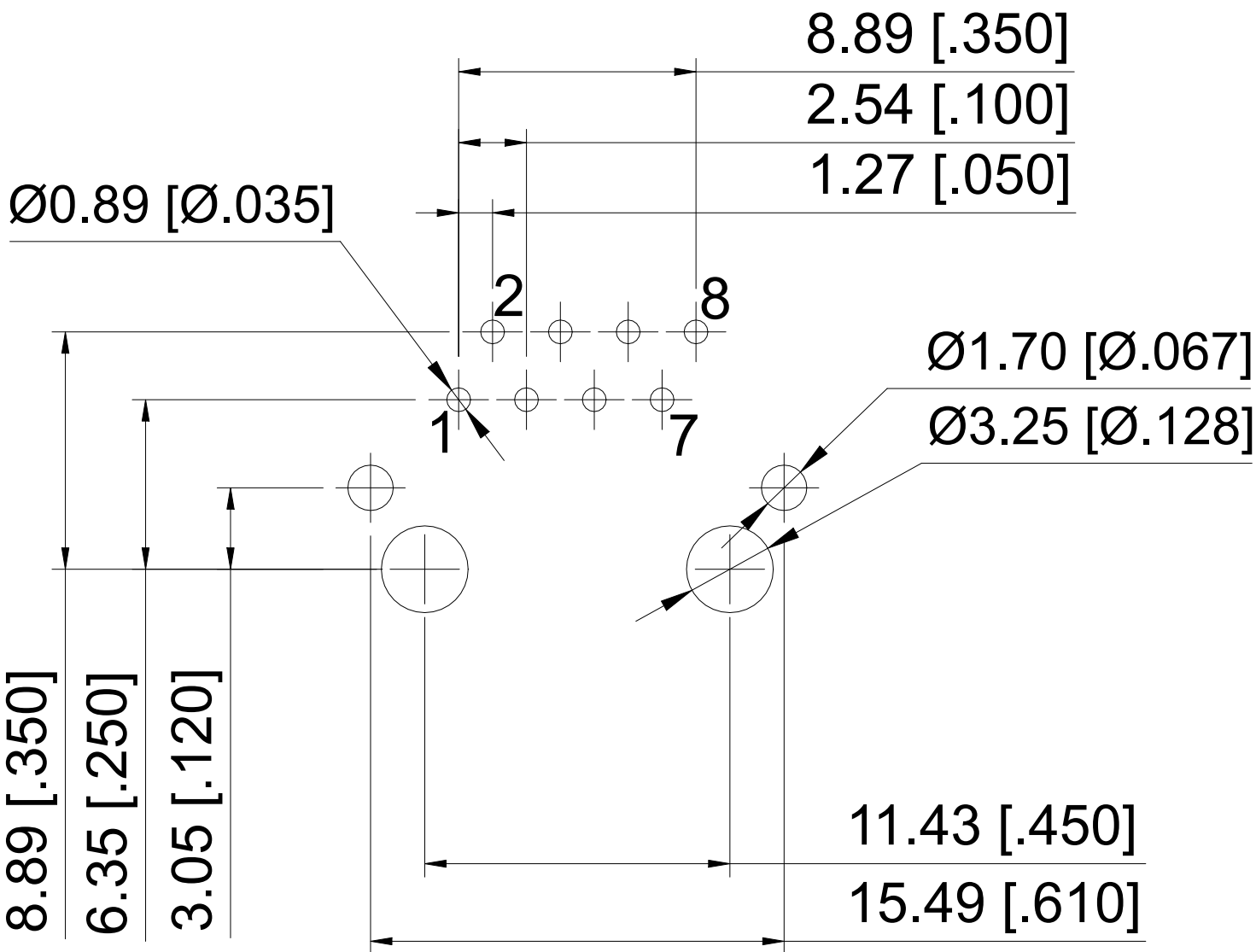
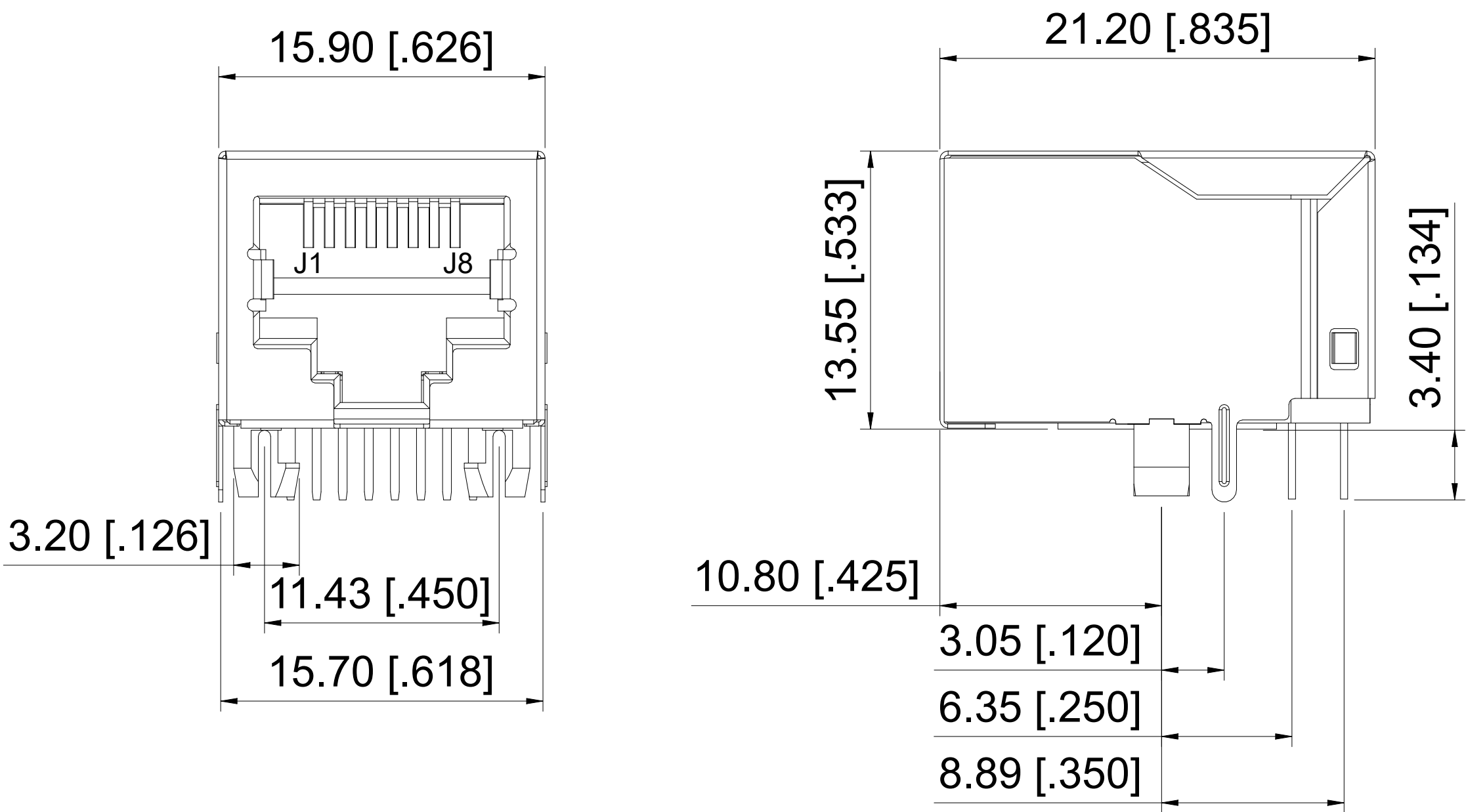


REVISIONS					
P	T	LTR	DESCRIPTION	DATE	DWN APVD
		A	INITIAL RELEASE	11JUL2024	RK PSS

NOTES:

- 1.HOUSING MATERIAL:GLASS FILLED PA46(BLACK) UL94V-0(ROHS)
- 2.CONTACT: PHOSPHOR BRONZE,50 MICROINCHES MIN. OVERALL DUCTILE NICKEL UNDERPLATE WITH SELECTED (REFER P/N TABLE) GOLD AT MATING INTERFACE
- 3.SHIELD:0.2mm THICKNESS WITH BRASS.
- 4.JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68 SUBPART F
- 5.THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS PEAK SOLDERING TEMPERATURE IS 260°C MAX,10 SECS MAX.
- 6.OPERATING TEMPERATURE:0°C TO +70°C.
STORAGE TEMPERATURE:-40°C TO +85°C.



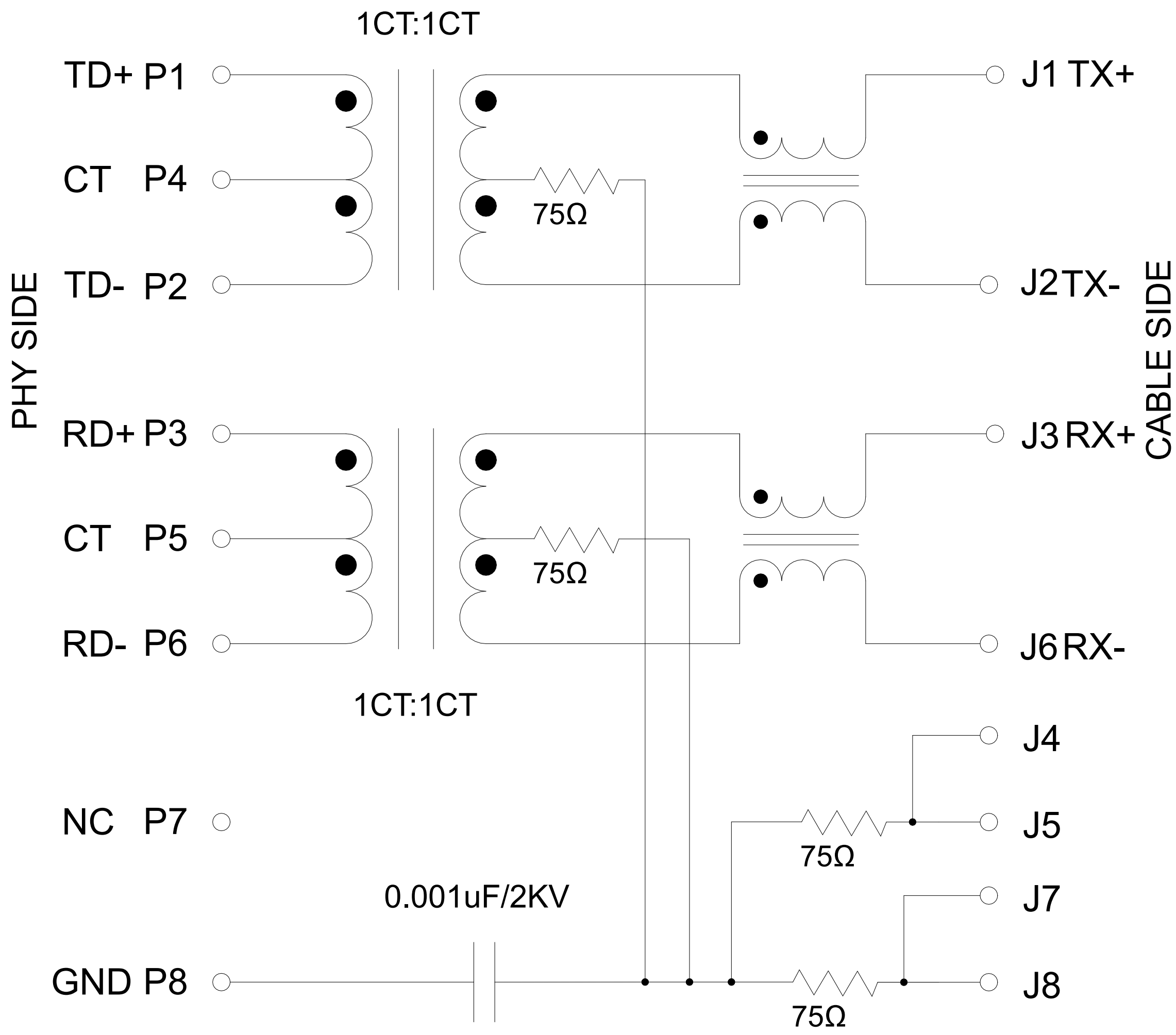
RECOMMENDED P.C.B LAYOUT
TOP VIEW (COMPONENT SIDE)

Dimensions Tolerance:
All PCB LAYOUT
Tolerances:±0.05[.002]

GF	2488965-6
0.0762 μm [3μ"]	2488965-5
0.1524 μm [6μ"]	24889654-
0.381 μm [15μ"]	2488965-3
0.762 μm [30μ"]	2488965-2
1.27 μm [50μ"]	2488965-1
GOLD PLATING THICKNESS	PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN RAMESH_KIVADER 13JUN2024	TE Connectivity	
		CHK RAMESH_KIVADER 13JUN2024		
DIMENSIONS: mm [INCHES]	TOLERANCES UNLESS OTHERWISE SPECIFIED:	APVD P.S. SATHISH 13JUN2024	NAME	RJ45 1X1 Tab_Down/Through Hole 10/100Base-T W/O LED
	0. PLC ± 0.10 1. PLC ± 0.25 2. PLC ± 0.25 3. PLC ± ~ 4. PLC ± ~ ANGLES ± ~ FINISH	PRODUCT SPEC	APPLICATION SPEC	RESTRICTED TO
		WEIGHT 0	SIZE A1 CAGE CODE 00779 DRAWING NO. 2488965	—
MATERIAL —	FINISH —	CUSTOMER DRAWING		
		SCALE 1:1	SHEET 1 OF 2	REV A

Schematic



PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	0°C To +70°C
URNS RATIO	1:1±2%
INDUCTANCE(OCL)	350uH MIN@100KHz/100mV With 8mA DC Bias
INSERTION LOSS	-1.0dB MAX@0.3MHz-100MHz;
RETURN LOSS (Z out=100 OHM)	-18dB MIN@1MHz-30MHz; -16dB MIN@30MHz-60MHz; -12dB MIN@60MHz-80MHz;-10dB MIN@80MHz-100MHz;
CROSSTALK (ADJACENT CHANNELS)	-30dB MIN@1MHz-100MHz;
COMMON MODE REJECTION RATIO	-30dB MIN@1MHz-100MHz;
HI-POT	2250 VDC@60 SECONDS

Mouser Electronics

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Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[TE Connectivity:](#)

[2488965-3](#)