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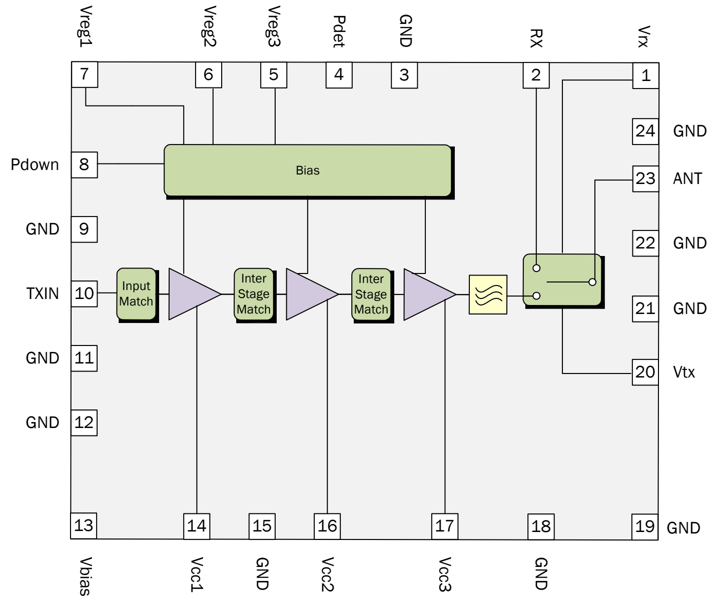
RFFM4202

5.0V, 2.4GHz TO 2.5GHz HIGH POWER FRONT END MODULE

Package: 6mm x 6mm Laminate



Functional Block Diagram

Features

- 34dB Typical Gain Across Frequency Band
- P_{OUT} = 27.5dBm <2.5% Dynamic EVM
- High Impedance Power Control
- 1x1 MIMO architecture
- Integrated 3-stage PA, filtering, and T/R switch
- Integrated Power Detector

Applications

- WiFi IEEE802.11b/g/n Applications
- Customer Premises Equipment (CPE)
- Spread-Spectrum and MMDS Systems

Product Description

RFFM4202 is a 1 x 1 MIMO module that is intently specified to address IEEE 802.11b/g/n WiFi 2.4GHz to 2.5GHz customer premises equipment (CPE) applications. The module has an integrated three-stage linear power amplifier, Tx harmonic filtering and SPDT switch. The RFFM4202 has fully matched input and output for a 50Ω system and incorporates matching networks optimized for linear output power and efficiency. The RFFM4202 is housed in a 6mm x 6mm laminate.

Ordering Information

RFFM4202SB	5 Piece Bag
RFFM4202SQ	25 Piece Bag
RFFM4202SR	100 Piece Reel
RFFM4202TR7	2500 Piece reel
RFFM4202PCK-410	RFFM4202 Evaluation Board and 5 Piece Bag

Optimum Technology Matching® Applied

☐ GaAs HBT	☐ SiGe BiCMOS	☑ GaAs pHEMT	☐ GaN HEMT
☐ GaAs MESFET	☐ Si BiCMOS	☐ Si CMOS	☐ BiFET HBT
☑ InGaP HBT	☐ SiGe HBT	☐ Si BJT	☐ LDMOS

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Absolute Maximum Ratings

Parameter	Rating	Unit
Supply Voltage (RF Applied)	-0.5 to +5.25	V
Supply Voltage (No RF Applied)	-0.5 to +6.0	V
DC Supply Current (RMS)	1200	mA
Input RF Power with 50Ω Output Load.	+10	dBm
Maximum VSWR with no Damage	10:1	
Operating Ambient Temperature	-40 to +85	°C
Storage Temperature	-40 to +150	°C
Maximum Juction Temperature $T_{J\text{-}MAX}$	175	°C
Moisture Sensitivity	MSL3	

Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

RFMD Green: RoHS compliant per EU Directive 2002/95/EC, halogen free per IEC 61249-2-21, < 1000 ppm each of antimony trioxide in polymeric materials and red phosphorus as a flame retardant, and <2% antimony in solder.

Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Typical Conditions					T=25 °C, V_{CC}=5.0V, V_{REG}=2.9V, using an IEEE802.11g waveform, 54 MBps, unless otherwise noted
Tx Performance - 11g/n					Compliance with standard 802.11g/n
Frequency	2412		2484	MHz	
802.11n Output Power	27	27.5		dBm	802.11n HT20 and HT40 MCS7
802.11n Dynamic EVM		2.5	3	%	
802.11g Output Power	27.3	27.8		dBm	At V_{CC} = 5.25V; 802.11n HT20 and HT40 MCS7
802.11n Dynamic EVM		2.5	3	%	
802.11g Output Power	27.5	28		dBm	802.11g 64QAM 54MBps
802.11n Dynamic EVM		2.5	3	%	
Second Harmonic		-40	-32	dBm/MHz	11n HT40 MCS0/7 at rated P_{OUT}
Third Harmonic		-50	-40	dBm/MHz	11n HT40 MCS0/7 at rated P_{OUT}
Tx Performance - Spectral Mask					Compliance with standard 802.11b
802.11b Output Power		27.5		dBm	Meet 802.11b CCK 1MBps mask spec
802.11n Output Power	27	27.5		dBm	Meet 802.11n HT20/HT40 MSC7 mask
Tx Performance - Generic					
Gain	31.5	34	37	dB	At rated P_{OUT}
Gain variation over Temperature			+/-2.5	dB	Over temperature of -40 °C to +85 °C
Low Gain Mode - Gain Reduction		23		dB	Drop in gain vs. high gain mode by setting V_{REG2} = 0V
Power Detect Range	0.165		2.3	V	P_{OUT} = 5dBm to 30dBm
Power Detect Voltage		1.55		V	At rated P_{OUT}
Input Return Loss - Tx_in pin	10	15		dB	In specified frequency band
Output Return Loss at ANT Pin	6	8		dB	In specified frequency band
Operating Current		925	1050	mA	At rated P_{OUT}
Quiescent Current		510	600	mA	V_{CC} = 5.0V, V_{REG} = 2.9V, and RF = OFF
PAE (Power Added Efficiency)		17		%	At rated P_{OUT} (PA only)
I_{REG}		7	10	mA	In TX mode
P_{DOWN} Current - V_{REG} Supply		10	12.5	mA	P_{DOWN} = 0V, V_{REG} = 2.9V, V_{CC} = 5V
P_{DOWN} Current - V_{CC} Supply		1.4	2.5	mA	P_{DOWN} = 0V, V_{REG} = 2.9V, V_{CC} = 5V
Leakage Current		0.2	0.6	mA	V_{CC} = 5V, V_{REG} = 0V, P_{DOWN} = 0V

Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Tx Performance - Generic (continued)					T=25 °C, V_{CC}=5.0V, V_{REG}=2.9V, using an IEEE802.11g waveform, 54MBps, unless otherwise noted
Power Supply - V_{CC}		5	5.25	V	
Power Supply - V_{REG1}, V_{REG2}, V_{REG3}	2.75	2.85	2.95	V	
Turn-On Time from Setting of V_{REG} Values			400	nsec	Output stable to within 90% of final gain
Turn-Off Time from Setting of V_{REG} Values			800	nsec	Output stable to within 90% of final gain
Stability	-25		33.5	dBm	No spurs above -47dBm into 4:1 VSWR
CW P1dB		33.5		dBm	TX mode in 50% duty cycle
Rx Performance					
Rx Insertion Loss - Rx		0.8	1	dB	
Noise Figure		0.8	1	dB	In specified frequency band
Return Loss - Rx	10	16		dB	
Rx to ANT Isolation While in Tx Mode		30		dB	
Rx to Tx Isolation While in Tx mode	25	30		dB	
Generic Performance					
T/R Switching Time			0.5	μsec	
Voltage Logic High	2.75	2.9	3.4	V	
Voltage Logic Low	0		0.3	V	
Control Current - Logic High		1	10	μA	
Thermal					
R_{TH_I}		15		°C/Watt	
ESD					
Human Body Model	250			V	EIAJESD22-114A, RF pins
	500			V	EIAJESD22-114A, DC pins
Charge Device	500			V	JESD22-C101C, all pins

RFFM4202 Truth Table

Status	PDOWN	VTX	VRX
TX Mode	High	High	Low
RX Mode	Low	Low	High

Note: PDOWN and VTX can be tied together or controlled separately. If they are controlled separately, VTX should be 'On' before PDOWN in 'Turn On', and PDOWN should 'Off' before VTX in 'Turn Off''

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Pin Names and Description

Pin	Function	Description
1	VRX	Switch control for Rx mode
2	RX	RF Output is internally matched to 50Ω and DC blocked.
3	GND	Ground connection
4	PDET	Power detector provides an output voltage proportional to the RF output power level.
5	VREG3	Third stage bias voltage. This Pin requires regulated supply for best performance.
6	VREG2	Second stage bias voltage. This Pin requires regulated supply for best performance.
7	VREG1	First stage bias voltage. This Pin requires regulated supply for best performance.
8	PDOWN	Power down pin. Apply <0.3V_{DC} to power down the three power amplifier stages. Apply 1.75V_{DC} to 5.0V_{DC} to power up. If function is not desired, Pin may be connected to V_{REG}.
9	GND	Ground connection
10	TXIN	RF input is internally matched to 50Ω and DC blocked.
11	GND	Ground connection
12	GND	Ground connection
13	VBIAS	Supply voltage for the bias reference and control circuits.
14	VCC1	This pin is connected internally to the collector of the 1st stage RF device. To achieve specified performance, the layout of these pins should match the Recommended Land Pattern.
15	GND	Ground connection
16	VCC2	This pin is connected internally to the collector of the 2nd stage RF device. To achieve specified performance, the layout of these pins should match the Recommended Land Pattern.
17	VCC3	This pin is connected internally to the collector of the 3rd stage RF device. To achieve specified performance, the layout of these pins should match the Recommended Land Pattern.
18	GND	Ground connection
19	GND	Ground connection
20	VTX	Switch control for Tx mode
21	GND	Ground connection
22	GND	Ground connection
23	ANT	RF Output is internally matched to 50Ω and DC blocked.
24	GND	Ground connection
Pkg Base	GND	Ground connection

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Theory of Operation and Applications

Overview

The RFFM4202 is a single-chip integrated front end module (FEM) for high performance WiFi applications in the 2.4GHz to 2.5GHz ISM band. The FEM greatly reduces the number of external components minimizing footprint and assembly cost of the overall 802.11b/g/n solution. The RFFM4202 has an integrated b/g/n power amplifier, a power Detector, and Tx filtering and a Switch, which is capable of switching between WiFi Rx and WiFi Tx operations. The device is manufactured using InGaP HBT and pHEMT processes on a 6mmx6mmx0.95mm Laminate package. The module meets or exceeds the RF front end needs of the 802.11b/g/n WiFi RF systems. As the RFFM4202 is fully RF matched to 50Ω internally and requires minimal external components, it is very easy to implement on to PCB designs. To reduce the design and optimization process on the customer application, the evaluation board layout should be copied as close as possible, in particular the ground and via configurations. Gerber files of RFMD PCBA designs can be provided upon request. The supply voltage lines should present an RF short to the FEM by using bypass capacitors on the V_{CC} traces. To simplify bias conditions, the RFFM4202 requires a single positive supply voltage (V_{CC}), a positive current control bias (V_{REG}) supply or high impedance enable, and a positive supply for switch control. The built-in Power Detector of the RFFM4202 can be used as power monitor in the system. All inputs and outputs are internally matched to 50Ω.

Transmit Path

The RFFM4202 has a typical gain of 35dB from 2.4GHz to 2.5GHz, and delivers>27dBm typical output power in 11n HT20 MCS7 and>27.5dBm typical in 11g 54Mbps with an EVM<3%. The RFFM4202 requires a single positive of 5.0V to operate at full specifications. The VREG pin requires a regulated supply at 2.85V to maintain nominal bias current.

Out of Band Rejection

The RFFM4202 contains a low pass filter (LPF) to attenuate the 2nd Harmonics to -40dBm/MHz (typical). Depending upon the end-user's application, additional filters may be needed to meet the out of band rejection requirements of the system. For the system to meet FCC' s spec, a simple LC can be used between FEM and Antenna, for impedance matching and extra Harmonics attenuation to meet spec.

Receive Path

The Rx path has a 50Ω single-ended port. The Receive port return loss is 9.6dB minimum. In this mode, the FEM has an insertion loss of 0.8dB and 30dB (typical) isolation to Tx port.

RFFM4202 Biasing Instructions to the Eval board:

802.11b/g/n Transmit:

1. Connect the FEM to a signal generator at the input and a spectrum analyzer at the output. Set the Pin at signal generator is at -20dBm.
2. Bias V_{CC} to 5.0V first with V_{REG}=0.0V. If available, enable the current limiting function of the power supply to 1100mA.
3. Refer to switch operational truth table to set the control lines at the proper levels for WiFi Tx. It is recommended to maintain at least 2.85V on VTx during Tx mode. A lower VTx voltage will enable the switch in Tx mode, but 2.85V is needed to ensure that the switch stays in Tx mode during high power peaks. Using a VTx voltage less than 2.85V in Tx mode could result in abnormal operation or device damage.
4. Turn on V_{REG} to 2.85V (typ).
5. On V_{REG} (of evaluation board), a regulated supply is recommended. Be extremely careful not to exceed 3.0V on the V_{REG} pin or the part may exceed device current limits.
6. Turn on P_{DOWN} to 2.85V (typ). P_{DOWN} pin can be tied to V_{REG} supply. **NOTE: It is important to adjust the V_{CC} voltage source so that +5V is measured at the board; and the +2.85V of V_{REG} is measured at the board. The high collector currents will drop the collector voltage significantly if long leads are used. Adjust the bias voltage to compensate.**
7. Turn on RF of signal generator and gradually increase power level to the rated power. **CAUTION: If the input signal exceeds the maximum rated power, the RFFM4202 Evaluation Board can be permanently damaged.**
8. To turn off FEM, turn off RF power of signal generator; then P_{DOWN}, V_{REG} and V_{CC}.

802.11b/g/n Receive

- To receive WiFi set the switch control lines per the truth table.

General Layout Guidelines and considerations:

For best performance the following layout guidelines and considerations must be followed regardless of final use or configuration:

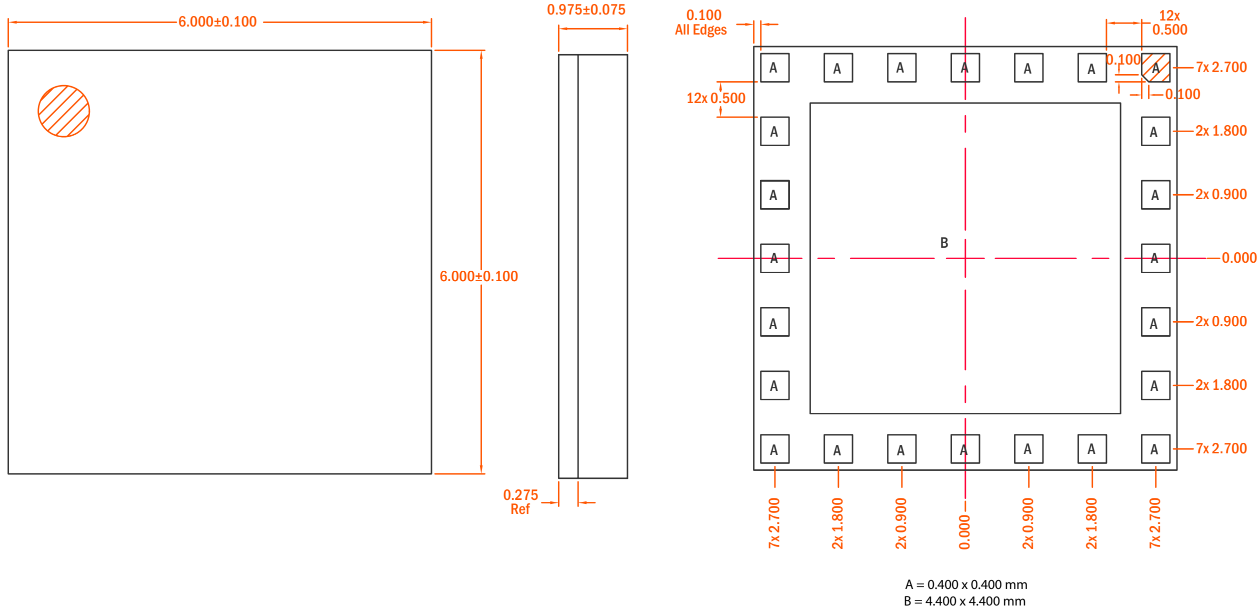
1. The ground pad of the RFFM4202 has special electrical and thermal grounding requirements. This pad is the main RF ground and main thermal conduct path for heat dissipation. The GND pad and vias pattern and size used on the RFMD evaluation board should be replicated. The RFMD layout files in Gerber format can be provided upon request. Ground paths (under device) should be made as short as possible.
2. The RF lines should be well separated with solid ground in between the traces to eliminate any possible RF leakages or cross-talking.
3. Bypass capacitors should be used on the DC supply lines. The V_{CC} lines may be connected after the RF bypass and decoupling capacitors to provide better isolation between each V_{CC} line.

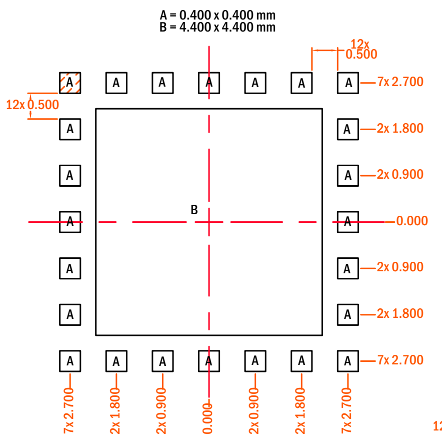
RFFM4202 Tx production and system calibration recommendation:

It is highly recommended to follow the DC biasing step and RF power settings in the production calibration or test:

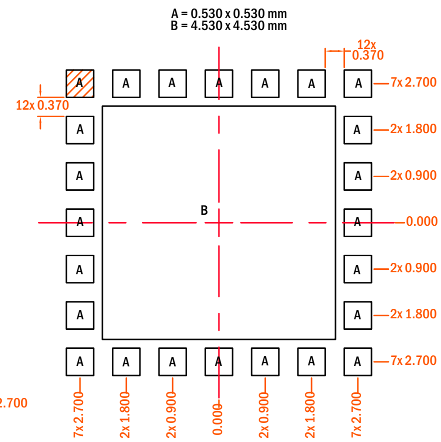
1. Connect the RF cables of input and output then connect to the proper equipment.
2. Apply V_{CC}, then V_{REG} as per the data sheet recommendations.
3. Set FEM in Tx mode by the truth table.
4. Apply P_{DOWN} = high.
5. Set RF input to the desired frequency and initial RF input power at -20dBm. This will insure the Power amplifier is in a linear state and not over driven.
6. Sweep RF from low to high output power and take measurements at the rated output power.
7. Insure that the output power at turn on doesn't saturate the power amplifier. The recommended output power should be about 10dB to 20dB below the nominal input power. Start calibrating from low to high power in reasonable steps until the rated power is reached then take the measurements. **CAUTION: If the input signal exceeds the maximum rated input power specifications, the RFFM4202 could be permanently damaged.**

Package Drawing

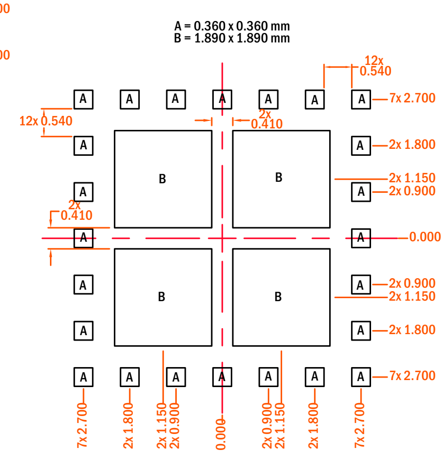




PCB Metal Land Pattern



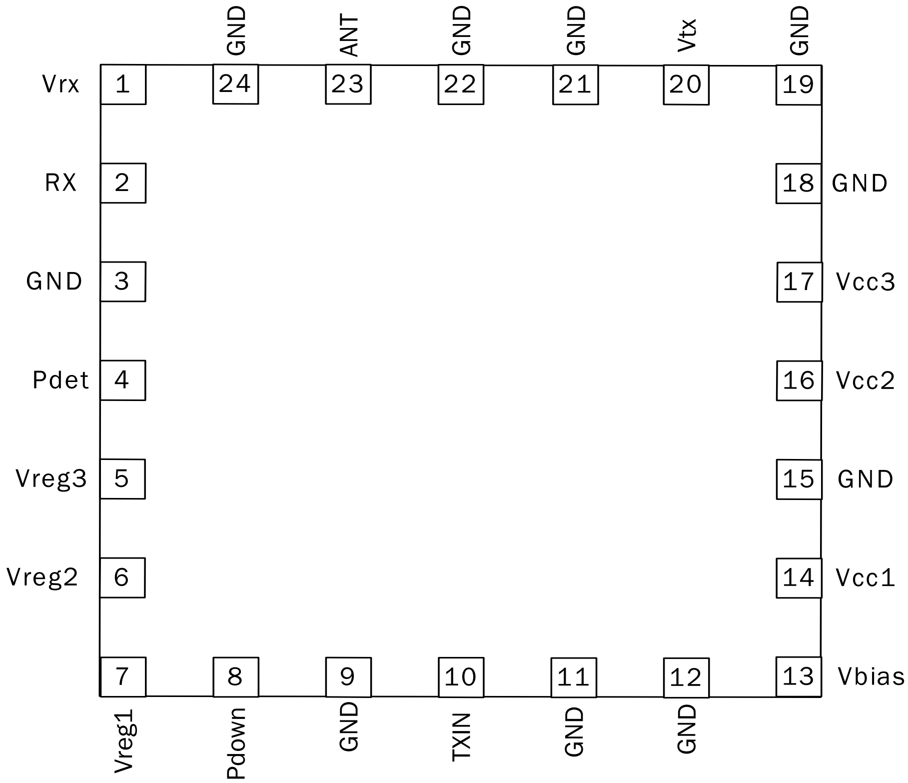
PCB Solder Mask Pattern



PCB Stencil Pattern

NOTES:
1. Thermal vias for center slug "B" should be incorporated into the PCB design. The number and size of thermal vias will depend on the application. Example of the number and size of vias can be found on the RFMD evaluation board layout.
2. Shaded area represents Pin 1 location.

Pin Out



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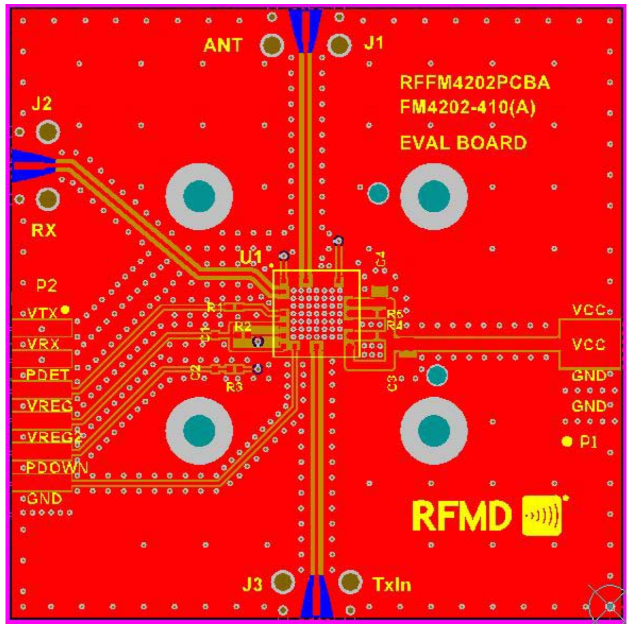
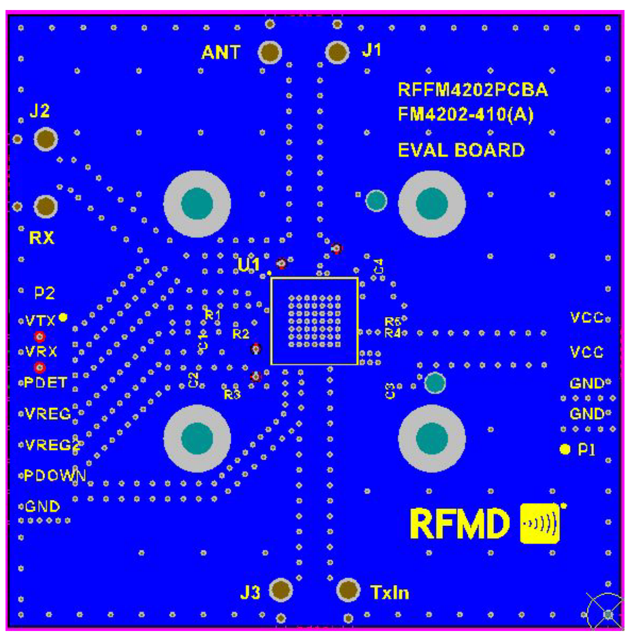
RFFM4202 Evaluation Board Top Layer



RFFM4202 Evaluation Board Bottom Layer



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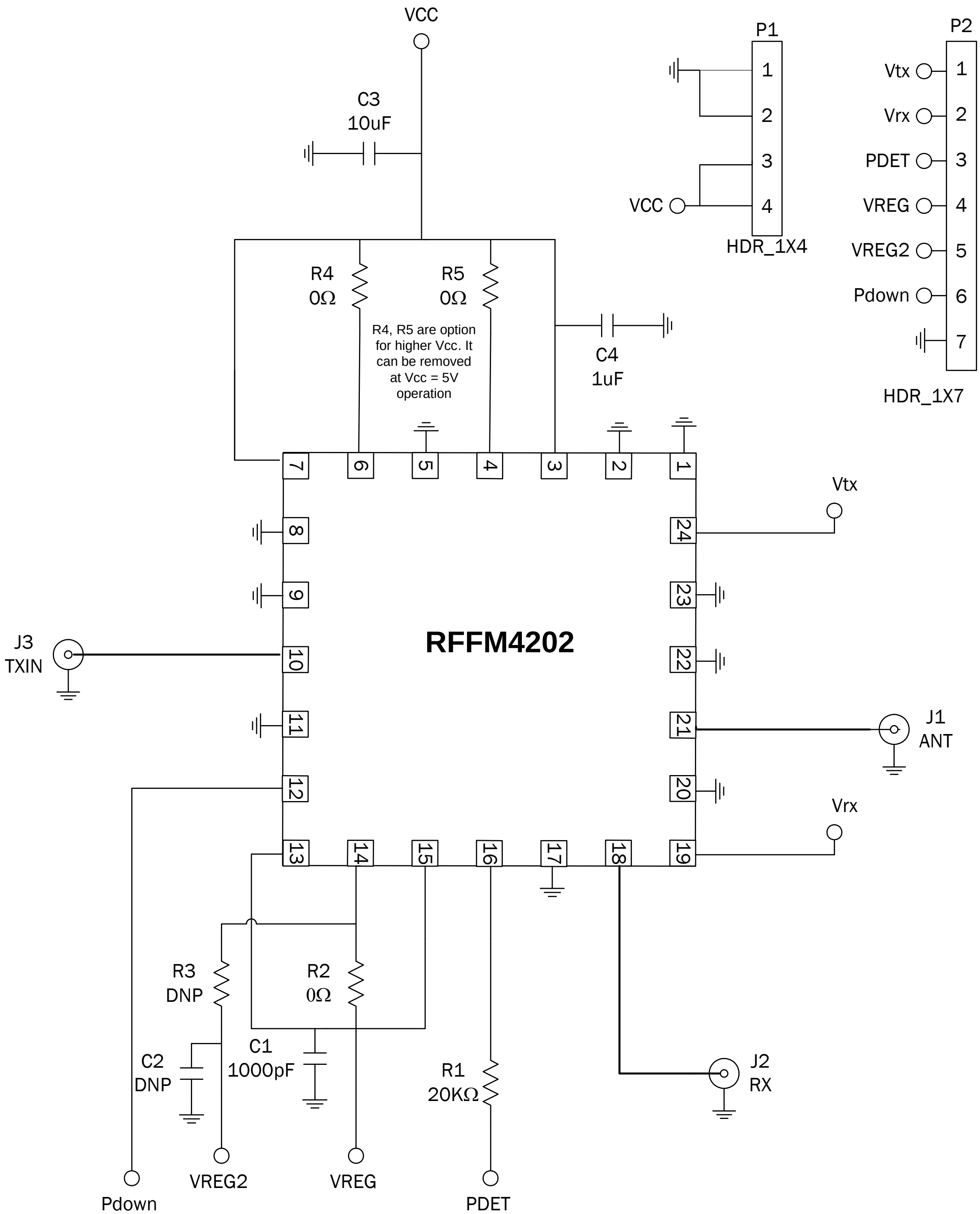
Evaluation Board Schematic



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Evaluation Board Bill of Materials (BOM)

Description	Qty	Reference Designator	Manufacturer	Manufacturer's P/N
CAP, 1000pF, 10% 50V, X7R,0402	1	C1	MURATA ELECTRONICS	GRM155R71H102KA01D
CAP, 1μF, 10%, 10V, X5R, 0603	1	C4	MURATA ELECTRONICS	GRM188R61A105KA61D
CAP, 10μF, 10%, 10V, X5R, 0805	1	C3	MURATA ELECTRONICS	RM21BR61A106KE19L
CONN, SMA, END LAUNCH, UNIV, HYB MNT, FLT	3	J1, J2, J3	MOLEX	SD-73251-4000
RES, 20KΩ , 5%, 1/16W, 0402	1	R1	PANASONIC INDUSTRIAL CO	ERJ-2GE-J203
RES, 0Ω, 0402	3	R2, R4, R5	KAMAYA, INC	RMC1/16SJPTH
DNI	2	R3, C2		
RFFM4202	1	U1	RFMD	RFFM4202

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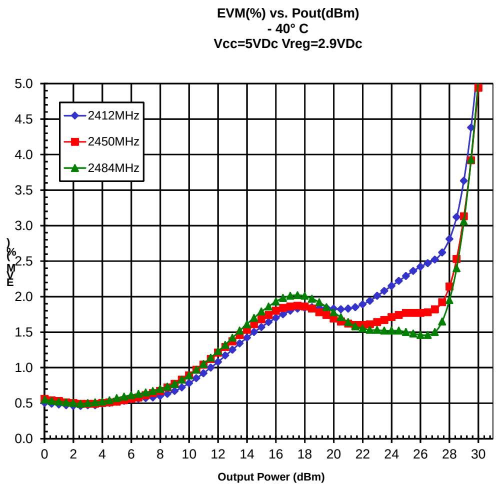
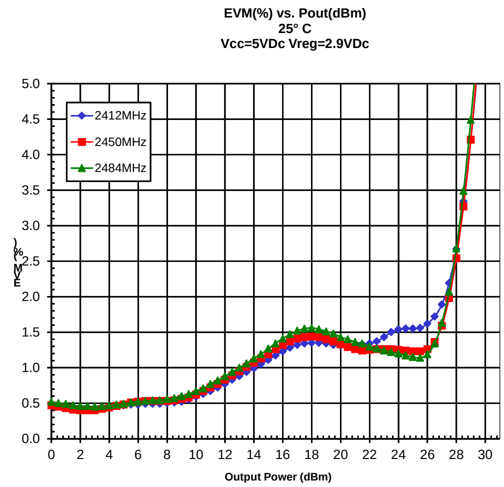
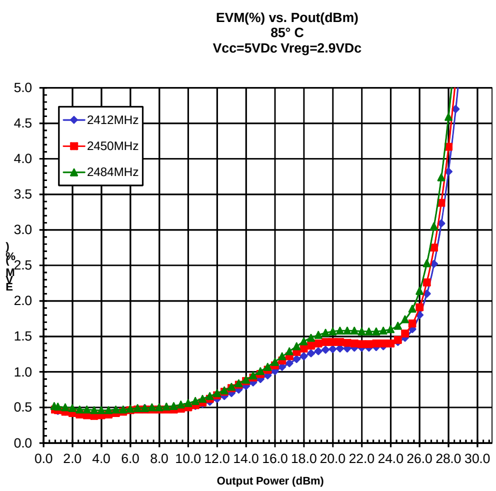
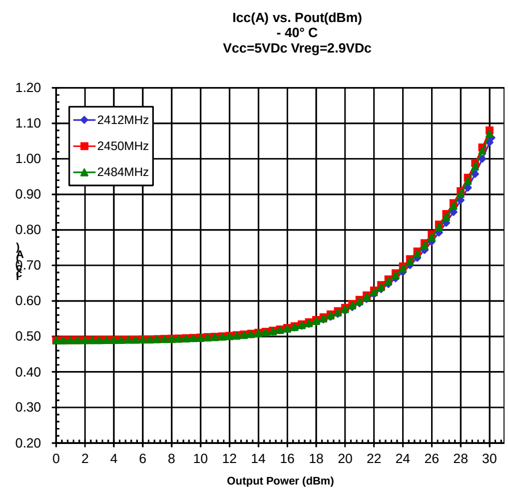
Typical Performance

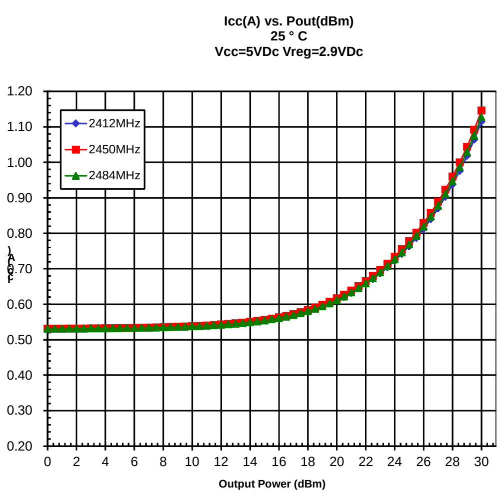
WiFi 802.11n HT20 MCS7 Performance Plots in 100% Duty Cycle

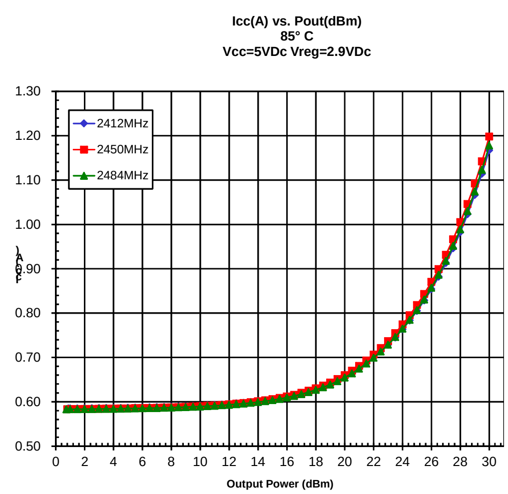












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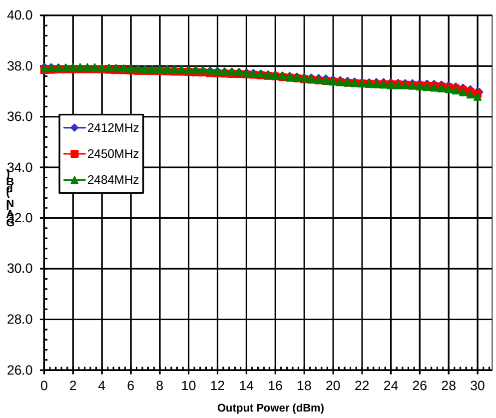
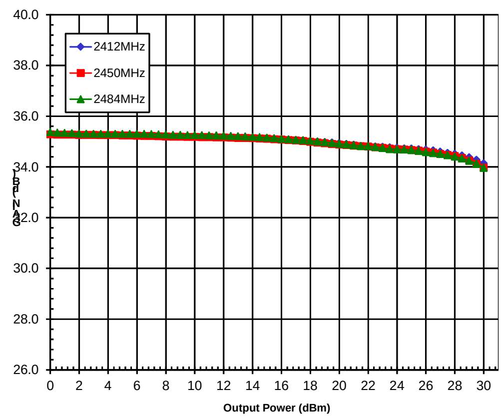
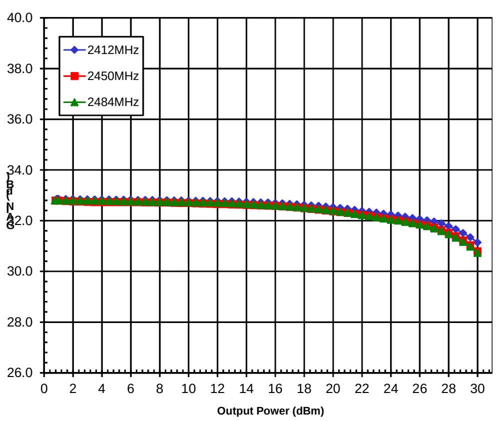
Gain(dB) vs. Pout(dBm)
- 40° C
Vcc=5VDc Vreg=2.9VDc



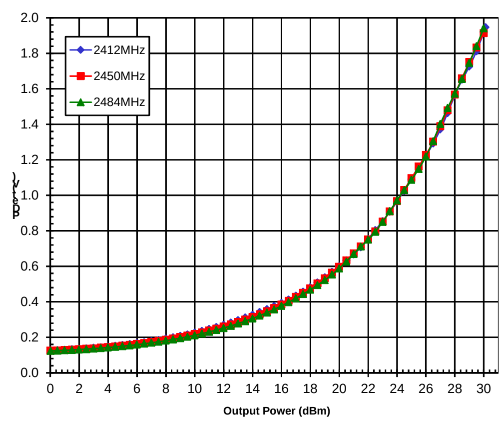
Gain(dB) vs. Pout(dBm)
25° C
Vcc=5VDc Vreg=2.9VDc



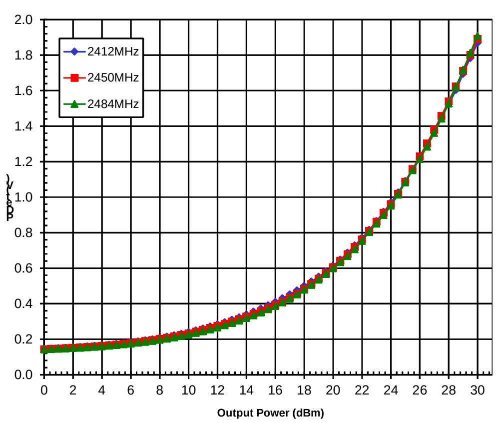
Gain(dB) vs. Pout(dBm)
85° C
Vcc=5VDc Vreg=2.9VDc



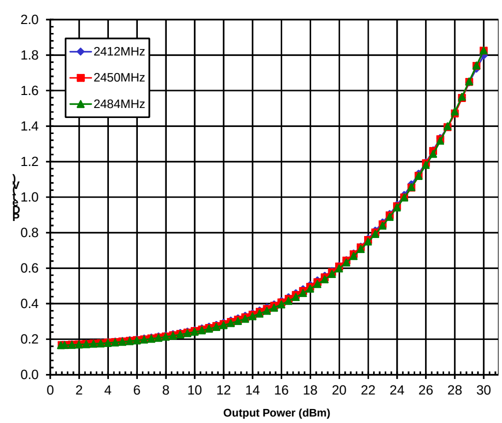
Power Detect (V) vs. Pout(dBm)
- 40° C
Vcc=5VDc Vreg=2.9VDc



Power Detect (V) vs. Pout(dBm)
25° C
Vcc=5VDc Vreg=2.9VDc



Power Detect (V) vs. Pout(dBm)
85° C
Vcc=5VDc Vreg=2.9VDc



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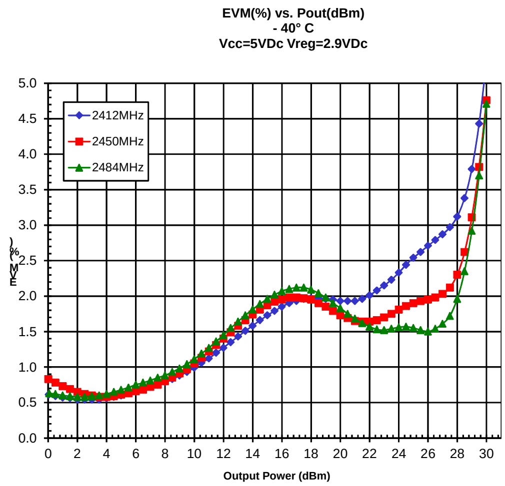
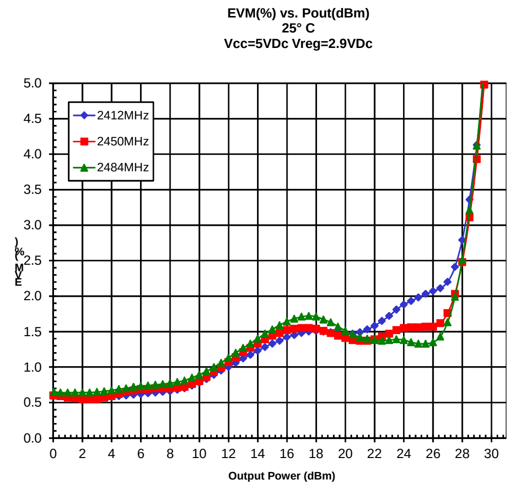
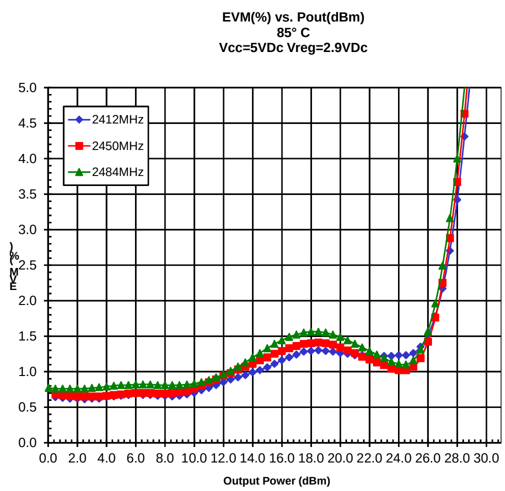
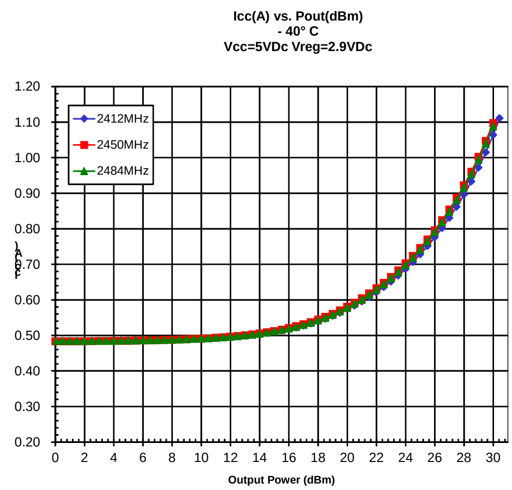
Typical Performance

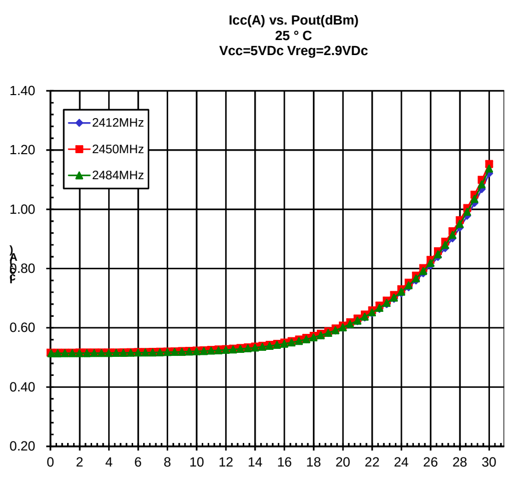
WiFi 802.11n HT20 MCS7 Performance Plots in 50% Duty Cycle

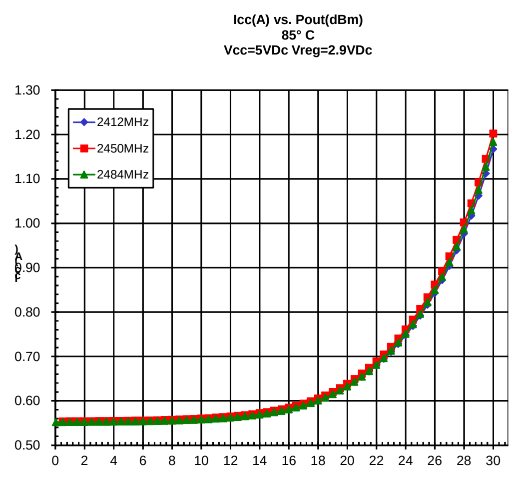




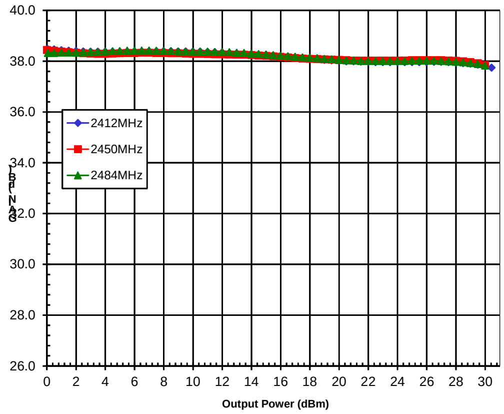




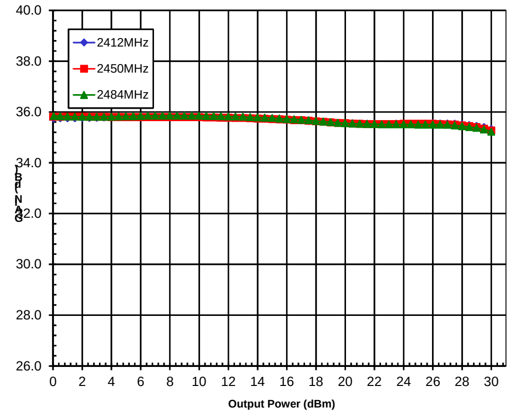




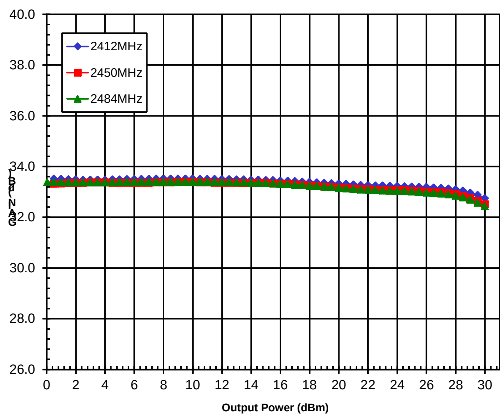
Gain(dB) vs. Pout(dBm)
- 40° C
Vcc=5VDc Vreg=2.9VDc



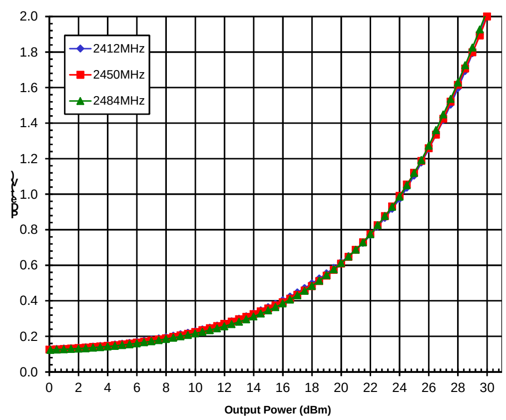
Gain(dB) vs. Pout(dBm)
25° C
Vcc=5VDc Vreg=2.9VDc



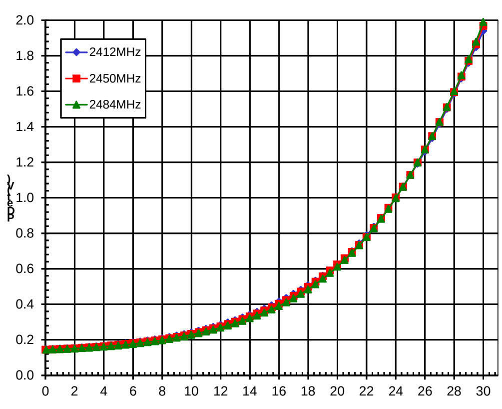
Gain(dB) vs. Pout(dBm)
85° C
Vcc=5VDc Vreg=2.9VDc



Power Detect (V) vs. Pout(dBm)
- 40° C
Vcc=5VDc Vreg=2.9VDc



Power Detect (V) vs. Pout(dBm)
25° C
Vcc=5VDc Vreg=2.9VDc



Power Detect (V) vs. Pout(dBm)
85° C
Vcc=5VDc Vreg=2.9VDc