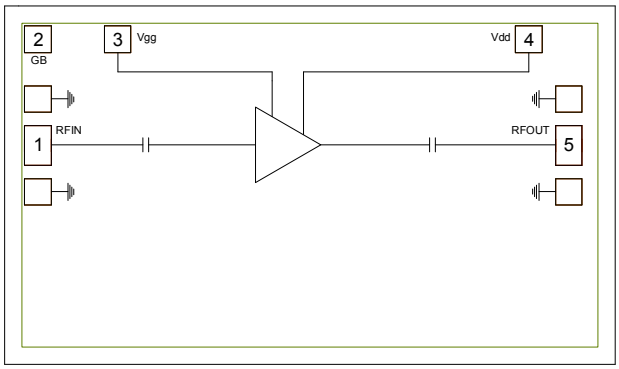
CMD163

17-27 GHz Low Noise Amplifier

Features

- Ultra low noise performance
- High linearity
- Small die size

Functional Block Diagram



Description

The CMD163 is a high dynamic range GaAs MMIC low noise amplifier ideally suited for military, space and communications systems where small size and high linearity are needed. The device is optimized for 21 GHz and delivers greater than 24 dB of gain with a corresponding output 1 dB compression point of +19 dBm and noise figure of 1.3 dB. The CMD163 is a 50 ohm matched design which eliminates the need for external DC blocks and RF port matching. The CMD163 offers full passivation for increased reliability and moisture protection. This amplifier is the perfect alternative to higher cost hybrid amplifiers.

Electrical Performance - V_{dd} = 4.0 V, V_{gg} = 3.0 V, T_A = 25 °C, F=21 GHz

Parameter	Min	Typ	Max	Units
Frequency Range		17 - 27		GHz
Gain		24		dB
Noise Figure		1.3		dB
Input Return Loss		10		dB
Output Return Loss		20		dB
Output P1dB		19		dBm
Supply Current		120		mA

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Specifications

Absolute Maximum Ratings

Parameter	Rating
Drain Voltage, Vdd	5.0 V
Gate Voltage, Vgg	4.0 V
RF Input Power	+20 dBm
Channel Temperature, Tch	150 °C
Power Dissipation, Pdiss	600 mW
Thermal Resistance	108 °C/W
Operating Temperature	-55 to 85 °C
Storage Temperature	-55 to 150 °C

Operation of this device outside the maximum ratings may cause permanent damage.

Recommended Operating Conditions

Parameter	Min	Typ	Max	Units
Vdd	2.0	4.0	5.0	V
Idd		120		mA
Vgg	0	3.0	4.0	V

Electrical performance is measured at specific test conditions. Electrical specifications are not guaranteed over all recommended operating conditions.

Electrical Specifications, V_{dd} = 4.0 V, V_{gg} = 3.0 V, T_A = 25 °C

Parameter	Min	Typ	Max	Min	Typ	Max	Units
Frequency Range	17 - 27			17.7 - 23.6			GHz
Gain	18	24	27	20	24	27	dB
Noise Figure		1.3	1.9		1.3	1.7	dB
Input Return Loss		8			10		dB
Output Return Loss		15			20		dB
Output P1dB		19			19		dBm
Output IP3		26			25		dBm
Supply Current	80	120	150	80	120	150	mA
Gain Temperature Coefficient		0.015			0.015		dB/°C
Noise Figure Temperature Coefficient		0.008			0.008		dB/°C

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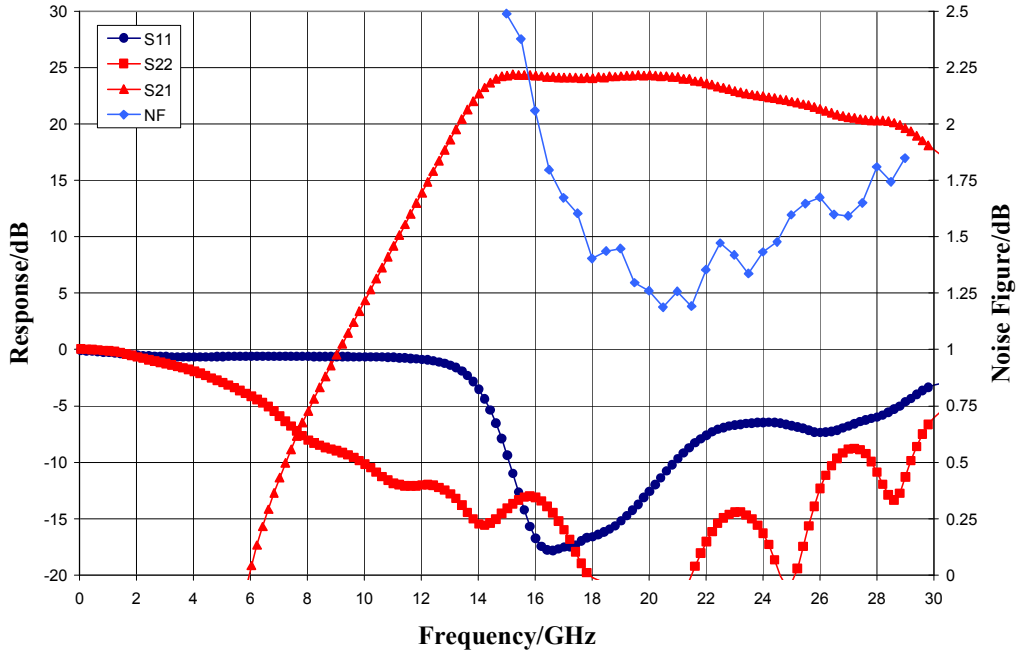
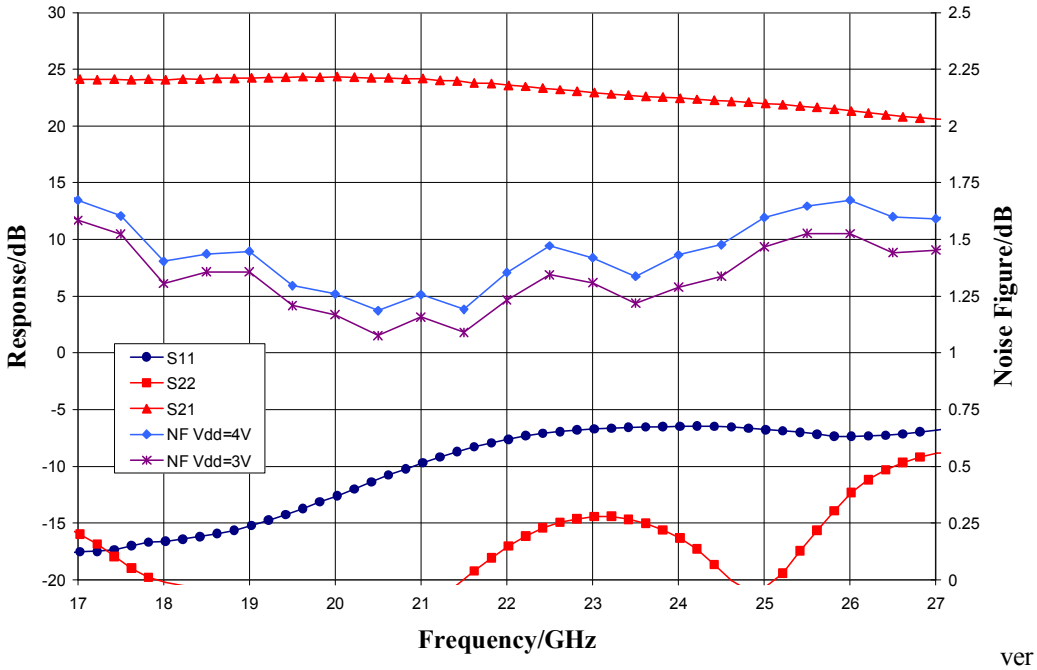
Typical Performance

Broadband Performance, V_{dd} = 4.0 V, V_{gg} = 3.0 V, I_{dd} = 120 mA, T_A = 25 °C



Narrow-band Performance, V_{dd} = 4.0 V, V_{gg} = 3.0 V, I_{dd} = 120 mA, T_A = 25 °C



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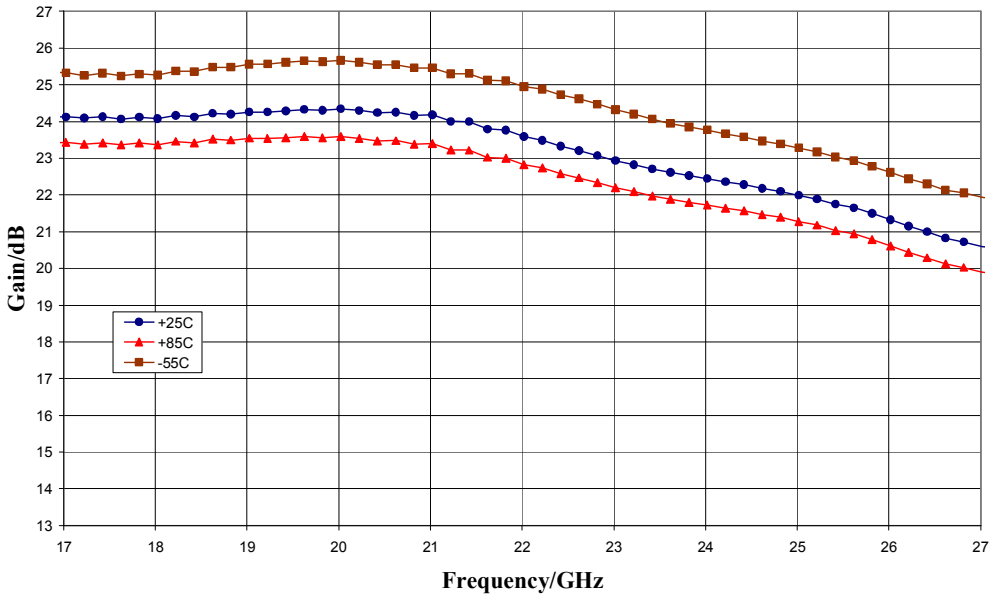
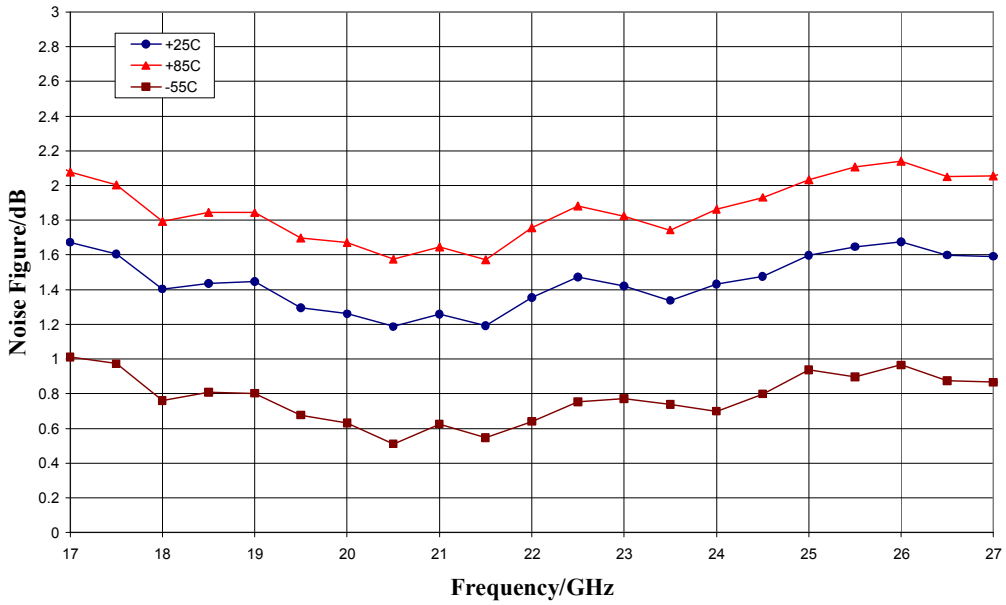
Typical Performance

Gain vs. Temperature, V_{dd} = 4.0 V, V_{gg} = 3.0 V



Noise Figure vs. Temperature, V_{dd} = 4.0 V, V_{gg} = 3.0 V



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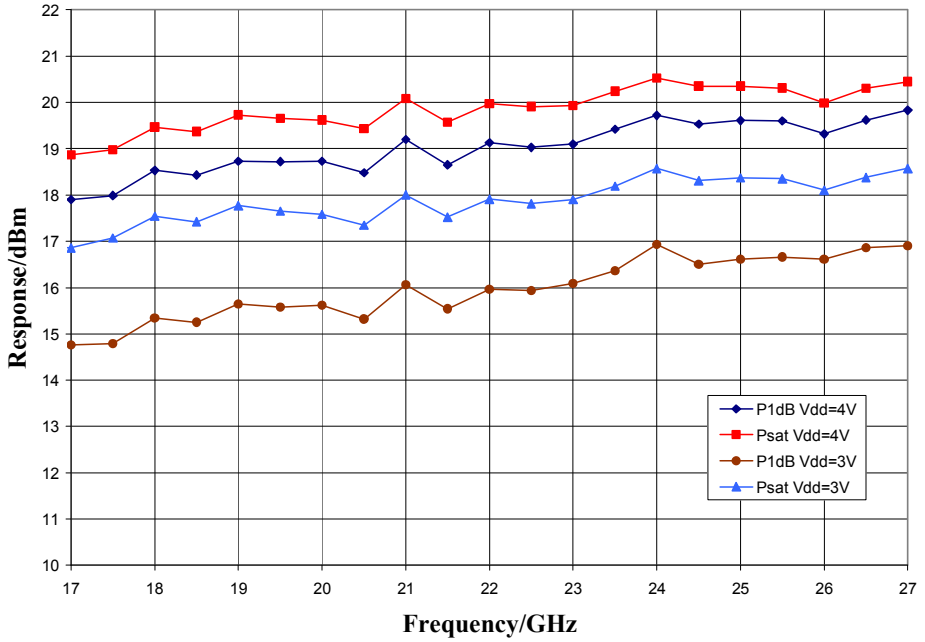
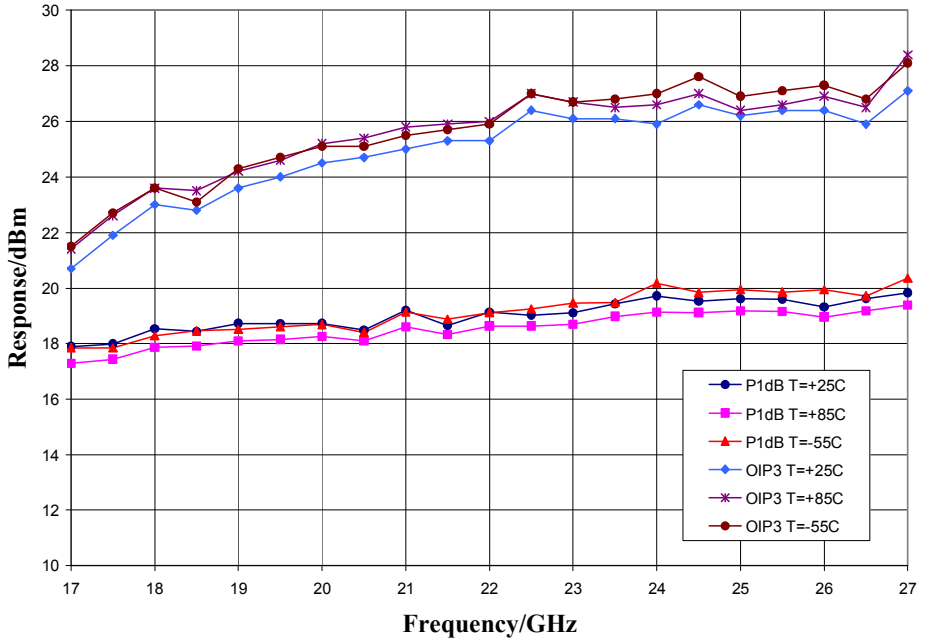
Typical Performance

Output Power, V_{dd} = 3.0 V & 4.0 V, V_{gg} = 3.0 V, T_A = 25 °C



P1dB & Output IP3 vs. Temperature, V_{dd} = 4.0 V, V_{gg} = 3.0 V



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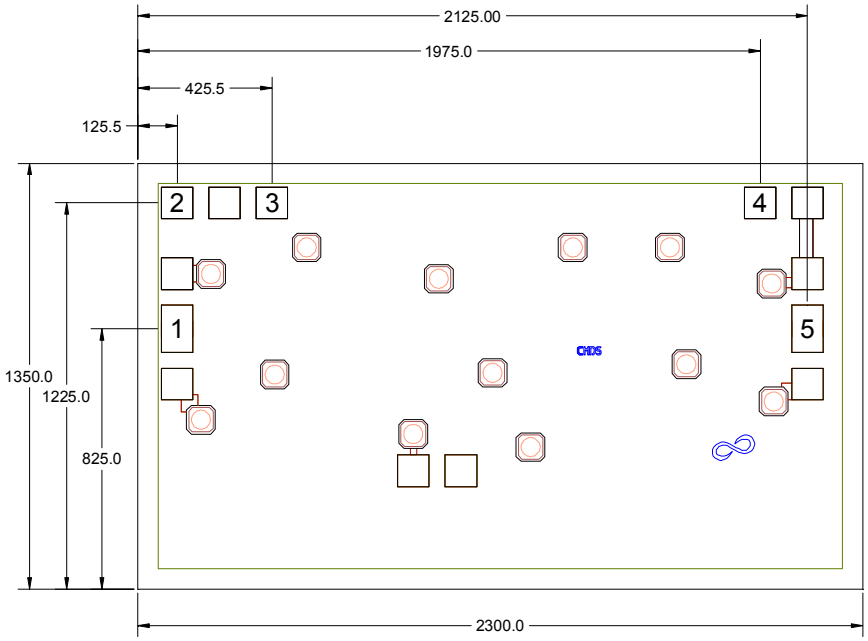
Mechanical Information

Die Outline (all dimensions in microns)



Notes:

1. No connection required for unlabeled pads
2. Backside is RF and DC ground
3. Backside and bond pad metal: Gold
4. Die is 85 microns thick
5. DC bond pads are 100 microns square

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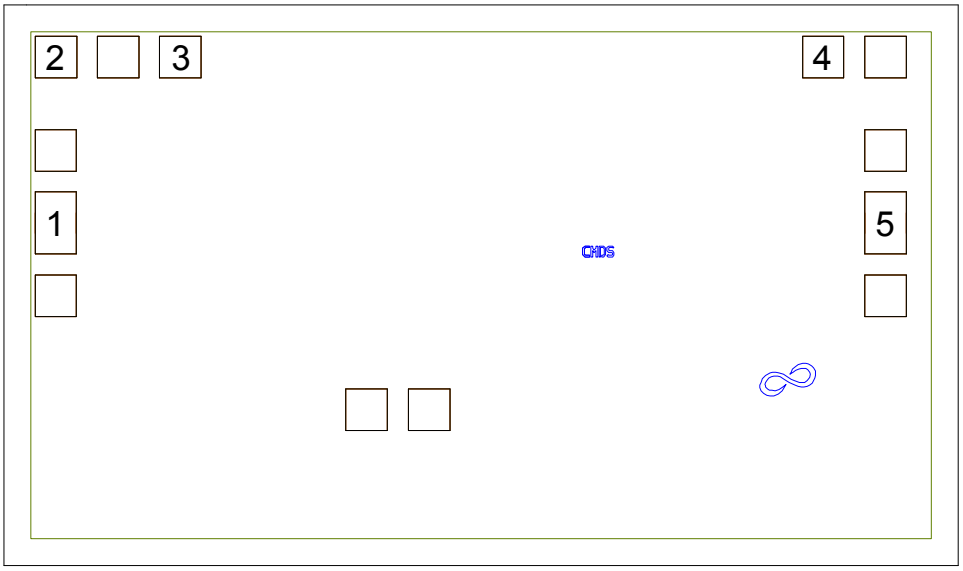
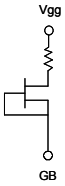
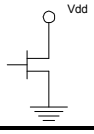
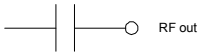
Pad Description

Pad Diagram



Functional Description

Pad	Function	Description	Schematic
1	RF in	DC blocked and 50 ohm matched	
2	GB	Connect to DC ground	
3	Vgg	Power supply voltage Decoupling and bypass caps required	
4	Vdd	Power supply voltage Decoupling and bypass caps required	
5	RF out	DC blocked and 50 ohm matched	
Backside	Ground	Connect to RF / DC ground	

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Applications Information

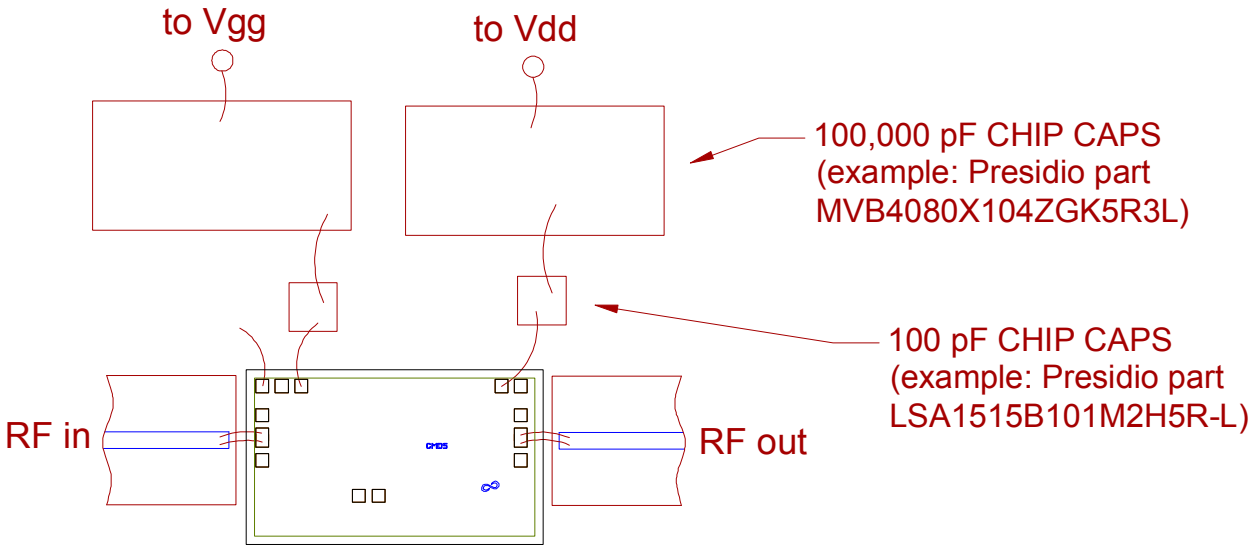
Assembly Guidelines

The backside of the CMD163 is RF ground. Die attach should be accomplished with electrically and thermally conductive epoxy only. Eutectic attach is not recommended. Standard assembly procedures should be followed for high frequency devices. The top surface of the semiconductor should be made planar to the adjacent RF transmission lines, and the RF decoupling capacitors placed in close proximity to the DC connections on chip.

RF connections should be made as short as possible to reduce the inductive effect of the bond wire. Use of a 0.8 mil thermosonic wedge bonding is highly recommended as the loop height will be minimized. The RF input and output require a double bond wire as shown.

The semiconductor is 85 um thick and should be handled by the sides of the die or with a custom collet. Do not make contact directly with the die surface as this will damage the monolithic circuitry. Handle with care.

Assembly Diagram



GaAs MMIC devices are susceptible to damage from Electrostatic Discharge. Proper precautions should be observed during handling, assembly and test.

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Applications Information

Biasing and Operation

The CMD163 is biased with a positive drain supply and positive gate supply. Performance is optimized when the drain voltage is set to +4.0 V, though it may be set to a minimum of +2.0 V and a maximum of +4.0 V. The recommended gate voltage is +3.0 V.

Turn ON procedure:

1.Apply drain voltage V_{dd} and set to +4 V
2.Apply gate voltage V_{gg} and set to +3 V

Turn OFF procedure:

1.Turn off gate voltage V_{gg}
2.Turn off drain voltage V_{dd}

RF power can be applied at any time.

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