

TX09 Series

32_{bit}

Development TMPA913CHXBG



High-performance 32-bit RISC microcontrollers with a USB device controller capable of operating at up to 200 MHz

Features

• ARM926EJ-S TM CPU Core ·Operating voltage: Internal:1.4 to 1.6 V I/O:1.7 to 1.9 V, 3	3.0 to 3.6 V	32kHz =	RTC WDT	ЛТАĞ I/F	16bit TIMER (6ch) → PW	/M Output
 Minimum instruction execution time: 5 ns (200 MHz internal, 0 to 70°C) 6.67 ns (150 MHz internal, -20 to 85°C) Data cache: 16 Kbytes Instruction cache: 16 Kbytes Internal ROM: 16 Kbytes (Boot) Internal RAM: 16 Kbytes 		Input/ Output Key Input (()	1/O 98 Key Port Melody/ Alarm UART	ARM 926EJ-S Core	10bit AD Ana	uch Screen Iog Input ernal Trigger
 External data bus width: Up to 16 bits On-chip Functions Memory controller Static memory SDR SDRAM LVCMOS DDR SDRAM 		TXD/RXD	(2ch) 1 ² C (2ch) 1 ² S 1/F (2ch) PMC	RAM DMA	Device (Hi	B Device igh Speed) K/DO/DI
 NAND Flash controller USB (High-speed) device controller DMA controller SSP (SPI/MicroWire mode) RTC 	 2 channels 1 channel 8 channels 2 channels 1 channel 	·UART ·I ² C ·1 ² S interface ·10-bit AD conv ·16-bit timer ·Touch-screen in ·JTAG interface	nterface		 2 channels 2 channels 2 channels 6 channels 6 channels 1 channel 	

·Power management circuit (PMC)

Package Information

Pin Assignments	A1	A2	AЗ	A4	A5	Aß	A7	A8	A9	A10	A11	A12	A13	A14	A15	A16	A17	A18	A19
U	B1	B2	вз	84	85	B6	87	B8	89	B10	B11	B12	B13	B14	B15	B16	B17	B18	B19
	C1	C2	СЗ	C4	C5	C6	C7	С8	C9	C10	C11	C12	C13	C14	C15	C16	C17	C18	C19
	D1	D2	DЗ	D4	D5	D6	D7	D8	D9	D10	D11	D12	D13	D14	D15	D16	D17	D18	D19
	E1	E2	E3	E4	E5	E6	E7	E8	E9	E10	E11	E12	E13	E14	E15	E16	E17	E18	E19
	F1	F2	FЗ	F4	F5	F6	F7	F8	F9	F10	F11	F12	F13	F14	F15	F16	F17	F18	F19
	G1	G2	G3	G4	G5	GB	G7	G8	G9	G10	G11	G12	G13	G14	G15	G16	G17	G18	G19
	H1	H2	ΗЗ	Н4	H5	H6	H7	H8	H9	H10	H11	H12	H13	H14	H15	H16	H17	H18	H19
	J1	J2	JЗ	J4	J5	J6	J7	J8	J9	J10	J11	J12	J13	J14	J15	J16	J17	J18	J19
	K1	К2	КЗ	K4	K5	K6	K7	K8	K9	K10	K11	K12	K13	K14	K15	K16	K17	K18	K19
	L1	12	L3	L4	L5	L6	L7	L8	19	L10	L11	L12	L13	L14	L15	L16	L17	L18	L19
	M1	M2	MЗ	M4	M5	M6	M7	M8	M9	M10	M11	M12	M13	M14	M15	M16	M17	M18	M19
Destross nome	N1	N2	NЗ	N4	N5	N6	N7	N8	N9	N10	N11	N12	N13	N14	N15	N16	N17	N18	N19
·Package name: FBGA361-P-1616-0.80AZ	P1	P2	PЗ	Ρ4	Ρ5	P6	P7	P8	P9	P10	P11	P12	P13	P14	P15	P16	P17	P18	P19
	R1	R2	RЗ	R4	R5	R6	R7	R8	R9	R10	R11	R12	R13	R14	R15	R16	R17	R18	R19
	T1	T2	ТЗ	т4	T5	Т6	T7	Т8	Т9	T 10	T 11	T 12	T13	T 14	T 15	T 16	T 17	T 18	T 19
Top View	U1	U2	UЗ	04	υ5	U6	U7	U8	U9	U10	U11	U12	U13	U14	U15	U16	U17	U18	U19
	V1	ν2	VЗ	ν4	∨5	V8	V7	V8	V9	V10	V11	V12	V13	V14	V15	V16	V17	V18	V19
	W1	W2	W3	W4	W5	W6	W7	W8	W9	W10	W11	W12	W13	W14	W15	W16	W17	W18	W19

• Pin Numbers and Names (1/3)

Ball No.	Pin name	Ball No.	Pin name	Ball No	Pin name	
C3	SPD/TCK	L6	PC5/MLDALMh/INT8	T6	SW2/NDALE	
D4	SP1/TMS	К2	DVCC3I04 N8 SW1/NDV		SW1/NDWEn	
K8	DVSSCOM1	L5	PC6/12C0CL	U6	SV3/NDD3	
D3	SP2/TDI	L1	PC2/PWE	W3	DVCC3109	
B1	DVCC3I01	L4	PT5/U1RXD	N9	SV6/NDD6	
ЕS	SP5/TDO	КЗ	DVSSCOM7	R7	SV2/NDD2	
E4	SP4/RTCK	N7	PT4/U1TXD	N10	SW0/NDREn	
F5	SP3/TRSTn	M1	PC7/I2C0DA/INT9	R8	PP2/INT2	
G6	DVCC3I019	T1	SM4/RESETn	\√4	DVCC1A3	
G5	PF6/I2C1CL	P7	TEST05	77	SV5/NDD5	
F4	PF7/I2C1DA/INTC	12	DVCC3105	\√5	SV1/NDD1	
H7	PF1	N6	TEST04	P9	PP1/INT1	
J7	PE7	L3	TEST03	L9	DVSSCOM11	
E3	PF2	M2	TEST02	P10	SV4/NDD4	
D2	PF3	MB	DVSSCOM8	U7	SV0/NDD0	
K8	DVSSCOM2	P1	TEST07	R9	PPO/INTO	
F3	PFO	M4	TEST15	W4	DVCC3I010	
L7	PE6	MB	TESTD6	M10	DVSSCOM12	
C2	PE3	M5	TESTD1	 	SN2/SELJTAG	
G4	PE4	N2	DVCC3106	T8	SM6/AMD	
E2	PES	N5	TESTDO	US	SM7/AM1	
K6	DVCC3I020	R2	DVCC1A2	1005	DVCC3I011	
C1	PED	P6	TEST16	R10	SND/SELMEMC	
H5	PEI	R1	TEST14	T10	SN1/SELDVCCM	
G3	PE2	R6	TEST12	1008	DVCC1C	
J8	DVSSCOM3	P2	TEST11	1007	SMD/X1	
D1	PT2/SP0D0	P4	DVSSCOM9	1 17	DVSS1C	
H4	PTD/SPDFSS	N3	TEST10	1008	SM1/X2	
E1	DVCC3I02	N1	TEST17	1/8	DVCC1C	
H6	PN1/U0RXD/SIR0IN	N4	TEST13	U9	PT7/X1USB	
F2	PND/UDTXD/SIRDOUT	P3	DVCC3107	T9	DVCC1A4	
J5	PT3/SPDDI	T2	VREFH	K9	DVSSCOM13	
F1	DVCC1A1	U2	VREFL	1/00	AVDD3C	
J8	DVSSCOM4	U1	AVSS3AD		SR4A/SENSE	
H3	PT1/SPDCLK	- VI	AVCC3AD	10	SR3/REAT	
K5	PT6/U1CTSn	P5	PD5/AN5/MY	W10	AVSS3C	
G2	PN4/U0DSRn/INTD	R3	PD4/AN4/MX	U10	AVDD3T1	
	PN7/U0RTSn/INTG	R5	PD3/AN3	V11	AVSS3T3	
M6 G1	DVCC3I03	R4	PD3/AN3 PD2/AN2	U11	AVSS313 AVSS3T2	
J4		T5	PD2/AN2 PD1/AN1	W11	SR1/DM	
	PN3/U0DCDn BN5/U0DIcANTE	T3		W12	SR0/DP	
H2	PN5/U0RIn/INTE		PD0/AN0			
K4	PN2/U0CTSn PN6/U0DTRn/INTF	U3 T4	PD6/PX/INTA(TSI)	V12	AVSS3T1	
H1		T4	PD7/PY/INTB	U12	AVSS3T1	
L8	DVSSCOM5	V2		W13	AVSS3TD AVDD2TD	
J1	SM2/XT1	V3	SW6/NDRB	V13	AVDD3T0	
К1	SM3/XT2	U4	SW4/NDCE0n	J10	DVSSCOM14	
1.40	DVSSCOM6	1002	SW5/NDCE1n	V14	DVCC1A6	
M9				T • • •	The second second	
M9 3 M7	PC4/FSOUT/PWM2OUT DVCC1B1	U5 L9	SW3/NDCLE DVSSCOM10	T11 √15	PMD/I2S1W/S PM2/I2S1DATO	

• Pin Numbers and Names (2/3)

Ball No.	Pin name	Ball No.	Pin name	Ball No	Pin name
W14	PL4/I2SSCLK	M14	TEST33	E19	DVCC1A9
T12	DVCC3I2S1	R18	SB1/D9	H14	TEST36
U13	PL1/I2SOCLK/SP1CLK	M15	TEST32	F18	SE4/A4
P11	PM3/I2S1MCLK	T19	DVCCM6	G15	SG4/A20
W16	PM1/I2S1CLK	M16	SB7/D15	J11	DVSSCOM26
N11	PL3/I2S0MCLK/SP1DI	P18	SB4/D12	G16	SG2/A18
L10	DVSSCOM15	L11	DVSSCOM21	G17	SF4/A12
R12	PLD/I2SDWS/SP1FSS	L12	PP3/INT3	F15	SG5/A21
U14	DVCC3I2S2	M17	PP4/INT4	D19	DVCCM9
T13	PR0/RESETOUTn	L13	PP5/INT5	F16	SG3/A19
W15	PR1/SMCWPn/FCOUT	R19	PP6/INT6	E18	SE5/A5
W17	DVCC1A6	L14	PP7/INT7	F17	SF5/A13
U15	DVCCM1	N18	PA0/KI0	G14	DVSSCOM27
R13	TEST23	P19	DVCC3I012	E15	SG6/A22
V16	SA0/D0	N19	PA1/KI1	D18	SE6/A6
P12	TEST26	L15	PA2/KI2	C19	SE7/A7
M11	DVSSCOM16	L17	PA3/KI3	E16	DVCCM10
P13	TEST25	L16	PA4/KI4	C18	SF1/A9
W18	SL6/DMCCLKIN	M18	PA6/KI5	E17	SF6/A14
P14	TEST24	L11	DVSSCOM22	D17	SF7/A15
U15	DVCCM2	M19	PA6/KI6	H13	DVSSCOM28
T14	TEST21	K16	PA7/KI7	B19	SFD/A8
∨17	SA1/D1	L19	DVCC1A8	C17	SGD/A16
V18	SA2/D2	K15	PB0/K00	D16	DVSSCOM29
M11	DVSSCOM17	L18	PB1/K01	B18	TEST43
R14	TEST22	K13	PB2/KO2	A18	TEST42
R15	TEST20	K17	SM5/TES T0n	D15	DVCCM11
U16	SA3/D3	K19	DVCC3I013	C16	SL2/DMCAP
T15	DVCCMB	K18	PB3/K03	E14	SG7/A23
T16	SA6/D5	K14	PB4/KO4	D14	DVSSCOM30
U17	SL4/DMCDDQS0	J19	PB5/K05	C15	SJ4/DMCBA0
V19	SL5/DMCDDQS1	K11	DVSSCOM23	C14	SJ5/DMCBA1
N13	DVSSCOM18	J18	PB6/K06	F14	DVSSCOM31
T17	SA6/D6	J15	PB7/K07	F13	SH1/A25
D47		1 212	FBUIKUU	1 1 13	01111/20
R17	SB0/D8	H19	PC0/K08	E13	DVCC1A10
R17 R16					
	SBD/D8	H19	PC0/KO8	E13	DVCC1A10
R16	SB0/D8 SA7/D7	H19 J16	PC0/K08 PC1/K09	E13 B17	DVCC1A10 SK0/DMCSDQM0/DMCDDM0
R16 P15	SB0/D8 SA7/D7 DVCCM4	H19 J16 K11	PC0/K08 PC1/K09 DVSSC0M24	E13 B17 B16	DVCC1A10 SK0/DMCSDQM0/DMCDDM0 DVCCM12
R16 P15 N14	SB0/D8 SA7/D7 DVCCM4 TEST30	H19 J16 K11 J14	PCD/K08 PC1/K09 DVSSCOM24 DVCC1B2	E13 B17 B16 G13	DVCC1A10 SK0/DMCSDQM0/DMCDDM0 DVCCM12 SH0/A24
R16 P15 N14 U18	SB0/D8 SA7/D7 DVCCM4 TEST30 SA4/D4	H19 J16 K11 J14 J17	PC0/K08 PC1/K09 DVSSCOM24 DVCC182 SF2/A10	E13 B17 B16 G13 A17	DVCC1A10 SK0/DMCSDQM0/DMCDDM0 DVCCM12 SH0/A24 DVSSC0M32
R16 P15 N14 U18 P16	SB0/D8 SA7/D7 DVCCM4 TEST30 SA4/D4 SB2/D10	H19 J16 K11 J14 J17 J13	PC0/K08 PC1/K09 DVSSCOM24 DVCC182 SF2/A10 TEST37	E13 B17 B16 G13 A17 D13	DVCC1A10 SK0/DMCSDQM0/DMCDDM0 DVCCM12 SH0/A24 DVSSCOM32 TEST47
R16 P15 N14 U18 P16 M12	SB0/D8 SA7/D7 DVCCM4 TEST30 SA4/D4 SB2/D10 DVSSCOM19	H19 J16 K11 J14 J17 J13 H18	PC0/K08 PC1/K09 DVSSCOM24 DVCC182 SF2/A10 TEST37 SE2/A2	E13 B17 B16 G13 A17 D13 C13	DVCC1A10 SK0/DMCSDQM0/DMCDDM0 DVCCM12 SH0/A24 DVSSC0M32 TEST47 SJ2/DMCRASn
R16 P15 N14 U18 P16 M12 N15	SB0/D8 SA7/D7 DVCCM4 TEST30 SA4/D4 SB2/D10 DVSSCOM19 TEST27	H19 J16 K11 J14 J17 J13 H18 K12	PCD/K08 PC1/K09 DVSSCOM24 DVCC182 SF2/A10 TEST37 SE2/A2 DVCCM7	E13 B17 B16 G13 A17 D13 C13 B13	DVCC1A10 SK0/DMCSDQM0/DMCDDM0 DVCCM12 SH0/A24 DVSSC0M32 TEST47 SJ2/DMCRASn DVSSC0M33
R16 P15 N14 U18 P16 M12 N15 P17	SB0/D8 SA7/D7 DVCCM4 TEST30 SA4/D4 SB2/D10 DVSSCOM19 TEST27 SB3/D11	H19 J16 K11 J14 J17 J13 H18 K12 G19	PC0/K08 PC1/K09 DVSSC0M24 DVCC1B2 SF2/A10 TEST37 SE2/A2 DVCCM7 SED/A0	E13 B17 B16 G13 A17 D13 C13 B13 D12	DVCC1A10 SK0/DMCSDQM0/DMCDDM0 DVCCM12 SH0/A24 DVSSCOM32 TEST47 SJ2/DMCRASn DVSSCOM33 SK5/SMCBE1n
R16 P15 N14 U18 P16 M12 N15 P17 U19	SB0/D8 SA7/D7 DVCCM4 TEST30 SA4/D4 SB2/D10 DVSSCOM19 TEST27 SB3/D11 DVCC1A7	H19 J16 K11 J14 J17 J13 H18 K12 G19 H16	PC0/K08 PC1/K09 DVSSC0M24 DVCC1B2 SF2/A10 TEST37 SE2/A2 DVCCM7 SED/A0 SG1/A17	E13 B17 B16 G13 A17 D13 C13 B13 D12 H12	DVCC1A10 SK0/DMCSDQM0/DMCDDM0 DVCCM12 SH0/A24 DVSSCOM32 TEST47 SJ2/DMCRASn DVSSCOM33 SK5/SMCBE1n PR2/INTH
R16 P15 N14 U18 P16 M12 N15 P17 U19 N12	SB0/D8 SA7/D7 DVCCM4 TEST30 SA4/D4 SB2/D10 DVSSCOM19 TEST27 SB3/D11 DVCC1A7 TEST31	H19 J16 K11 J14 J17 J13 H18 K12 G19 H16 G18	PCD/K08 PC1/K09 DVSSC0M24 DVCC1B2 SF2/A10 TEST37 SE2/A2 DVCCM7 SED/A0 SG1/A17 SE3/A3	E13 B17 B16 G13 A17 D13 C13 B13 D12 H12 B15	DVCC1A10 SK0/DMCSDQM0/DMCDDM0 DVCCM12 SH0/A24 DVSSC0M32 TEST47 SJ2/DMCRASn DVSSC0M33 SK5/SMCBE1n PR2/INTH DVCCM13
R16 P15 N14 U18 P16 M12 N15 P17 U19 N12 T18	SB0/D8 SA7/D7 DVCCM4 TEST30 SA4/D4 SB2/D10 DVSSCOM19 TEST27 SB3/D11 DVCC1A7 TEST31 DVCCM5	H19 J16 K11 J14 J17 J13 H18 K12 G19 H16 G18 J12	PC0/K08 PC1/K09 DVSSC0M24 DVCC1B2 SF2/A10 TEST37 SE2/A2 DVCCM7 SED/A0 SG1/A17 SE3/A3 DVSSC0M25	E13 B17 B16 G13 A17 D13 C13 B13 D12 H12 B15 F12	DVCC1A10 SK0/DMCSDQM0/DMCDDM0 DVCCM12 SH0/A24 DVSSC0M32 TEST47 SJ2/DMCRASn DVSSC0M33 SK5/SMCBE1n PR2/INTH DVCCM13 SH2/SMCBEDn
R16 P15 N14 U18 P16 M12 N15 P17 U19 N12 T18 M13	SB0/D8 SA7/D7 DVCCM4 TEST30 SA4/D4 SB2/D10 DVSSCOM19 TEST27 SB3/D11 DVCC1A7 TEST31 DVCCM5 TEST34	H19 J16 K11 J14 J17 J13 H18 K12 G19 H16 G18 J12 H17	PCD/K08 PC1/K09 DVSSC0M24 DVCC1B2 SF2/A10 TEST37 SE2/A2 DVCCM7 SED/A0 SG1/A17 SE3/A3 DVSSC0M25 SF3/A11	E13 B17 B16 G13 A17 D13 C13 B13 D12 H12 B15 F12 C12	DVCC1A10 SK0/DMCSDQM0/DMCDDM0 DVCCM12 SH0/A24 DVSSC0M32 TEST47 SJ2/DMCRASn DVSSC0M33 SK5/SMCBE1n PR2/INTH DVCCM13 SH2/SMCBEDn TEST46

Pin Numbers and Names (3/3)

Date Pin name Date Pin name A15 \$L1/DMCDCLKN A4 DVCC31017 A14 DVSCOM35 J6 PJ2 B14 \$K1/DMCSDDMIDMCDDM1 B3 PJ1 D11 \$J7/SMCAVOn E7 TEST62 F10 DVCCM14 C6 DVSSCOM43 G12 \$J1/DMC0MEh G8 PJ6 A13 \$L3/SMCCLK A3 PJ4 F11 DVCC1411 D6 PJ3 B11 DVSSC0M36 A2 DVCC3018 E11 \$J0/SMC0Eh C4 TEST61 B12 \$J3DMCCASn E6 PJ7 A12 DVSSC0M37 D5 PJ6 B10 \$H6/SMCC33n C5 TEST64 D10 \$H6/SMCC33n F7 TEST64 D10 \$H4/SMCC51n C C G10 \$K4/SMCC51n C C G11 \$L7/SMCWAITn C C A9 DVCC1	Dell		D-11	
A15 SL1/DMCDCLKN A4 DVCC31017 R14 DVSSC 0M35 J6 PJ2 B14 SK1/DMCSD QM1/DMC DDM1 B3 PJ1 D11 SJ7/SMCA/On E7 TEST62 F10 DVCCM14 C6 DVSSC 0M43 G12 SJ1/DMC/WEn G8 PJ6 A13 SL3/SMCCLK A3 PJ4 F11 DVCC1A11 D6 PJ3 B11 DVSSC 0M36 A2 DVCC31018 E11 SJ0/SMCC4sn E8 PJ7 A12 DVSSC 0M37 D6 PJ6 B10 SH6/SMCC53n C5 TEST63 A12 DVSSC 0M38 F7 TEST64 D10 SH3/SMCC50n F6 PK7 A14 SJ6/DMCCKE B2 DVC1A14 F10 DVCCM15 I I C10 SH4/SMCC51n I I C11 SH7/SMCWATN I I A9 DVCC3	Ball	Pin name	Ball	Pin name
A14 DVSSCOM35 J6 PJ2 B14 SK1/DMCSDQM1/DMCDDM1 B3 PJ1 D11 SJ7/SMCAVDn E7 TESTB2 F10 DVCCM14 C6 DVSSCOM43 G12 SJ1/DMCWEn G8 PJ5 A13 SL3/SMCCLK A3 PJ4 F11 DVCC1A11 D6 PJ3 B11 DVSSCOM36 A2 DVCC31018 E11 SJ0/SMCCASn E6 PJ7 A12 DVSSCOM37 D5 PJ6 B10 SH6/SMCCSn C5 TESTB3 A12 DVSSCOM38 F7 TESTB4 D10 SH3/SMCCSn C5 TESTB4 A11 SJ60MCCKE B2 DVCC1A14 F10 DVSSCOM39 D C114 C10 SH4/SMCCS1n C C C11 SH7/SMCWATTN C C A20 DVCC1A14 C C D9 DVCC31014				
B14 SK1/DMC SD QM1/DMC DDM1 B3 PJ1 D11 SJ7/SMC AV.On E7 TEST62 F10 D_VCCM14 C6 D/VSSC0M43 G12 SJ1/DMC/WEn G8 PJ4 F11 D_VCC1A11 D6 PJ3 B11 D_VSSC0M36 A2 D_VCC3I018 E11 SJ/SMCC4K H3 PJ4 B12 SJ/SDMCCASn E6 PJ7 A12 D/VSSC0M37 D5 PJ6 B10 SH6/SMCC53n C5 TEST63 B10 SH6/SMCC53n C5 TEST63 A12 D/VSSC0M38 F7 TEST64 D10 SH3/SMCC50n F6 PK7 A11 SJ6/DMCCKE B2 D/VC1/14 F10 D/VCCM15 D D C10 SH4/SMCC51n L L G11 SL7/SMC/WAITN L L A9 D/VCC1A12 L L G10 PK1<				
D11 SJ7/SMCAVOn E7 TEST82 F10 DVCCM14 C6 DVSSCOM43 G12 SJ1/DMCWEn G8 PJ6 A13 SL3/SMCCLK A3 PJ4 F11 DVCC1A11 D6 PJ3 B11 DVSCOM36 A2 DVCC31018 E11 SJ0/SMCOEn C4 TEST61 B12 SJ3/DMCCASn E8 PJ7 A12 DVSSCOM37 D6 PJ6 B10 SH6/SMCCSn H9 DVSSCOM44 E10 SH6/SMCCSn F6 PK7 A11 SJ6/DMCCKE B2 DVCC1A14 D10 SH3/SMCCSn F6 PK7 A11 SJ6/DMCKE B2 DVCC1A14 C10 SH4/SMCCSn A1 A1 A11 SJ7/SMC/WATn A1 A1 A9 DVCC1A12 A1 A1 G10 PK1 A1 A1 B9 PK0 A1 <td></td> <td></td> <td></td> <td></td>				
F10 DVCCM14 C6 DVSSC0M43 G12 SJ/IDMCWEn G8 PJ5 A13 SL3/SMCCLK A3 PJ4 F11 DVCC1A11 D6 PJ3 B11 DVSSC0M36 A2 DVCC3I018 E11 SJ0/SMCCAsn E8 PJ7 A12 DVSSC0M37 D5 PJ6 B10 SH6/SMCCS2n H9 DVSSC0M44 E10 SH6/SMCCS2n F7 TEST63 A11 SJ6DMCCKE B2 DVCC1A14 F10 DVCSC0M39 F7 TEST64 D10 SH4/SMCCSn F6 PK7 A11 SJ6DMCCKE B2 DVCC1A14 F10 DVCCM15 Image: Common Common F6 G11 SL7/SMCWATn Image: Common				
G12 SJ1/DMCWEn G8 PJ5 A13 SL3/SMCCLK A3 PJ4 F11 DVCC1A11 D6 PJ3 B11 DVSSC0M36 A2 DVCC31018 E11 SJ0/SMC0En C4 TEST61 B12 SJ3/DMCCASn E6 PJ7 A12 DVSSC0M37 D5 PJ6 B10 SH6/SMCCS2n H9 DVSSC0M44 E10 SH6/SMCCS3n C5 TEST63 A12 DVSSC0M38 F7 TEST64 D10 SH3/SMCCS1n F6 PK7 A11 SJ6/OMCKE B2 DVCC1A14 F10 DVSCCM15 DVCC1A14 F11 C10 SH4/SMCCS1n I I C11 SH7/SMCCMATIN I I A9 DVCC1A12 I I G10 PK1 I I B9 PK0 I I G11 SUZ/SMCM40 I				
A13 SL3/SMCCLK A3 PJ4 F11 DVCC1/A11 D6 PJ3 B11 DVSSC0M36 A2 DVCC31018 E11 SJJ0SMC0Eh C4 TEST81 B12 SJJ3DMCCASn E6 PJ7 A12 DVSSC0M37 D5 PJ6 B10 SH6/SMCC32n H9 DVSSC0M44 E10 SH6/SMCC32n C5 TEST83 A12 DVSSC0M38 F7 TEST84 D10 SH3/SMCC3n C5 TEST84 D10 SH3/SMCC3n F6 PK7 A11 SJØDMCCKE B2 DVCC1A14 F10 DVCCM15 C C C11 SH7/SMCW4ITn A A A10 DVSC1/A12 C C G10 PK1 B B B B9 PK0 C C C B1 DVSC1/A12 C C G10 PK1				
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