TOSHIBA CMOS LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TC75W55FU, TC75W55FK

DUAL OPERATIONAL AMPLIFIER

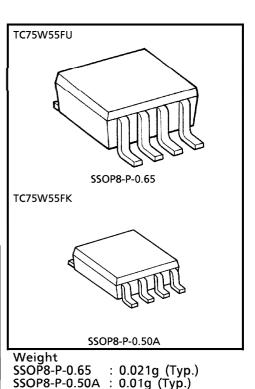
TC75W55 is a CMOS operational amplifier with low supply voltage, low supply current.

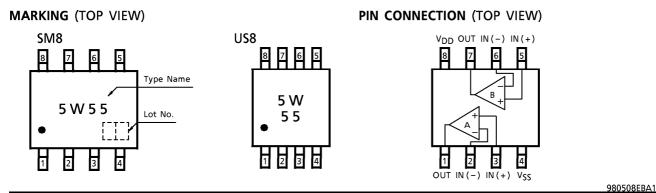
FEATURES

- Low supply voltage : $V_{DD} = \pm 0.9 \sim 3.5 V$ or $1.8 \sim 7 V$
- Low supply current : I_{DD} ($V_{DD} = 3V$) = 20 μ A (Typ.)
- The internally phase compensated operational amplifier.
- Small package

MAXIMUM RATINGS (Ta = 25° C)

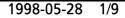
CHARACTERISTIC	SYMBOL	RATING	Ν
Supply Voltage	V _{DD} , V _{SS}	7	V
Differential Input Voltage	DVIN	±7	V
Input Voltage	VIN	V _{DD} ~V _{SS}	V
Power Dissipation	D-	250 (SM8)	mW
	PD	200 (US8)	mvv
Operating Temperature	T _{opr}	- 40~85	°C
Storage Temperature	T _{stg}	- 55~125	°C





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ELECTRICAL CHARACTERISTICS

DC CHARACTERISTICS ($V_{DD} = 3.0V$, $V_{SS} = GND$, $Ta = 25^{\circ}C$)

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	VIO	1	$R_{S} = 10k\Omega$	_	2	10	mV
Input Offset Current	lio	_	—	_	1	—	pА
Input Bias Current	Ц	_	—	—	1	—	pА
Common Mode Input Voltage	CMV _{IN}	2	—	0.0	—	2.1	V
Voltage Gain (Open Loop)	GV	_	—	60	70	—	dB
	∨он	3	$R_L \ge 1 M \Omega$	2.9		_	v
Maximum Output Voltage	VOL	4	$R_L \ge 1 M \Omega$	—		0.1	v
Common Mode Input Signal Rejection Ratio	CMRR	2	V _{IN} = 0.0~2.1V	60	70	_	dB
Supply Voltage Rejection Ratio	SVRR	1	V _{DD} = 1.8~7.0V	60	70	_	dB
Supply Current	I _{DD}	5	_	_	20	40	μA
Source Current	I _{source}	6	_	10	20	_	μA
Sink Current	l _{sink}	7		100	450	_	μA

DC CHARACTERISTICS ($V_{DD} = 1.8V$, $V_{SS} = GND$, $Ta = 25^{\circ}C$)

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	VIO	1	$R_{S} = 100 k\Omega$	—	2	10	mV
Input Offset Current	li0	_	—	_	1	_	pА
Input Bias Current	Ц	_	—	—	1	_	pА
Common Mode Input Voltage	CMVIN	2	—	0.0	—	0.9	V
Voltage Gain (Open Loop)	GV	_		60	70	—	dB
Maximum Output Voltage	Voн	3	$R_L \ge 1M\Omega$	1.7	—	_	v
	VOL	4	$R_L \ge 1M\Omega$	—	—	0.1	v
Supply Current	IDD	5	—	—	16	32	μA
Source Current	lsource	6		8	16	_	μA
Sink Current	l _{sink}	7	—	100	400	_	μΑ

AC CHARACTERISTICS ($V_{DD} = 3.0V$, $V_{SS} = GND$, $Ta = 25^{\circ}C$)

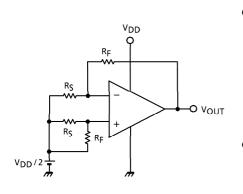
CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Slew Rate	SR	—	—	_	0.08	—	V/µs
Unity Gain Cross Frequency	fŢ	—	_	_	160	—	kHz

AC CHARACTERISTICS ($V_{DD} = 1.8V$, $V_{SS} = GND$, $Ta = 25^{\circ}C$)

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Slew Rate	SR	_	—	_	0.06	_	V/µs
Unity Gain Cross Frequency	fŢ	—	—		140	—	kHz

TEST CIRCUIT

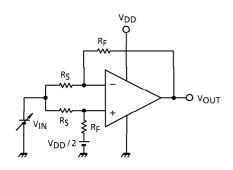
1. SVRR, VIO



• SVRR $V_{DD} = 1.8V : V_{DD} = V_{DD}1, V_{OUT} = V_{OUT}1$ $V_{DD} = 7.0V : V_{DD} = V_{DD}2, V_{OUT} = V_{OUT}2$ $SVRR = 20 \ell og \left(\left| \frac{V_{OUT}1 - V_{OUT}2}{V_{DD}1 - V_{DD}2} \right| \times \frac{R_S}{R_F + R_S} \right)$

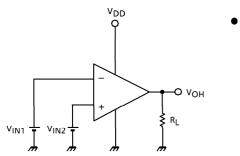
$$V_{IO} = \left(V_{OUT} - \frac{V_{DD}}{2} \right) \times \frac{R_{S}}{R_{F} + R_{S}}$$

2. CMRR, CMVIN



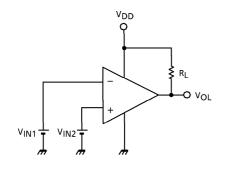
- CMRR $V_{IN} = 0.0V : V_{IN} = V_{IN}1, V_{OUT} = V_{OUT}1$ $V_{IN} = 2.1V : V_{IN} = V_{IN}2, V_{OUT} = V_{OUT}2$ $CMRR = 20\ell og \left(\left| \frac{V_{OUT}1 - V_{OUT}2}{V_{IN}1 - V_{IN}2} \right| \times \frac{R_{S}}{R_{F} + R_{S}} \right)$
- CMV_{IN}

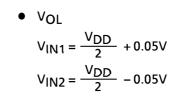
3. V_{OH}



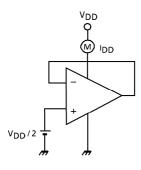
 V_{OH} $V_{IN1} = \frac{V_{DD}}{2} - 0.05V$ $V_{IN2} = \frac{V_{DD}}{2} + 0.05V$

4. V_{OL}



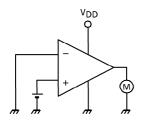


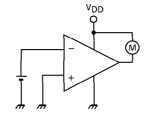
5. I_{DD}

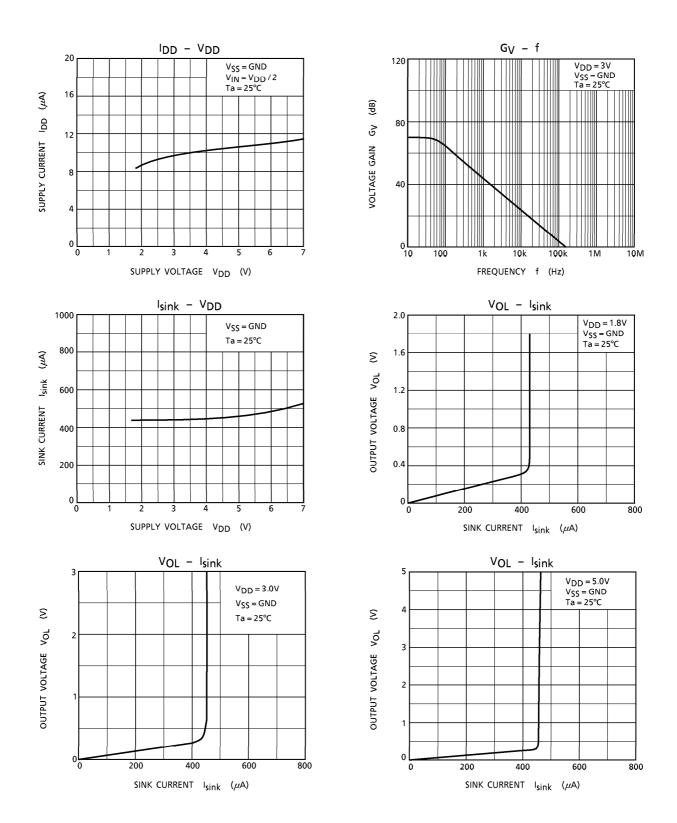


6. I_{source}

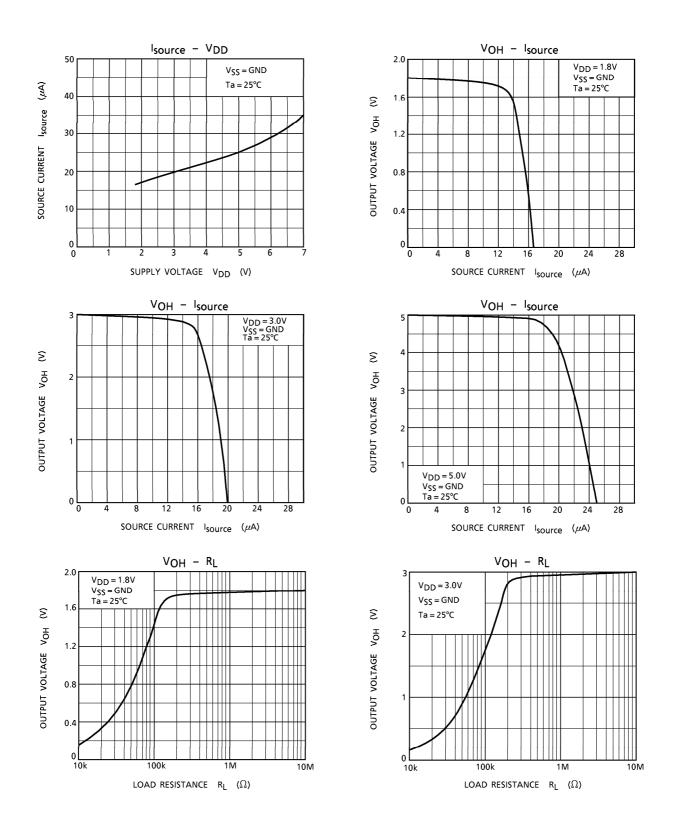
7. I_{sink}

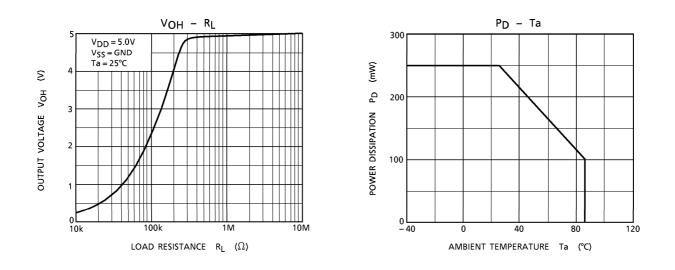






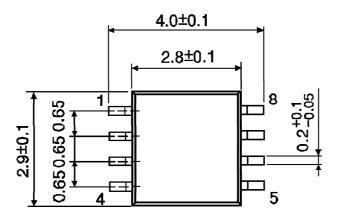
<u>TOSHIBA</u>

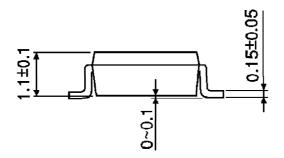




OUTLINE DRAWING SSOP8-P-0.65

Unit : mm

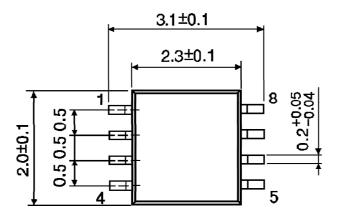


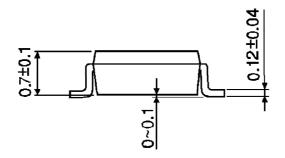


Weight : 0.021g (Typ.)

OUTLINE DRAWING SSOP8-P-0.50A

Unit : mm





Weight : 0.01g (Typ.)

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