

ST13003DN

High voltage fast-switching NPN power transistor

Preliminary data

Features

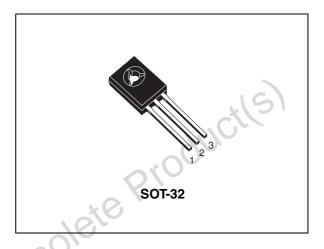
- High voltage capability
- Low spread of dynamic parameters
- Very high switching speed
- Integrated free-wheeling diode

Application

Compact fluorescent lamps (CFLs)

Description

The device is manufactured using high voltage multi epitaxial planar technology for high switching speeds and high voltage capability. It uses a cellular emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.



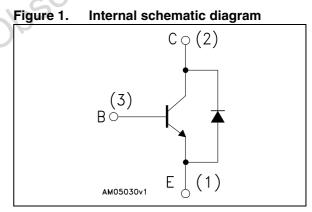


Table 1. Device summary

Order code	Marking	Package	Packaging
ST13003DN	13003DN	SOT-32	BAG

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This is preliminary information on a new product now in development or undergoing evaluation. Details are subject to change without notice.

Electrical ratings 1

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{CES}	Collector-emitter voltage (V _{BE} = 0)	700	V
V _{CEO}	Collector-emitter voltage ($I_B = 0$)	400	V
V_{EBO}	Collector-base voltage $(I_C = 0)$	9	V
Ι _C	Collector current	1	А
I _{CM}	Collector peak current (t _P < 5 ms)	2	Α
Ι _Β	Base current	0.5	SA
I _{BM}	Base peak current (t _P < 5 ms)	1.0	Α
P _{TOT}	Total dissipation at $T_c = 25 \text{ °C}$	20	W
T _{STG}	Storage temperature	-55 to 150	°C
TJ	Max. operating junction temperature	150	
Table 3.	Thermal data	ete	·

Table 3. Thermal data

	Symbol Parameter		Value	Unit	
	R _{thJC} Thermal resistance junction-case		6.25	°C/W	
R _{thJC} Thermal resistance junction-case					



2 Electrical characteristics

 $T_{case} = 25$ °C; unless otherwise specified.

Table 4.	Electrical	characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
lana	Collector cut-off current	V _{CE} = 700 V			1	mA
I _{CES}	(V _{BE} = 0)	V _{CE} = 700 V T _C = 125 °C			5	mA
I _{EBO}	Emitter cut-off current $(I_{C} = 0)$	V _{EB} = 9 V			1	mA
V _{CEO(sus)}	Collector-emitter sustaining voltage (I _B = 0)	I _C = 10 mA	400	č	S	v
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation	I _C = 0.5 A I _B = 125 mA	21	20	0.7	V
•CE(sat)	voltage	$I_{\rm C} = 1 \text{ A}$ $I_{\rm B} = 330 \text{ mA}$	0		1.2	V
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation	$I_{\rm C} = 0.5 {\rm A}$ $I_{\rm B} = 125 {\rm mA}$			1.2	V
• BE(sat)	voltage	$I_{\rm C} = 1 \text{ A}$ $I_{\rm B} = 330 \text{ mA}$			1.3	V
h	DC ourrent goin	$I_{\rm C} = 0.5 \text{ A}, \qquad V_{\rm CE} = 2 \text{ V}$	6		18	
h _{FE}	DC current gain	I _C = 1 A V _{CE} = 10 V	5		15	
	Inductive Load	$I_{C} = 0.4 \text{ A}$ $V_{clamp} = 300 \text{ V}$				
t _s	Storage time	$I_{B(on)} = -I_{B(off)} = 80 \text{ mA}$		2.5		μs
t _f	Fall time	$V_{BB(off)} = -5 V$ Figure 2		180		ns
V _F	Diode forward voltage	I _F = 350 mA		1.5		V

1. Pulse test: pulse duration $\leq 300 \ \mu$ s, duty cycle $\leq 2 \ \%$



2.1 Test circuit

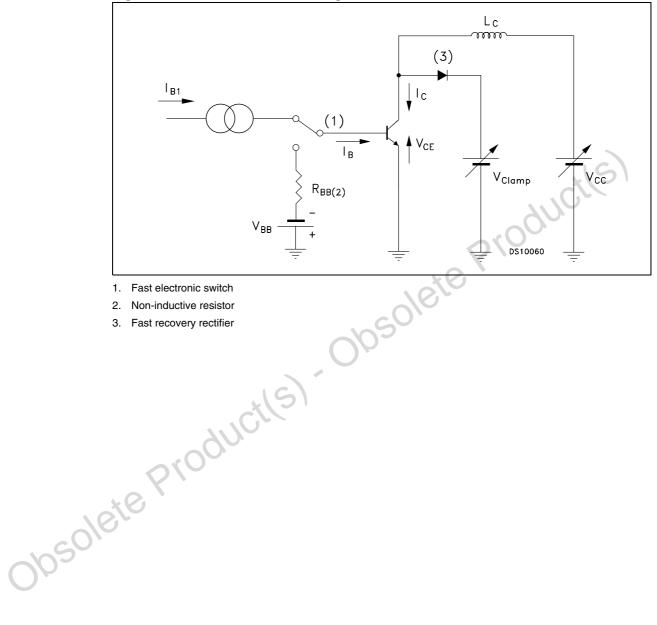


Figure 2. Inductive load switching test circuit



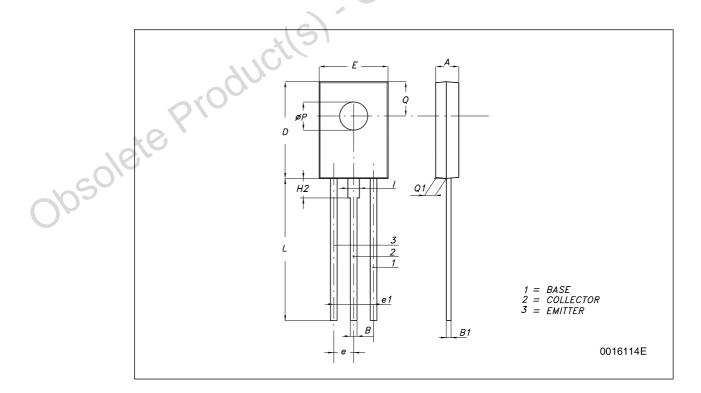
3 Package mechanical data

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obsolete Product(s). Obsolete Product(s)

DIM.		mm.	
DIM.	MIN.	ТҮР	MAX.
A	2.4		2.9
В	0.64		0.88
B1	0.39		0.63
D	10.5		11.05
E	7.4		7.8
е	2.04	2.29	2.54
e1	4.07	4.58	5.08
L	15.3		16
Р	2.9	Q	3.2
Q		3.8	
Q1	1	10	1.52
H2		2.15	





4 Revision history

Table 5.Document revision history

Date	Revision	Changes
25-Feb-2010	1	First release.

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