

VB325SP

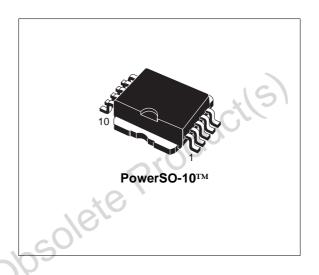
HIGH VOLTAGE IGNITION COIL DRIVER POWER I.C.

TYPE	V _{cl}	I _{cl}	I _{CC}	
VB325SP	380V	10A	150mA	

- PRIMARY COIL VOLTAGE INTERNALLY SET
- COIL CURRENT LIMIT INTERNALLY SET
- LOGIC LEVEL COMPATIBLE INPUT
- DRIVING CURRENT QUASI PROPORTIONAL TO COLLECTOR CURRENT
- SINGLE FLAG-ON COIL CURRENT
- LOW VOLTAGE CLAMP THERMAL SHUTDOWN

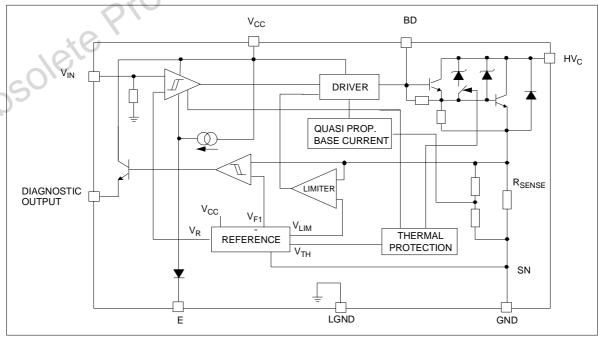


The VB325SP is a high voltage power integrated circuit made using the STMicroelectronics VIPower™ M1-3 technology, with vertical current flow power darlington and logic level compatible driving circuit. The enable pin allows to externally block the switch when the input is on. Built-in protection circuit for coil current limiting and collector voltage clamping allows the device to be used as smart, high voltage, high current interface



in advanced electronic ignition system. If the input signal from the micro happens to remain high, the device protects itself against over-heating by forcing collector current to smooth decrease (low voltage clamp feature) and no undesidered spark occurs (see figure 4).

BLOCK DIAGRAM



September 2013 DocID9671 Rev 4 1/9

ABSOLUTE MAXIMUM RATING

Symbol	Parameter	Value	Unit
HV _c	Collector voltage (Internally limited)	-0.3 to V _{clamp}	V
I _C	Collector current (Internally limited)	10	Α
I _{C(gnd)}	DC current on Emitter Power	± 10.5	Α
V _{CC}	Driving stage supply voltage	-0.3 to 7	V
Is	Driving circuitry supply current	± 200	mA
I _{s(gnd)}	DC current on Ground pin	±1	Α
V _{IN}	Input voltage	-0.3 to V _{CC} + 0.3	V
I _{IN}	Maximum Input Current	100	mA
f _{IN}	Logic Input Frequency in Operative Mode	DC to 150	Hz
V _{OUT(flag)}	Output Voltage Primary Threshold Current Level	-0.3 to V _{CC} + 0.3	V
I _{OUT(flag)}	Flag Output Current	100	mA
P _{max}	Power Dissipation (T _c =25°C)	125	W
E _{s/b}	Self Clamped Energy during Output Power Clamping (See figure 2)	275	mJ
V_{ESD}	ESD voltage (HV _c pin)	±4	K۷
V_{ESD}	ESD voltage (Enable pin)	+ 1.5 ; -2	K۷
V_{ESD}	ESD voltage (Other pins)	±2	K۷
I _{BD}	Input Darlington Base Current	150	mA
V _{BD}	Input Darlington Base Voltage	Internally limited	V
Tj	Operating Junction Temperature	-40 to 150	°C
T _{stg}	Storage temperature Range	-55 to 150	°C
VE	Maximum Enable Voltage	-0.3 to 5.5	V
Ι _Ε	Maximum Enable Current	± 150	μΑ

THERMAL DATA

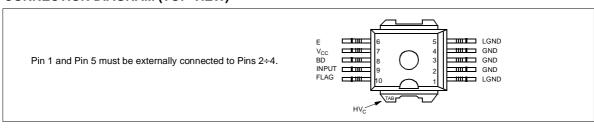
Symbol	ymbol Parameter		Value	Unit
R _{thj-case}	Thermal resistance junction-case	(MAX)	1	°C/W
R _{thi-amb}	Thermal resistance junction-ambient	(MAX)	51	°C/W

PIN FUNCTION

No	Name	Function
1 - 5	LGND	Signal Ground
2 - 3 - 4	GND	Emitter Power Ground
6	Е	Enable (*)
7	V _{CC}	Logic Supply Voltage
8	BD	Base Darlington
9	INPUT	Logic input channel (Internal Pull Down)
10	FLAG	Diagnostic Output Signal (Open Emitter)
TAB	HV _C	Primary Coil Output Driver (Open Collector)

(*) When grounded the Input is Enabled

CONNECTION DIAGRAM (TOP VIEW)



ELECTRICAL CHARACTERISTICS (5.3V < V_{bat} < 24V; V_{CC} =5V \pm 10%; -40°C < T_{j} < 125°C; R_{coil} =580m Ω ; L_{coil} =3.75mH unless otherwise specified; See note 1)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
V _{cl}	High Voltage Clamp	I _{coil} =6.5A	320	380	420	V
V _{IcI}	Low Voltage Clamp	I_{coil} =6.5A; T_j = T_{sd}	30	40	50	V
V _{ce(sat)}	Power Stage Saturation Voltage	I _C =6A; V _{IN} =4V		1.5	2	V
I _{CC(stdby)}	Stand-by Supply Current	IN=Off			11	mA
I _{CC}	DC Logic Current	V_b =16V; I_C =6.5A; f=100Hz; Load = Coil; V_{CC} =5.5V			40	mA
I _{CC(peak)}	Peak DC Logic Current during On Phase	I _C =6.5A		100	150	mΑ
V_{CC}	DC Logic Voltage		4.5		5.5	V
I _{cl}	Coil Current Limit	-40°C < T _j < 125°C (See note 2)	9		11	Α
I _{c(off)}	Output Off State Current	IN=Off; V_{HVC} =24V; V_{CC} =5V; T_j =25°C			5	mA
V _{INH}	High Level Input Voltage	V _{CC} =4.5V	4		V _{CC}	V
V_{INL}	Low Level Input Voltage	V _{CC} =5.5V	-0.3		0.8	V
V _{IN(hyst)}	Input Threshold Hysteresis		0.4			V
I _{INH}	High Level Input Current	V _{IN} =4V)		100	μΑ
I _{INL}	Low Level Input Current	V _{IN} =0.8V	0		30	μΑ
I _{INpd}	Input Active Pull Down	V _{IN} =4V	10		100	μΑ
V _{diagH}	High Level Flag Output Voltage	R_{EXT} =22K Ω ; C_{EXT} =1nF (See note 3)	V _{CC} - 1		V _{CC}	V
V _{diagL}	Low Level Flag Output Voltage	R_{EXT} =22K Ω ; C_{EXT} =1nF (See note 3)			0.5	V
I _{diagTH}	Coil Current Level Threshold	T _j =25°C	4.25	4.5	4.75	Α
I _{diagTD}	Coil Current Level Threshold Drift	(See figure 1)				
I _{diag}	High Level Flag Output Current	$I_C > I_{diagTH}; V_{diag} = 3V$	0.5			mA
I _{diag(leak)}	Leakage Current on Flag Output	V _{IN} =Low; V _{CC} =5.5V			10	μΑ
V _F	Antiparallel Diode Forward Voltage	I _C = -1A			2	V
E _{s/b}	Single Pulse Avalanche Energy	L=6mH; I _C = 8A		180		mJ
t _{ON}	Turn-on time	R_c =0.5 Ω ; L_c =3.75mH; T_j =25°C; V_{bat} =13V (See figure 6)		1	5	μs
t _{OFF}	Turn-off time	R_c =0.5 Ω ; L_c =3.75mH; I_C =6.5A; T_j =25°C; V_{bat} =13V (See figure 6)		15	25	μs
T_{sd}	Thermal shut-down intervention		150			°C
V_{EH}	High Level Enable Voltage	V _{IN} =V _{INH} ; OUT=Off (See Note 4)	2			V
V _{EL}	Low Level Enable Voltage	V _{OUT} free to follow V _{IN}			0.40	V



Note 1: parametric degratation are allowed with $5.3 < V_b < 10V$ and $V_b > 24V$. Note 2: the primary coil current value I_{cl} must be measured 1ms after desaturation of the power stage. Note 3: no internal Pull-down. Note 4: if ENABLE pin is floating OUT=Off for every input status. (*) Internally Limited

ELECTRICAL CHARACTERISTICS (continued)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
I _{EH}	High Level Enable Current	V _E =5V			500	μΑ
I _{EL}	Low Level Enable Current	V _E <0.4V	- 200			μΑ
V _{BD(off)}	Base Darlington Voltage Off	V _E =V _{EH}			1	V
V _{BD(on)}	Base Darlington Voltage On	$V_{IN}=V_{INH}$; $V_{E}=V_{EL}$; $I_{C}=6.5A$	1.8			V

PRINCIPLE OF OPERATION

The VB325SP is mainly intended as a high voltage power switch device driven by a logic level input and interfaces directly to a high energy electronic ignition coil.

The input V_{IN} of the VB325SP is fed from a low power signal generated by an external controller that determinesboth dwell time and ignition point. During Vin high (\geq 4V) the VB325SP increases current in the coil to the desired, internally set current level.

After reaching this level, the coil current remains constant until the ignition point, that corresponds to the transition of Vin from high to low (typ. 1.9V threshold).

During the coil current switch-off, the primary voltage HV_C is clamped at an internally set value

 $\rm V_{cl},$ typically 380V. The transition from saturation to desaturation, coil current limiting phase, must have the ability to accomodate an overvoltage.

A maximum overshoot of 20V is allowed.

FEEDBACK

When the collector current exceeds 4.5A, the feedback signal is turned high and it remains so, until the input voltage is turned-off.

OVERVOLTAGE

The VB325SP can withstand the following transients of the battery line:

- $-100V/2msec(R_i = 10 \Omega)$
- +100V/0.2msec (R_i =10 Ω)
- +50V/400msec ($R_i = 4.2 \Omega$, with $V_{IN} = 3V$)

Figure 1: Flag current Vs. temperature

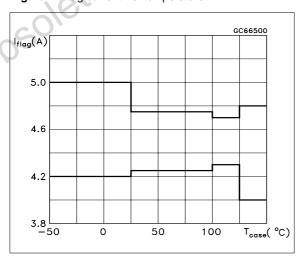


Figure 2: Single pulse avalanche energy capability

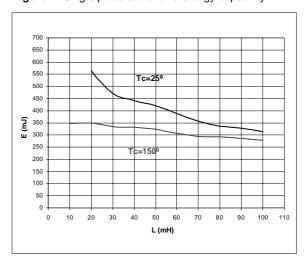


Figure 3: Self Clamped Inductive Switching Current Vs. Time

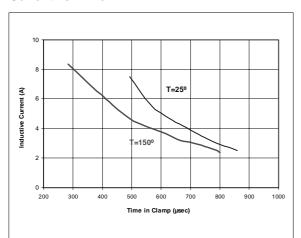


Figure 4: Low voltage clamp feature

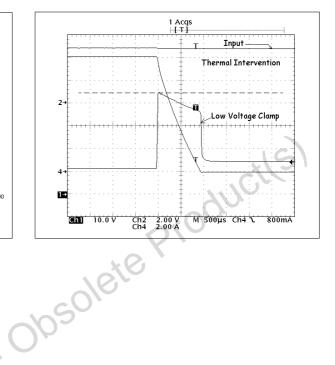


Figure 5: Typical application diagram

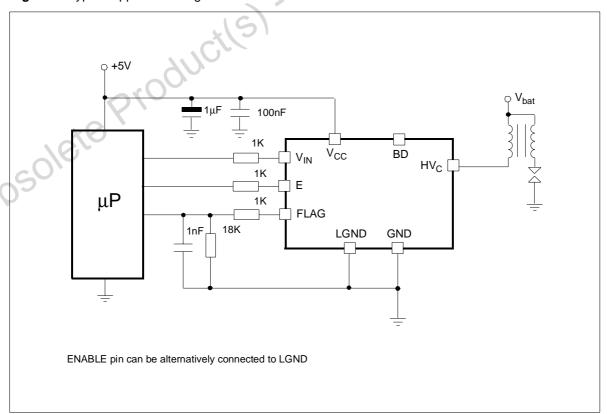
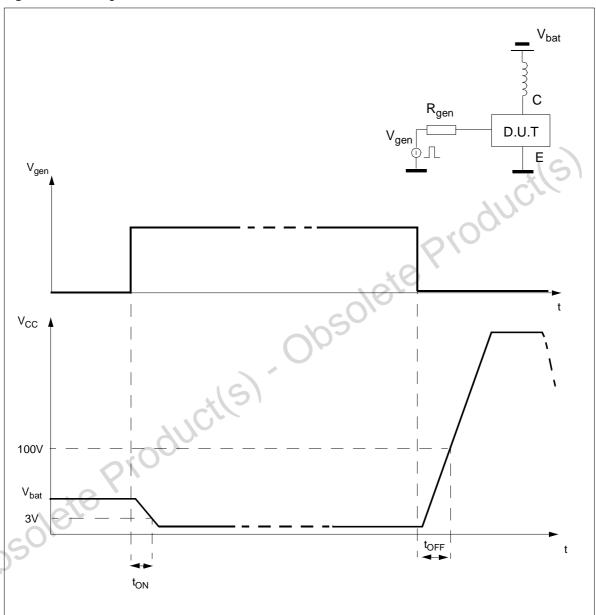


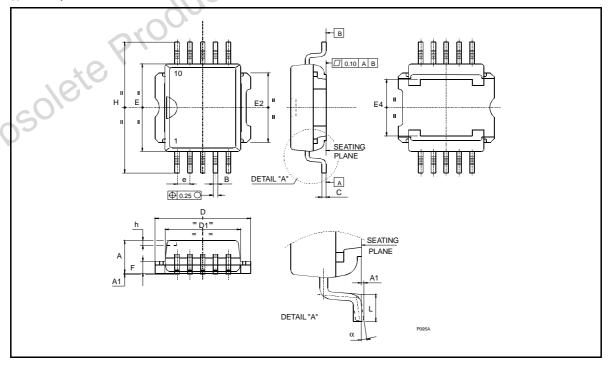
Figure 6: Switching time for inductive load



PowerSO- 10^{TM} MECHANICAL DATA

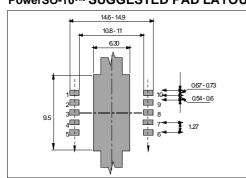
DIM.	mm.			inch			
DIIVI.	MIN.	TYP	MAX.	MIN.	TYP.	MAX.	
Α	3.35		3.65	0.132		0.144	
A (*)	3.4		3.6	0.134		0.142	
A1	0.00		0.10	0.000		0.004	
В	0.40		0.60	0.016		0.024	
B (*)	0.37		0.53	0.014		0.021	
С	0.35		0.55	0.013		0.022	
C (*)	0.23		0.32	0.009		0.0126	
D	9.40		9.60	0.370		0.378	
D1	7.40		7.60	0.291		0.300	
E	9.30		9.50	0.366	Al	0.374	
E2	7.20		7.60	0.283		300	
E2 (*)	7.30		7.50	0.287	710	0.295	
E4	5.90		6.10	0.232		0.240	
E4 (*)	5.90		6.30	0.232		0.248	
е		1.27		10	0.050		
F	1.25		1.35	0.049		0.053	
F (*)	1.20		1.40	0.047		0.055	
H	13.80		14.40	0.543		0.567	
H (*)	13.85		14.35	0.545		0.565	
h		0.50			0.002		
L	1.20		1.80	0.047		0.070	
L (*)	0.80		1.10	0.031		0.043	
α	00	16	8°	00		8°	
α (*)	2º	7/2	8°	2º		8°	

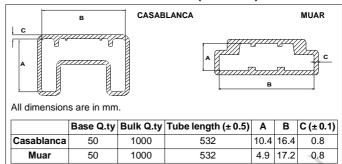
(*) Muar only POA P013P



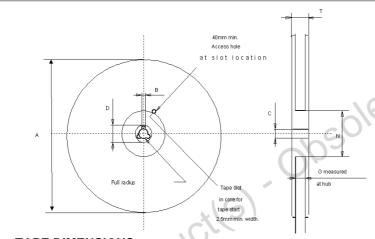
PowerSO-10TM SUGGESTED PAD LAYOUT

TUBE SHIPMENT (no suffix)





TAPE AND REEL SHIPMENT (suffix "13TR")



REEL DIMENSIONS

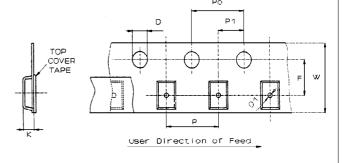
Base Q.ty	600
Bulk Q.ty	600
A (max)	330
B (min)	1.5
C (± 0.2)	13
F	20.2
G (+ 2 / -0)	24.4
N (min)	60
T (max)	30.4

All dimensions are in mm.

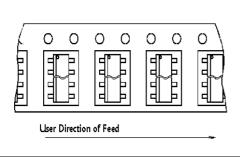
TAPE DIMENSIONS

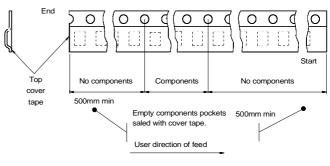
According to Electronic Industries Association (EIA) Standard 481 rev. A, Feb 1986

Tape width	W	24
Tape Hole Spacing	P0 (± 0.1)	4
Component Spacing	Р	24
Hole Diameter	D (± 0.1/-0)	1.5
Hole Diameter	D1 (min)	1.5
Hole Position	F (± 0.05)	11.5
Compartment Depth	K (max)	6.5
Hole Spacing	P1 (± 0.1)	2



All dimensions are in mm.





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