

# 10V Drive Nch MOSFET

# RCJ450N20

### Structure

Silicon N-channel MOSFET

### Features

- 1) Low on-resistance.
- 2) High-speed switching.
- 3) Wide range of SOA.
- 4) Drive circuits can be simple.
- 5) Parallel use is easy.

# Application

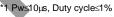
Switching

### Packaging specifications

	• .		
	Package	Taping	
Type	Code	TL	
	Quantity (pcs)	1000	
RCJ450N2	0	0	

# • Absolute maximum ratings (Ta = 25°C)

Paramete	Symbol	Limits	Unit	
Drain-source voltage	$V_{\rm DSS}$	200	V	
Gate-source voltage	$V_{GSS}$	±30	V	
Drain current	Continuous	I <sub>D</sub> *3	±45	Α
Diam current	Pulsed	I <sub>DP</sub> *1	±180	Α
Source current	Continuous	l <sub>s</sub> *3	45	Α
(Body Diode)	Pulsed	I <sub>SP</sub> *1	180	Α
Avalanche current		I <sub>AS</sub> *2	22.5	Α
Avalanche energy		E <sub>AS</sub> *2	160	mJ
Power dissipation		P <sub>D</sub> *4	211	W
Channel temperature	Tch	150	°C	
Range of storage temper	Tstg	-55 to +150	°C	



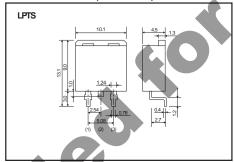
<sup>\*2</sup> L  $\stackrel{\bullet}{=}$  500 $\mu$ H,  $V_{DD}$ =50V,  $R_G$ =25 $\Omega$ ,  $T_{ch}$ =25 $^{\circ}$ C

### • Thermal resistance

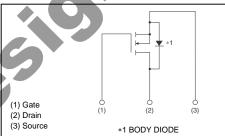
Parameter	Symbol	Limits	Unit
Channel to Case	Rth (j-c)*	0.59	°C/W

<sup>\*</sup> T<sub>C</sub>=25°C

### Dimensions (Unit : mm)



# • Inner circuit



<sup>\*3</sup> Limited only by maximum temperature allowed.

<sup>\*4</sup> T<sub>C</sub>=25°C

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### • Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	$I_{GSS}$	-	-	±100	nΑ	$V_{GS}=\pm30V, V_{DS}=0V$
Drain-source breakdown voltage	$V_{(BR)DSS}$	200	-	-	V	I <sub>D</sub> =1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =200V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS (th)</sub>	3.0	-	5.0	٧	$V_{DS}$ =10V, $I_{D}$ =1mA
Static drain-source on-state resistance	R <sub>DS (on)</sub> *	-	42	55	mΩ	I <sub>D</sub> =22.5A, V <sub>GS</sub> =10V
Forward transfer admittance	I Y <sub>fs</sub> I*	17.0	-	-	S	V <sub>DS</sub> =10V, I <sub>D</sub> =22.5A
Input capacitance	C <sub>iss</sub>	-	4200	-	pF	V <sub>DS</sub> =25V
Output capacitance	C <sub>oss</sub>	-	270	-	pF	V <sub>GS</sub> =0V
Reverse transfer capacitance	$C_{rss}$	-	160	-	pF	f=1MHz
Turn-on delay time	t <sub>d(on)</sub> *	-	52	-	ns	V <sub>DD</sub> ≒ 100V, I <sub>D</sub> =22.5A
Rise time	t <sub>r</sub> *	-	210	-	ns	V <sub>GS</sub> =10V
Turn-off delay time	t <sub>d(off)</sub> *	-	90	-	ns	$R_L=4.4\Omega$
Fall time	t <sub>f</sub> *	-	70	-	ns	$R_G=10\Omega$
Total gate charge	Q <sub>g</sub> *	-	80	-	nC	V <sub>DD</sub> ≒ 100V, I <sub>D</sub> =45A
Gate-source charge	Q <sub>gs</sub> *	-	28	-	nC	V <sub>GS</sub> =10V
Gate-drain charge	Q <sub>gd</sub> *	-	28	-	nC	

<sup>\*</sup>Pulsed

# ●Body diode characteristics (Source-Drain)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward Voltage	V <sub>SD</sub> *	-	-	1.5	V	$I_s$ =45A, $V_{GS}$ =0V



<sup>\*</sup>Pulsed

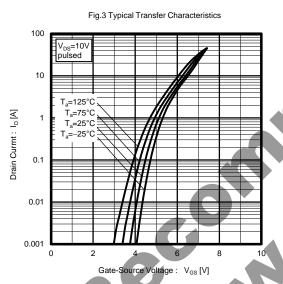
### ●Electrical characteristic curves (Ta=25°C)

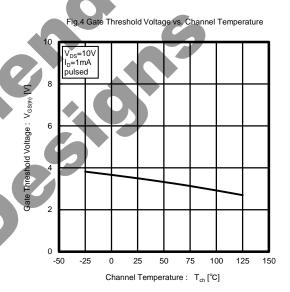
20 T<sub>a</sub>=25°C V<sub>GS</sub>=10.0V pulsed V<sub>GS</sub>=8.0V 15 Drain Current : I<sub>D</sub> [A] 10 V<sub>GS</sub>=6.5V 5 V<sub>GS</sub>=6.0V V<sub>GS</sub>=5.5V 0

Drain-Source Voltage :  $V_{DS}$  [V]

Fig.1 Typical Output Characteristics ( I )

Fig.2 Typical Output Characteristics (  ${\rm I\hspace{-.1em}I}$  ) 45 T<sub>a</sub>=25°C V<sub>GS</sub>=10.0V 40 pulsed V<sub>GS</sub>=8.0V 35 30 Drain Current: I<sub>D</sub> [A] 25 20 15 =5.5V 10 V<sub>GS</sub>=6.0V 5 0 10 Drain-Source Voltage : V<sub>DS</sub> [V]







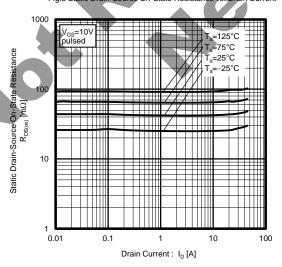
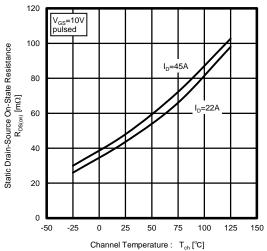


Fig.6 Static Drain-Source On-State Resistance vs. Channel Temperature





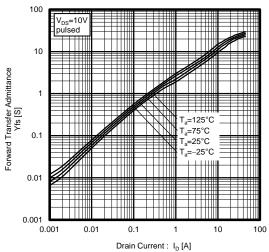


Fig.8 Source Current vs. Source-Drain Voltage

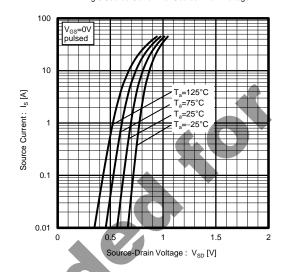


Fig.9 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

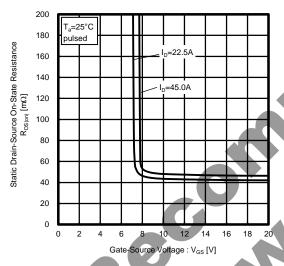


Fig.10 Switching Characteristics

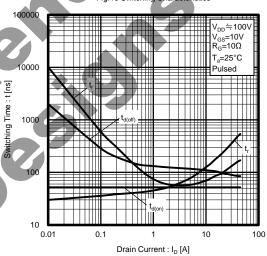


Fig.11 Dynamic Input Characteristics

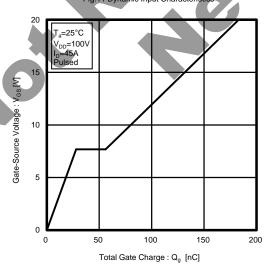


Fig.12 Typical Capacitance vs. Drain-Source Voltage

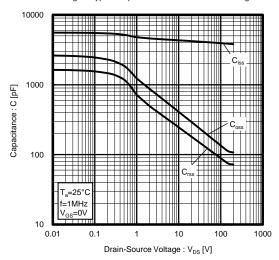


Fig.13 Reverse Recovery Time vs. Source Current

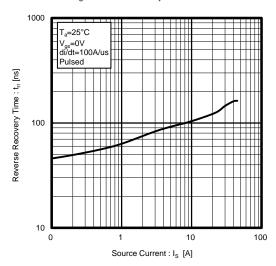


Fig.15 Normalized Transient Thermal Resistance v.s. Pulse Width

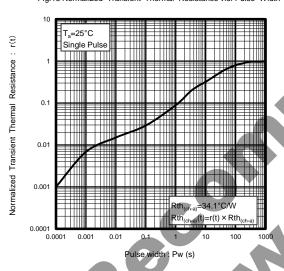
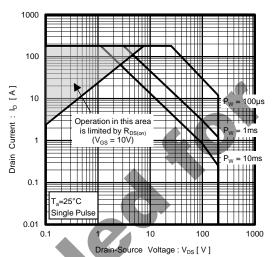


Fig.14 Maximum Safe Operating Area



### Measurement circuits

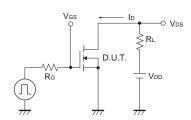


Fig.1-1 Switching Time Measurement Circuit

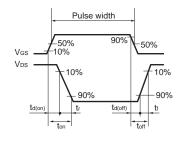


Fig.1-2 Switching Waveforms

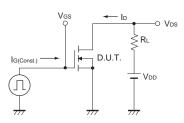


Fig.2-1 Gate Charge Measurement Circuit

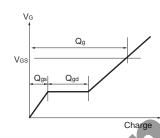


Fig.2-2 Gate Charge Waveform

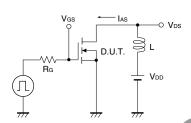


Fig.3-1 Avalanche Measurement Circuit

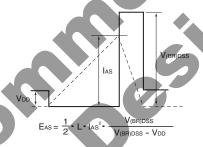


Fig.3-2 Avalanche Waveform

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- 8. Confirm that operation temperature is within the specified range described in the product specification.
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