R6524ENJ

Nch 650V 24A Power MOSFET

Datasheet

V_{DSS}	650V
$R_{DS(on)}$(Max.)	0.185Ω
I_D	±24A
P_D	245W

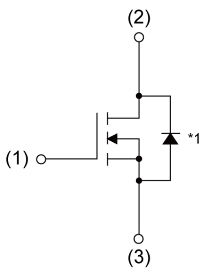
●Outline

LPT(S)

●Features

1) Low on-resistance
2) Fast switching speed
3) Parallel use is easy
4) Pb-free plating ; RoHS compliant

●Inner circuit



(1) Gate
(2) Drain
(3) Source

*1 Body Diode

●Application

Switching

●Packaging specifications

Packing	Embossed Tape
Packing code	TL
Marking	R6524ENJ
Basic ordering unit (pcs)	1000

●Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

Parameter		Symbol	Value	Unit
Drain - Source voltage		V_{DSS}	650	V
Continuous drain current (T_c = 25°C)		I_D *1	±24	A
Pulsed drain current		I_{DP} *2	±72	A
Gate - Source voltage	static	V_{GSS}	±20	V
	AC(f>1Hz)		±30	V
Avalanche current, single pulse		I_{AS}	4.1	A
Avalanche energy, single pulse		E_{AS} *3	654	mJ
Power dissipation (T_c = 25°C)		P_D	245	W
Junction temperature		T_j	150	℃
Operating junction and storage temperature range		T_{stg}	-55 to +150	℃

●Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal resistance, junction - case	R_{thJC} *4	-	-	0.51	℃/W
Thermal resistance, junction - ambient	R_{thJA} *5	-	-	80	℃/W
Soldering temperature, wavesoldering for 10s	T_{sold}	-	-	265	℃

●Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 1mA	650	-	-	V
Zero gate voltage drain current	I_{DSS}	V_{DS} = 650V, V_{GS} = 0V				μA
		T_j = 25°C	-	-	100	
		T_j = 125°C	-	-	1000	
Gate - Source leakage current	I_{GSS}	V_{GS} = ±20V, V_{DS} = 0V	-	-	±100	nA
Gate threshold voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 750μA	2	-	4	V
Static drain - source on - state resistance	$R_{DS(on)}$ *6	V_{GS} = 10V, I_D = 11.3A				Ω
		T_j = 25°C	-	0.160	0.185	
		T_j = 125°C	-	-	-	
Gate resistance	R_G	f = 1MHz, open drain	-	6.5	-	Ω

●Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Input capacitance	C_{iss}	V_{GS} = 0V	-	1650	-	pF
Output capacitance	C_{oss}	V_{DS} = 25V	-	1850	-	
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	220	-	
Turn - on delay time	$t_{d(on)}$ *6	V_{DD} ≃ 300V, V_{GS} = 10V	-	30	-	ns
Rise time	t_r *6	I_D = 12A	-	55	-	
Turn - off delay time	$t_{d(off)}$ *6	R_L ≃ 27.4Ω	-	180	-	
Fall time	t_f *6	R_G = 10Ω	-	55	-	

●Gate charge characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Total gate charge	Q_g *6	V_{DD} ≃ 300V	-	70	-	nC
Gate - Source charge	Q_{gs} *6	I_D = 24A	-	10	-	
Gate - Drain charge	Q_{gd} *6	V_{GS} = 10V	-	35	-	
Gate plateau voltage	$V_{(plateau)}$	V_{DD} ≃ 300V, I_D = 24A	-	5.8	-	V

*1 Limited only by maximum channel temperature allowed.

*2 Pw ≤ 10μs, Duty cycle ≤ 1%

*3 L≒70mH, V_{DD}=50V, R_G=25Ω, STARTING T_j=25℃

*4 T_C=25℃

*5 Mounted on an epoxy PCB FR4 (25mm x 27mm x 0.8mm)

*6 Pulsed

● **Body diode electrical characteristics** (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Source current	I_S *1	T_C = 25°C	-	-	24	A
Pulsed source current	I_{SP} *2		-	-	72	A
Source-Drain voltage	V_{SD} *6	V_{GS} = 0V, I_S = 24A	-	-	1.5	V
Reverse recovery time	t_{rr} *6	I_S = 24A di/dt = 100A/μs	-	540	-	ns
Reverse recovery charge	Q_{rr} *6		-	9.5	-	μC
Peak reverse recovery current	I_{rr} *6		-	35	-	A

●Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

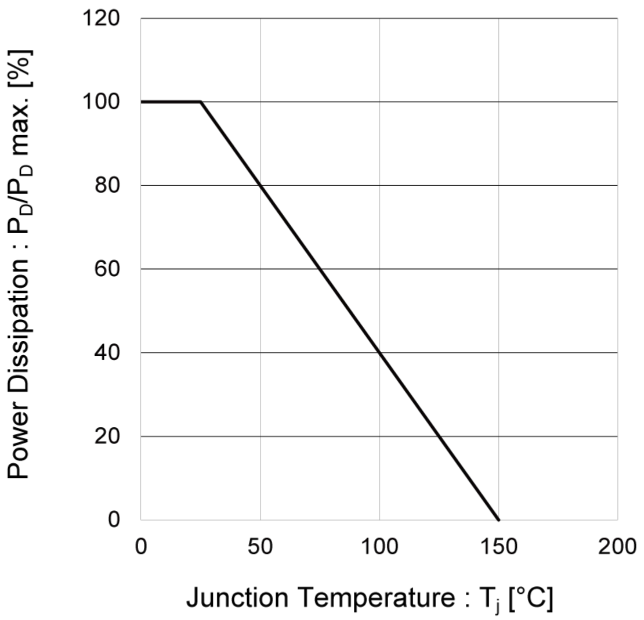


Fig.2 Drain Current Derating Curve

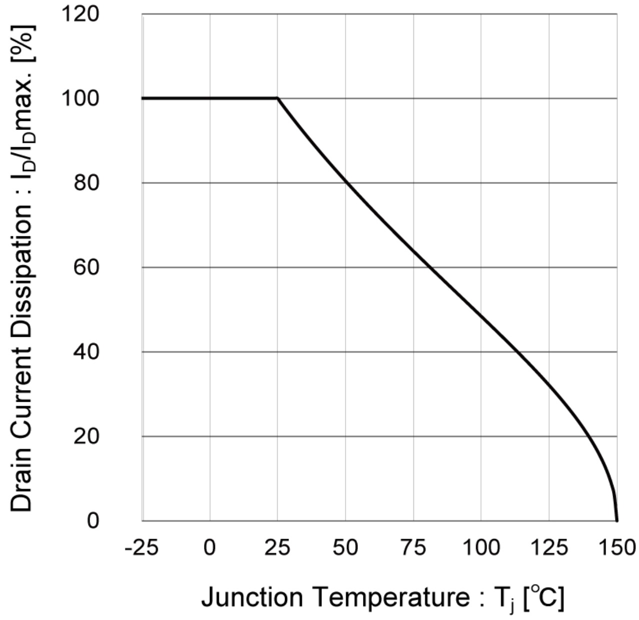


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

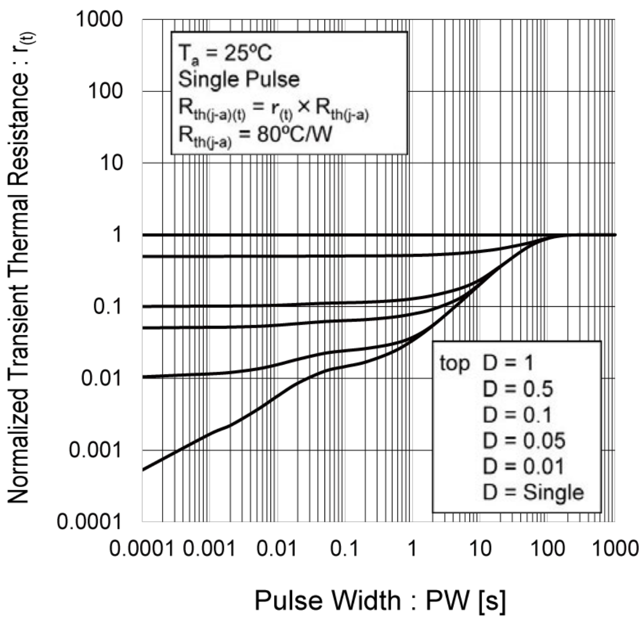
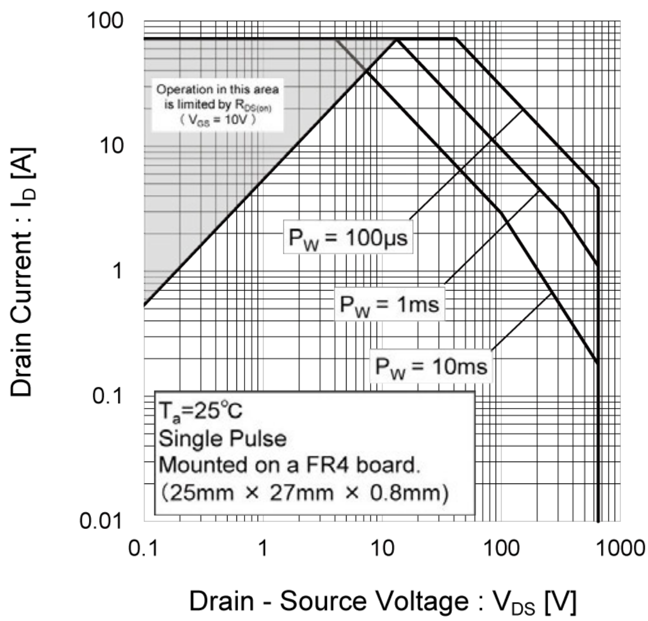


Fig.4 Maximum Safe Operating Area



●Electrical characteristic curves

Fig.5 Avalanche Energy Derating Curve vs. Junction Temperature

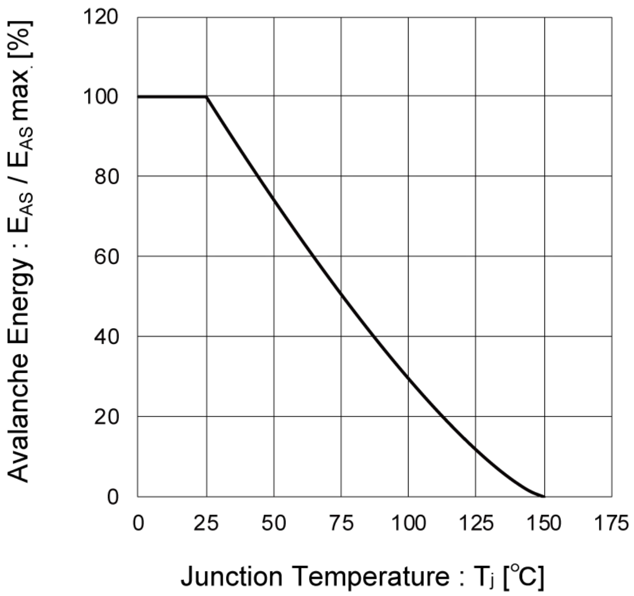


Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

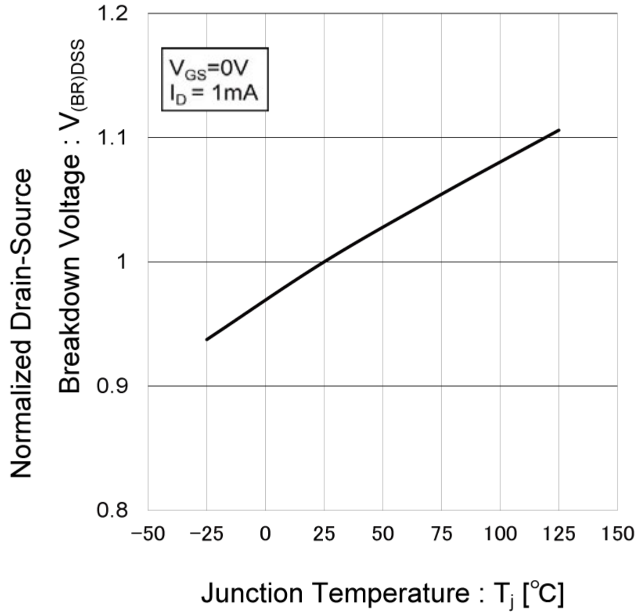


Fig.7 Typical Output Characteristics(I)

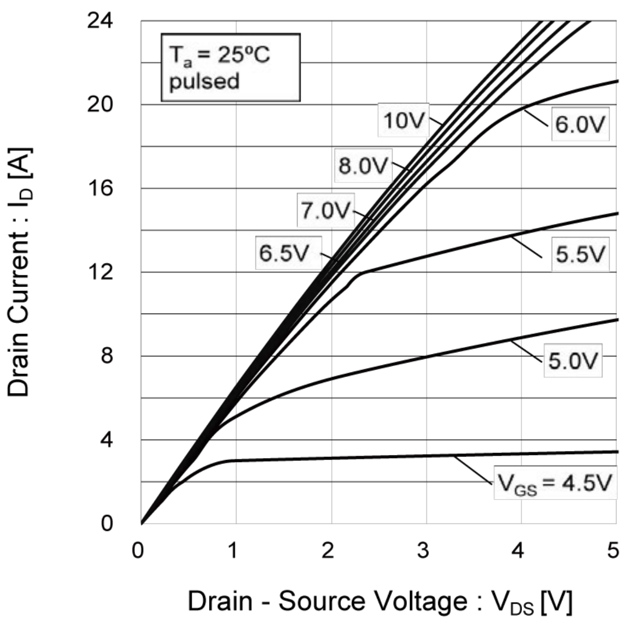
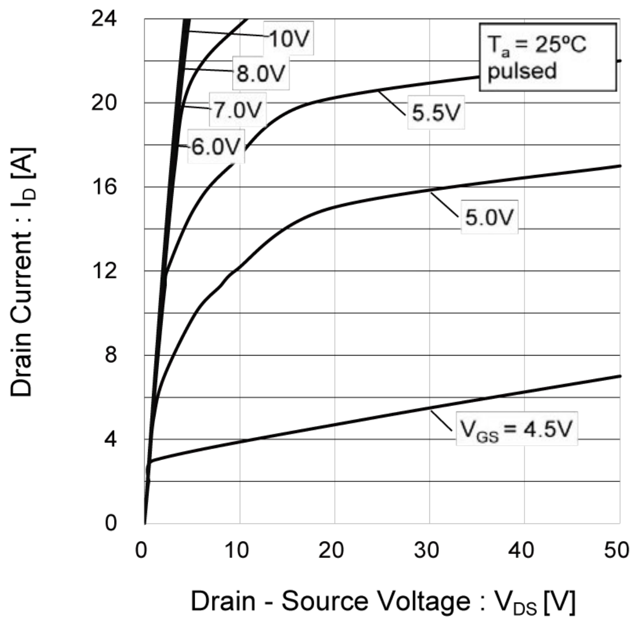


Fig.8 Typical Output Characteristics(II)



●Electrical characteristic curves

Fig.9 Typical Transfer Characteristics

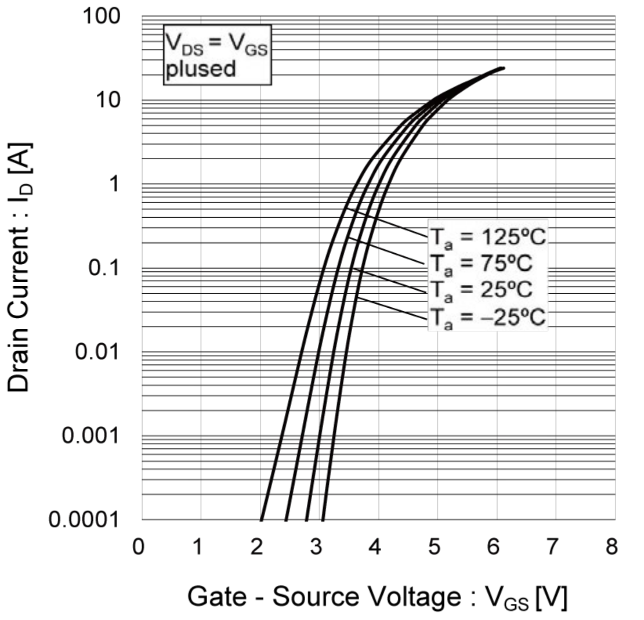


Fig.10 Normalized Gate Threshold . Voltage vs Junction Temperature

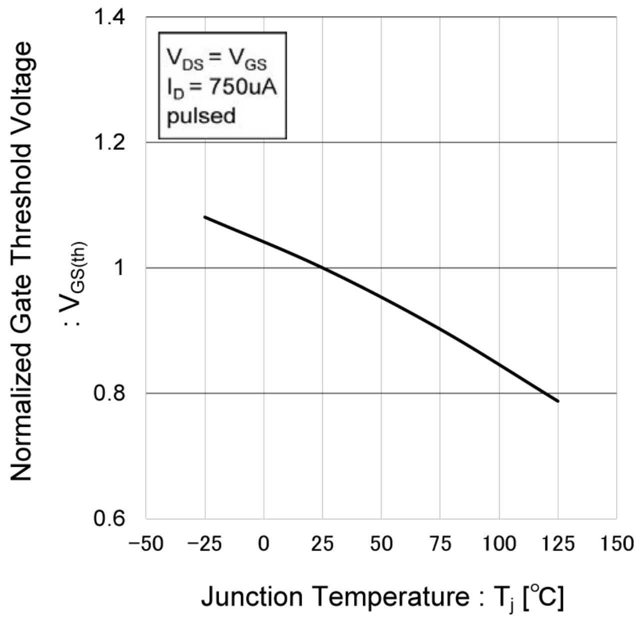


Fig.11 Static Drain - Source On - State Resistance vs. Drain Current

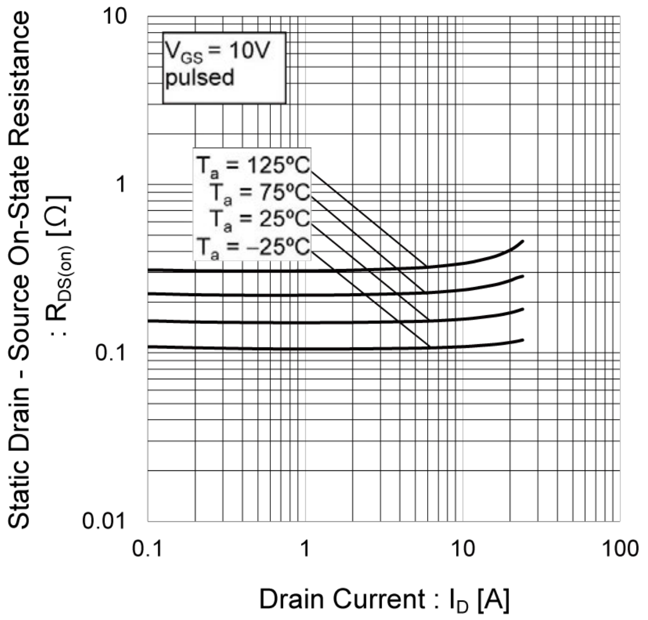
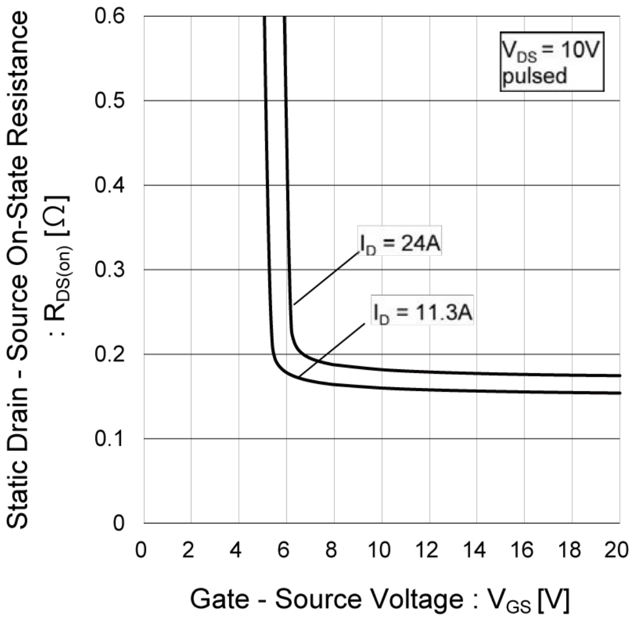


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



● **Electrical characteristic curves**

Fig.13 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature

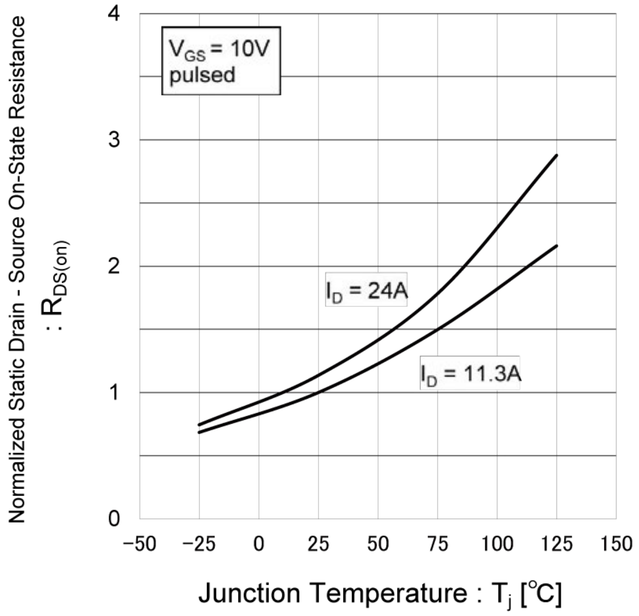


Fig.14 Typical Capacitance vs. Drain - Source Voltage

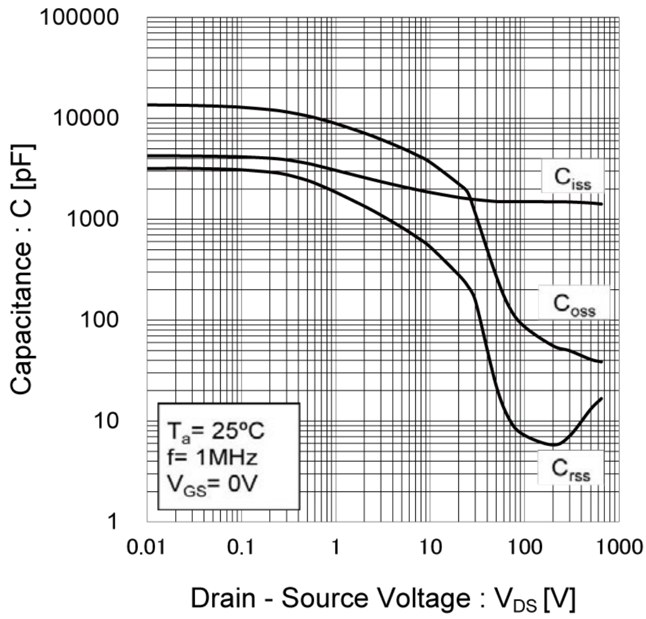


Fig.15 Switching Characteristics

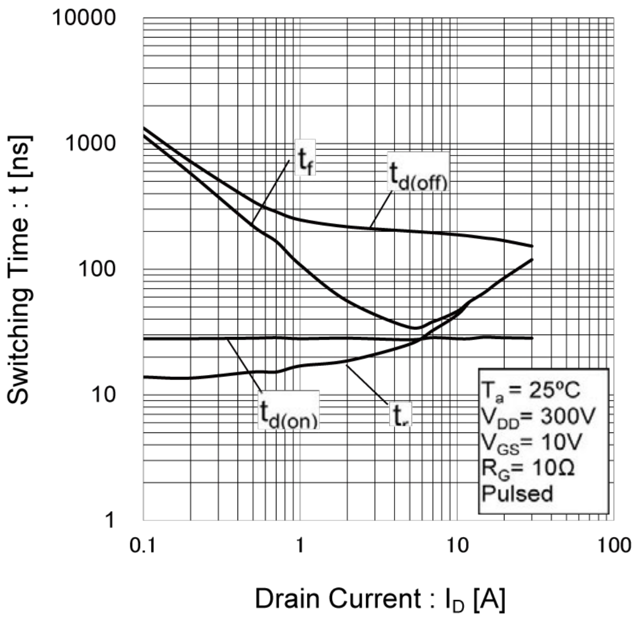
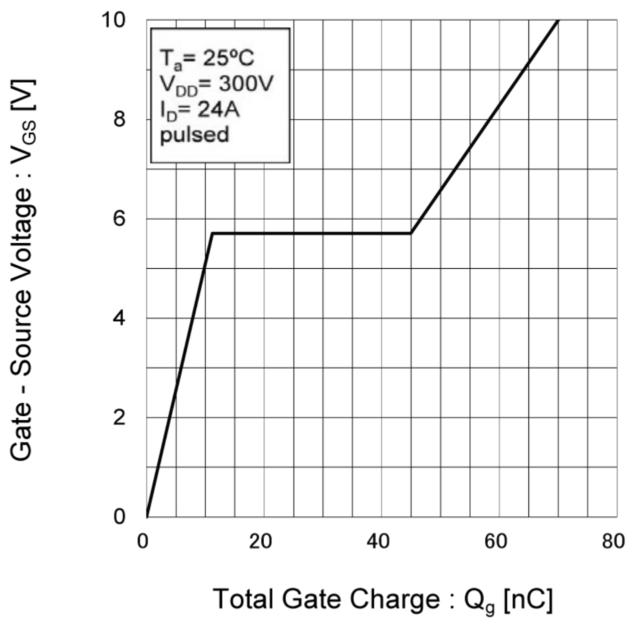


Fig.16 Typical Gate Charge



● Electrical characteristic curves

Fig.17 Source Current vs. Source - Drain Voltage

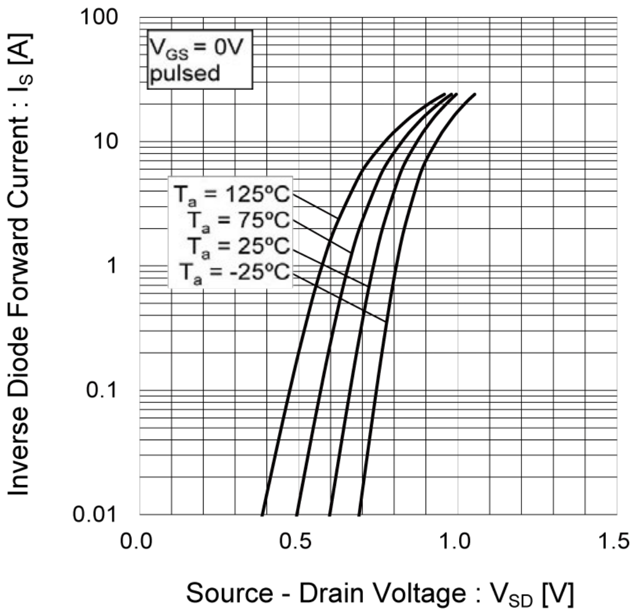
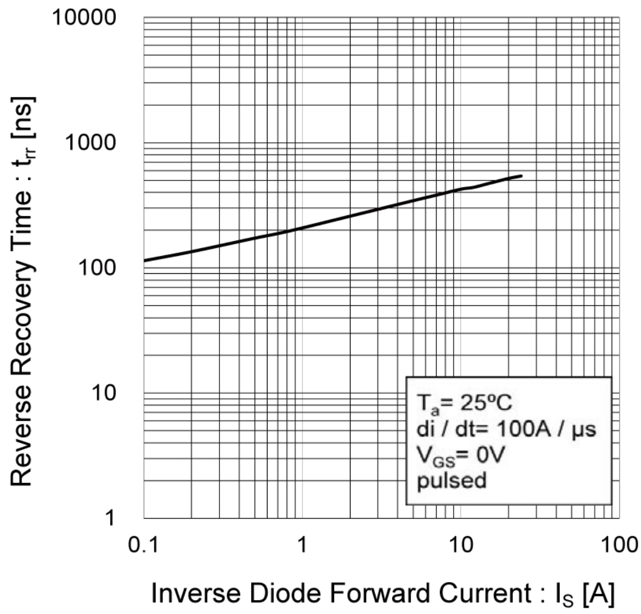


Fig.18 Reverse Recovery Time vs. Inverse Diode Forward Current



●Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

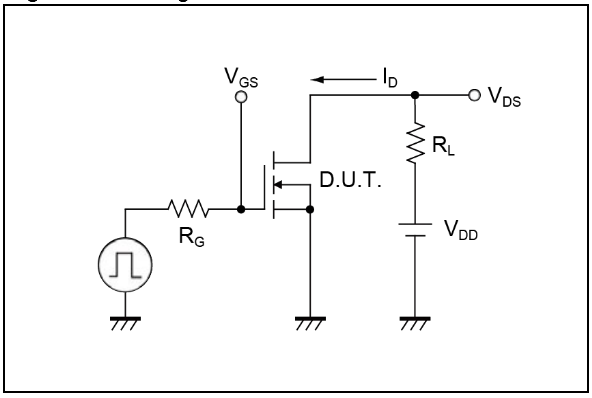


Fig.1-2 Switching Waveforms

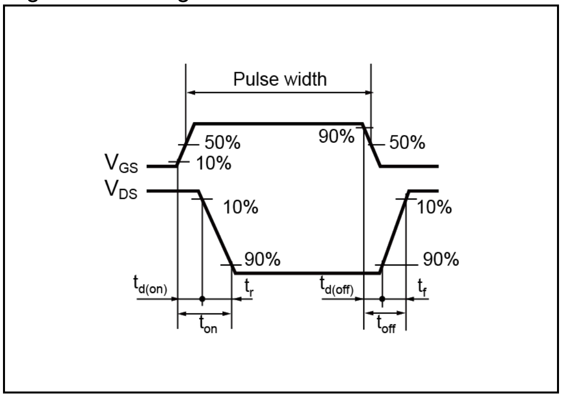


Fig.2-1 Gate Charge Measurement Circuit

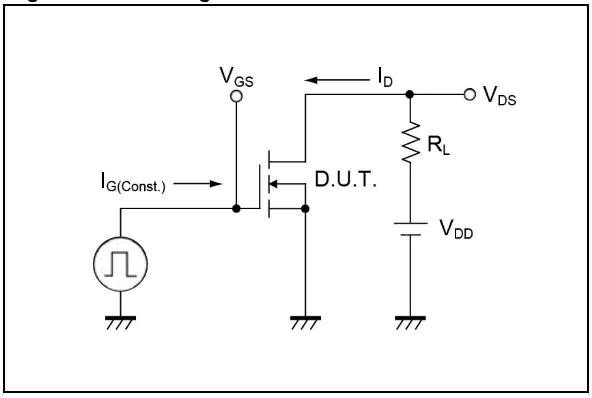


Fig.2-2 Gate Charge Waveform

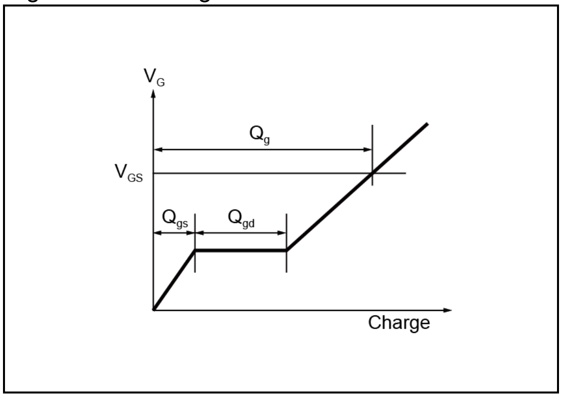


Fig.3-1 Avalanche Measurement Circuit

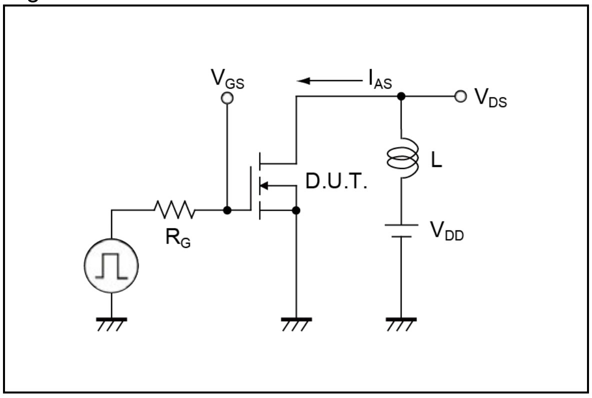
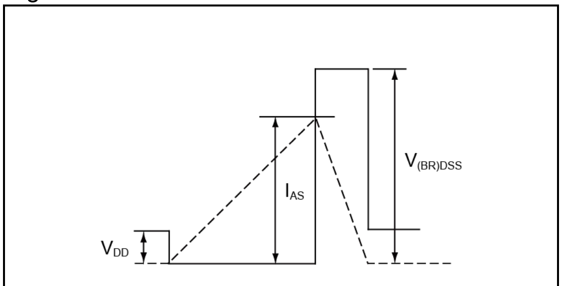


Fig.3-2 Avalanche Waveform



$$E_{AS} = \frac{1}{2} \cdot L \cdot I_{AS}^2 \cdot \frac{V_{(BR)DSS}}{V_{(BR)DSS} - V_{DD}}$$

Fig.4-1 trr Measurement Circuit

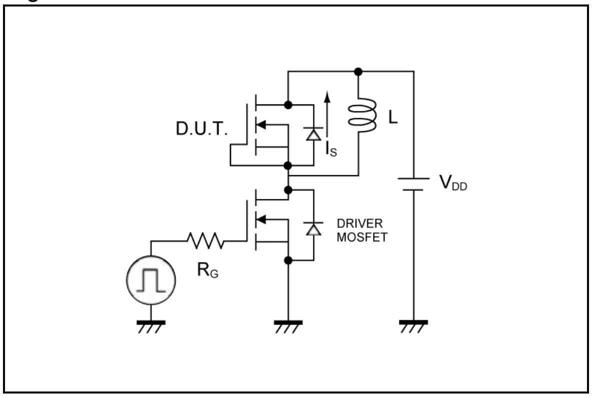
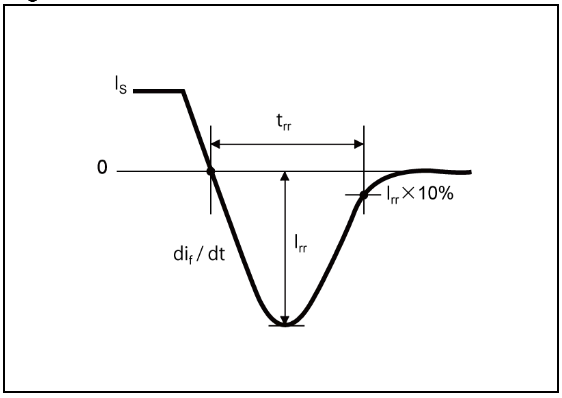
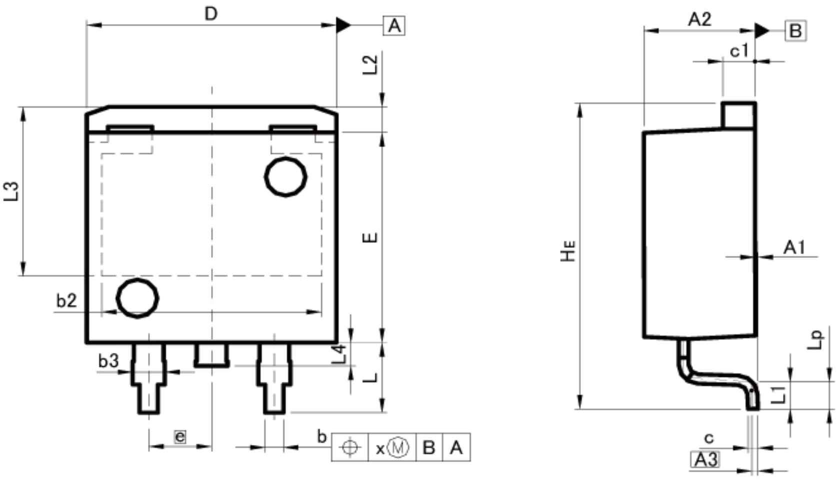


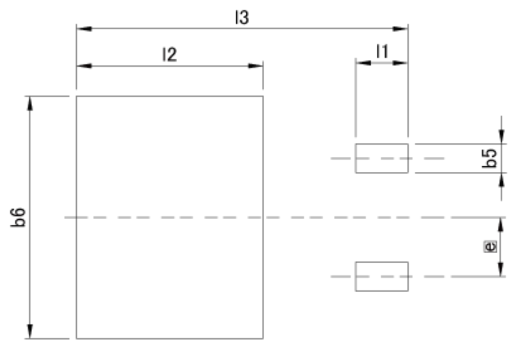
Fig.4-2 trr Waveform



●Dimensions

TO-263S
SC-83
(LPTS D2-PAK)





Pattern of terminal position areas
[Not a pattern of soldering pads]

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A1	0.00	0.30	0.000	0.012
A2	4.30	4.70	0.169	0.185
A3	0.25		0.010	
b	0.68	0.98	0.027	0.039
b2	8.90		0.350	
b3	1.14	1.44	0.045	0.057
c	0.30	0.60	0.012	0.024
c1	1.10	1.50	0.043	0.059
D	9.80	10.40	0.386	0.409
E	8.80	9.20	0.346	0.362
e	2.54		0.100	
HE	12.80	13.40	0.504	0.528
L	2.70	3.30	0.106	0.130
L1	1.20		0.047	
L2	1.10		0.043	
L3	7.25		0.285	
L4	1.00		0.039	
Lp	0.90	1.50	0.035	0.059
x	–	0.25	–	0.010

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
b5	–	1.23	–	0.049
b6	–	10.40	–	0.409
l1	–	2.10	–	0.083
l2	–	7.55	–	0.297
l3	–	13.40	–	0.528

Dimension in mm/inches

Notice

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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASSⅡb	CLASSⅢ
CLASSⅣ		CLASSⅢ	

2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
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   [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure

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   [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
   [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
   [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl_2, H_2S, NH_3, SO_2, and NO_2
   [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
   [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
   [f] Sealing or coating our Products with resin or other coating materials
   [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
   [h] Use of the Products in places subject to dew condensation

4. The Products are not subject to radiation-proof design.

5. Please verify and confirm characteristics of the final or mounted products in using the Products.

6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.

8. Confirm that operation temperature is within the specified range described in the product specification.

9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.

2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.

2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
   [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
   [b] the temperature or humidity exceeds those recommended by ROHM
   [c] the Products are exposed to direct sunshine or condensation
   [d] the Products are exposed to high Electrostatic

2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.

3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.

4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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