

## 500mA/40V Digital transistor (with built-in resistor)

Parameter	Value		
V <sub>CEO</sub>	40V		
I <sub>C</sub>	500mA		
R <sub>1</sub>	2.2kΩ		

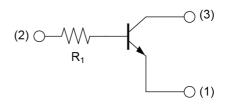
# Outline SOT-23

(SST3)

## Features

- 1) Built-In Biasing Resistors,  $R_1 = 2.2k\Omega$
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
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- 4) Complementary PNP Types: DTB123TC

## •Inner circuit



- (1) EMITTER
- (2) BASE
- (3) COLLECTOR

## Application

INVERTER, INTERFACE, DRIVER

## Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTD123TC	SOT-23 (SST3)	2924	T116	180	8	3000	F02

# ● Absolute maximum ratings (T<sub>a</sub> = 25°C)

Parameter	Symbol	Values	Unit
Collector-base voltage	$V_{CBO}$	50	V
Collector-emitter voltage	V <sub>CEO</sub>	40	V
Emitter-base voltage	V <sub>EBO</sub>	5	V
Collector current	I <sub>C</sub> *1	500	mA
Power dissipation	P <sub>D</sub> *2	200	mW
Junction temperature	T <sub>j</sub>	150	°C
Range of storage temperature	T <sub>stg</sub>	-55 to +150	°C

## ● Electrical characteristics (T<sub>a</sub> = 25°C)

Parameter	Cumbal	Conditions	Values			Lloit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Collector-base breakdown voltage	BV <sub>CBO</sub>	I <sub>C</sub> = 50μA	50	-	-	V	
Collector-emitter breakdown voltage	BV <sub>CEO</sub>	I <sub>C</sub> = 1mA	40	-	-	V	
Emitter-base breakdown voltage	BV <sub>EBO</sub>	I <sub>E</sub> = 50μA	5	-	-	V	
Collector cut-off current	I <sub>CBO</sub>	V <sub>CB</sub> = 50V	-	-	500	nA	
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> = 4V	-	-	500	nA	
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> = 50mA, I <sub>B</sub> = 2.5mA	-	-	300	mV	
DC current gain	h <sub>FE</sub> *3	V <sub>CE</sub> = 5V, I <sub>C</sub> = 50mA	100	250	600	-	
Input resistance	R <sub>1</sub>	-	1.54	2.2	2.86	kΩ	
Transition frequency	f <sub>T</sub> *1	V <sub>CE</sub> = 10V, I <sub>E</sub> = -50mA, f = 100MHz	-	200	-	MHz	

<sup>\*1</sup> Characteristics of built-in transistor.

<sup>\*2</sup> Each terminal mounted on a reference land.

<sup>\*3</sup> Pulsed

## ● Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.1 Grounded Emitter Propagation Characteristics

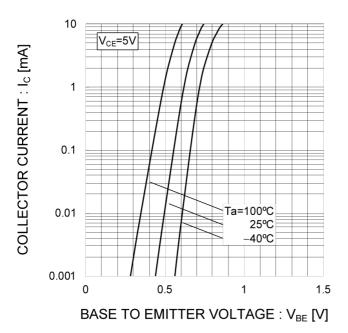
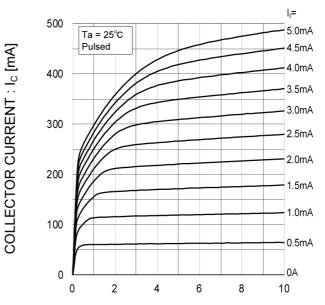


Fig.2 Grounded Emitter Output Characteristics



COLLECTOR TO EMITTER VOLTAGE: V<sub>CE</sub> [V]

Fig.3 DC Current Gain vs. Collector Current

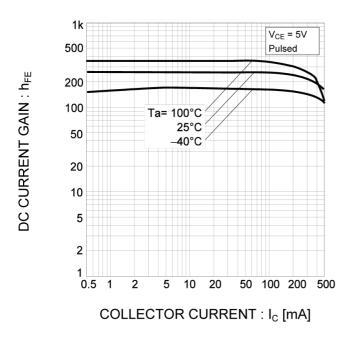
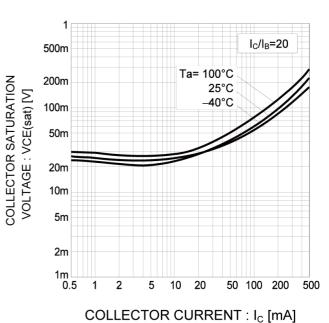
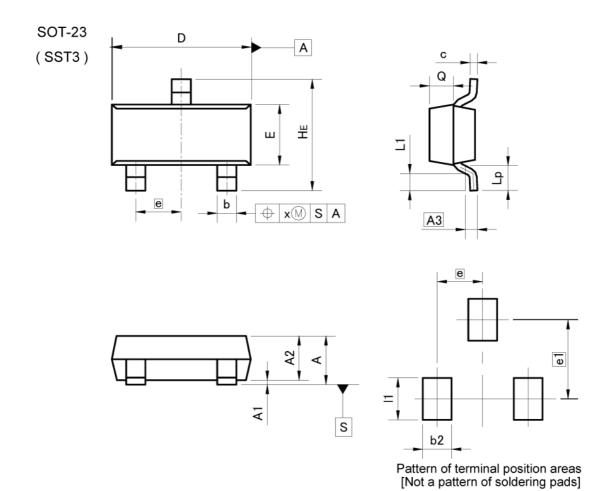


Fig.4 Collector-Emitter Saturation Voltage vs. Collector Current



## Dimensions



DIM MILIMETERS		ETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
Α	0.90	1.20	0.035	0.047
A1	0.00	0.10	0.000	0.004
A2	0.85	1.15	0.033	0.045
A3	0.3	25	0.0	10
b	0.35	0.50	0.014	0.020
С	0.09	0.25	0.004	0.010
D	2.70	3.10	0.106	0.122
E	1.20	1.50	0.047	0.059
е	0.95		0.0	37
HE	2.20	2.60	0.087	0.102
L1	0.20	2-3	0.008	_
Lp	0.30	g.=g	0.012	u=-
Q	0.40	0.60	0.016	0.024
х	- 7	0.10	-	0.004

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
b2	-	0.60	-	0.024	
e1	1.70		0.0	67	
- 11	-3	0.90	-	0.035	

Dimension in mm/inches



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JÁPAN	USA	EU	CHINA
CLASSⅢ	CL ACCIII	CLASS II b	CL ACCIII
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
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For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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