PNP -100mA -50V Digital Transistor (Bias Resistor Built-in Transistor)

Datasheet

AEC-Q101 Qualified

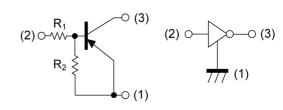
Parameter	Value
V _{CC}	-50V
I _{C(MAX.)}	-100mA
R ₁	10kΩ
R_2	47kΩ

Outline SOT-23 (SST3)

Features

- 1) Built-In Biasing Resistors, $R_1 = 10k\Omega$, $R_2 = 47k\Omega$
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 4) Complementary NPN Types: DTC114YCA HZG

•Inner circuit



- (1) GND (+) (EMITTER)
- (2) IN (BASE)
- (3) OUT (COLLECTOR)

Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTA114YCA HZG	SOT-23 (SST3)	2924	T116	180	8	3000	54

● **Absolute maximum ratings** (T_a = 25°C)

Parameter	Symbol	Values	Unit
Supply voltage	V _{CC}	-50	V
Input voltage	V _{IN}	-40 to 6	V
Output current	Io	-70	mA
Collector current	I _{C(MAX)} *1	-100	mA
Device a dispiration	P _D *2	200	mW
Power dissipation	P _D *3	350	mW
Junction temperature	Tj	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

● Electrical characteristics (T_a = 25°C)

Davamatav	Cymahal	Conditions	Values			l lm:4	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
land the state of	$V_{l(off)}$	$V_{CC} = -5V, I_{O} = -100\mu A$	-	-	-0.3	V	
Input voltage	V _{I(on)}	$V_O = -0.3V$, $I_O = -1\mu A$	-1.4	-	-		
Output voltage	V _{O(on)}	I _O = -5mA, I _I = -0.25mA	-	-100	-300	mV	
Input current $I_1 V_1 = -5V$		V _I = -5V	-	-	-880	μA	
Output current	I _{O(off)}	$V_{CC} = -50V, V_{I} = 0V$	-	-	-500	nA	
DC current gain	G _I	$V_{O} = -5V, I_{O} = -5mA$	68	-	1	-	
Input resistance	R ₁	-	7	10	13	kΩ	
Resistance ratio	R ₂ /R ₁	-	3.7	4.7	5.7	-	
Transition frequency f_T^{*1}		V _{CE} = -10V, I _E = 5mA, f = 100MHz	-	250	-	MHz	

^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a reference land.

^{*3} Mounted on a ceramic board(7.0×5.0×0.6mm).

● Electrical characteristic curves (T_a =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)

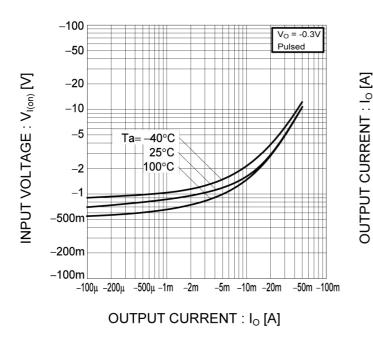
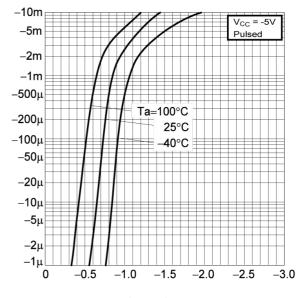


Fig.2 Output current vs. input voltage (OFF characteristics)



INPUT VOLTAGE : $V_{I(off)}$ [V]

Fig.3 Output current vs. output voltage

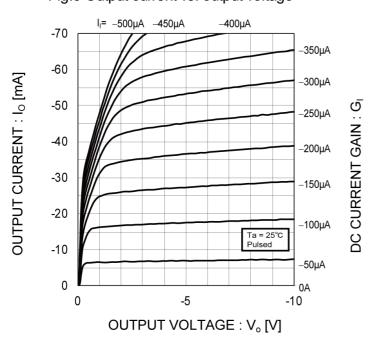
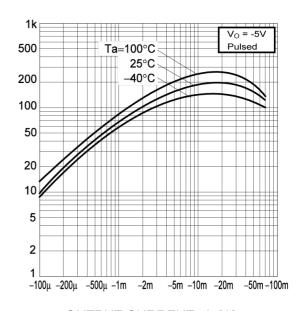


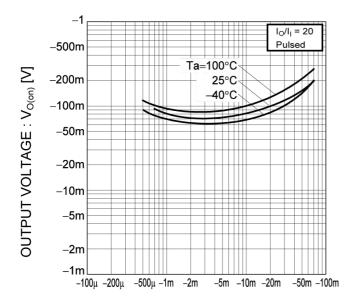
Fig.4 DC current gain vs. output current



OUTPUT CURRENT : Io [A]

●Electrical characteristic curves (T_a =25°C)

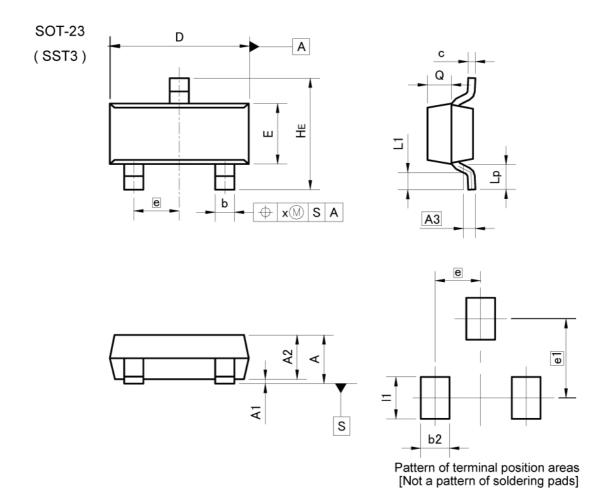
Fig.5 Output voltage vs. output current



OUTPUT CURRENT : Io [A]

4/5

Dimensions



DIM	MILIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	0.90	1.20	0.035	0.047	
A1	0.00	0.10	0.000	0.004	
A2	0.85	1.15	0.033	0.045	
A3	0.3	25	0.0	10	
b	0.35	0.50	0.014	0.020	
С	0.09	0.25	0.004	0.010	
D	2.70	3.10	0.106	0.122	
E	1.20	1.50	0.047	0.059	
е	0.9	95	0.0	37	
HE	2.20	2.60	0.087	0.102	
L1	0.20	00	0.008	_	
Lp	0.30	p.=.;	0.012	u=-	
Q	0.40	0.60	0.016	0.024	
х	-0	0.10	-	0.004	

DIM	MILIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
b2	-	0.60	_	0.024	
e1	1.	70	0.067		
- 11	-3	0.90	-	0.035	

Dimension in mm/inches

Notice

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JAPAN	USA	EU	CHINA
CLASSⅢ	OL ACOM	CLASS II b	ОГУООШ
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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