Application

- · Motor drive
- · Converter
- · Photovoltaics, wind power generation.

Features

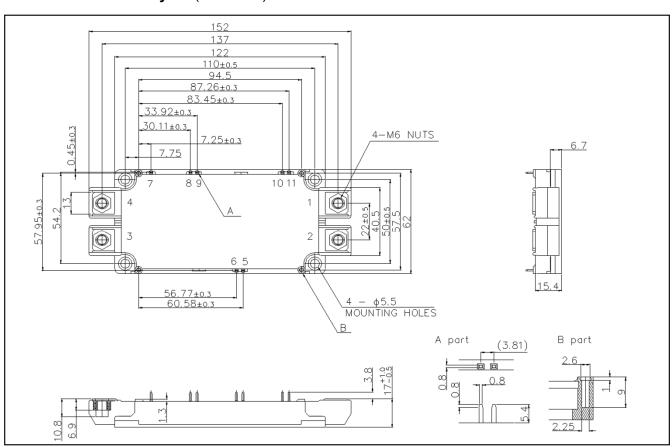
- 1) Low surge, low switching loss.
- 2) High-speed switching possible.
- 3) Reduced temperature dependence.

*Do not connect anything to NC pin.

Construction

This product is a chopper module consisting of SiC-UMOSFET and SiC-SBD from ROHM.

●Dimensions & Pin layout (Unit : mm)



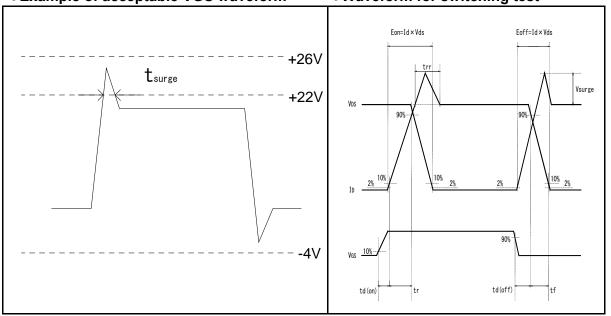
•Absolute maximum ratings $(T_j = 25^{\circ}C)$

Parameter	Symbol	Conditions	Limit	Unit	
Drain-source voltage	V_{DSS}	G-S short	1200		
Repetitive reverse voltage	V_{DSS}	Clamp diode	1200		
Gate-source voltage(+)	V		22	V	
Gate-source voltage(-)	V_{GSS}	D-S short	-4		
G - S Voltage (t _{surge} <300nsec)	$V_{GSSsurge}$		-4 to 26		
Drain current *1	I_{D}	DC (T _c =60°C)	300		
	I_{DRM}	Pulse (T _c =60°C) 1ms *2	600		
Source current *1	I _S	DC (T_c =60°C) V_{GS} =18V	300		
	I _{SRM}	I _{SRM} Pulse (Tc=60°C) 1ms V _{GS} =18V * ²		Α	
	I _{SRM}	Pulse (Tc=60°C) 10 μ s V _{GS} =0V * ²	600		
Forward curent	I _F	DC (T _c =60°C) V _{GS} =18V	300		
(clamp diode) *1	I _{FRM}	Pulse (Tc=60°C) 1ms V _{GS} =18V * ²	600		
Total power disspation *3	Ptot	T _c =25°C	1360	W	
Max Junction Temperature	T _{jmax}		175		
Junction temperature	T_jop		-40 to 150	°C	
Storage temperature	T _{stg}		-40 to 125		
Isolation voltage	Visol	Terminals to baseplate, f=60Hz AC 1min.	2500	Vrms	
Mounting torque		Main Terminals : M6 screw	4.5	NI ma	
Mounting torque	_	Mounting to heat shink: M5 screw	3.5	N · m	

^(*1) Case temperature (T_c) is defined on the surface of base plate just under the chips.

●Example of acceptable VGS waveform

Waveform for switching test



^(*2) Repetition rate should be kept within the range where temperature rise if die should not exceed T_{i max}.

^(*3) T_i is less than 175°C

●Electrical characteristics (T_i=25°C)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
Static drain-source on-state voltage	V _{DS(on)}	I _D =300A, V _{GS} =18V	T _j =25°C	-	1.9	3.0	V
			T _j =125°C	1	2.7	-	
			T _j =150°C	-	3	4.5	
Drain cutoff current	I _{DSS}	V _{DS} =1200V, V _{GS} =0V		-	-	10	μΑ
Forwad Voltag	V _F	I _F =300A	T _j =25°C	-	1.6	2.1	V
			T _j =125°C		2.2	-	
			T _j =150°C	ı	2.3	3.2	
Reverse curent	I_{RRM}	Clamp diode		1	-	3.2	mA
Gate-source threshold voltage	$V_{GS(th)}$	V _{DS} =10V, I _D =80mA		2.7	-	5.6	V
Gate-source leakage current	I _{GSS}	V_{GS} =22V, V_{DS} =0V		1	-	0.5	μА
		$V_{GS} = -6V, V_{DS} = 0V$		-0.5	-	-	
Switching characteristics	t _{d(on)}	$V_{GS(on)}=18V, V_{GS(off)}=0V$ - 40			-		
	t _r	V _{DS} =600V		ı	35	-	ns
	t _{rr}	I _D =300A	ı	6	-		
	$t_{d(off)}$	R_G =1.8 Ω inductive load		ı	155	ı	
	t _f			ı	40	-	
Input capacitance	Ciss	V_{DS} =10V, V_{GS} =0V,100kHz		ı	15	-	nF
Gate Registance	R_{Gint}	T _j =25°C		ı	0.9	-	Ω
NTC Rated Resistance	R25				5.0		kΩ
NTC B Value	B50/25				3370		K
Stray Inductance	Ls				13	-	nΗ
Creepage Distance	1	Terminal to heat sink			14.5	-	mm
		Terminal to terminal			15.0	-	mm
Clearance Distance	-	Terminal to heat sink			12.0	-	mm
		Terminal to terminal			9.0	-	mm
Junction-to-case thermal resistance	R _{th} (j-c)	UMOS (1/2 module) *4		ı	-	0.11	°C/W
		SBD (1/2 module) *4		ı	-	0.11	
Case-to-heat sink	R _{th} (c-f)	Case to heat sink, per 1 module,			0.035		
Thermal resistance	i th(C ³ I)	Thermal grease applie	ed * ⁵	- 0.035		-	

^(*4) Measurement of Tc is to be done at the point just under the chip.

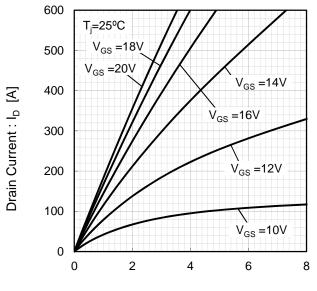
^(*5) Typical value is measured by using thermally conductive grease of λ =0.9W/(m·K).

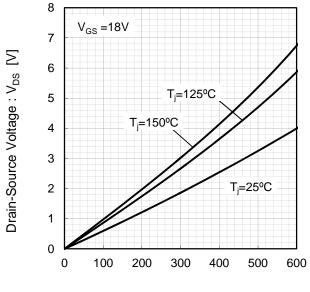
^(*6) SiC devices have lower short cuicuit withstand capability due to high current density.

Please be advised to pay careful attention to short cuicuit accident and try to adjust protection time to shutdown them as short as possible.

^(*7) If the Product is used beyond absolute maximum ratings defined in the Specifications, as its internal structure may be dameged, please replace such Product with a new one.

Fig.1 Typical Output Characteristics [T_j =25°C] Fig.2 Drain-Source Voltage vs. Drain Current





Drain-Source Voltage : V_{DS} [V]

Drain Current : I_D [A]

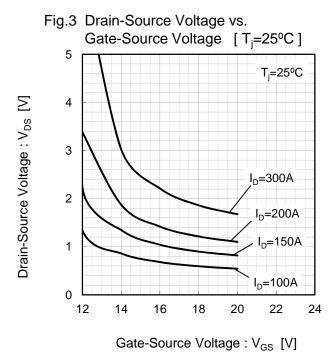
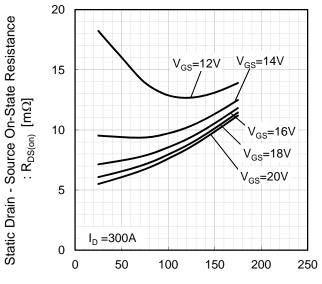


Fig.4 Static Drain - Source On-State Resistance vs. Junction Temperature



Junction Temperature : T_i [°C]

Fig.5 Forward characteristic of Diode

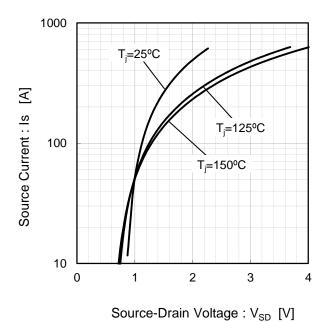
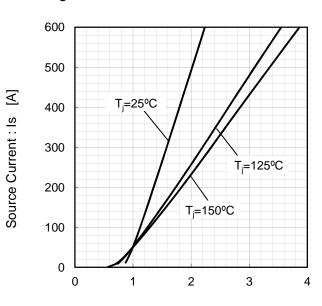


Fig.6 Forward characteristic of Diode



Source-Drain Voltage : V_{SD} [V]

Fig.7 Drain Current vs. Gate-Source Voltage

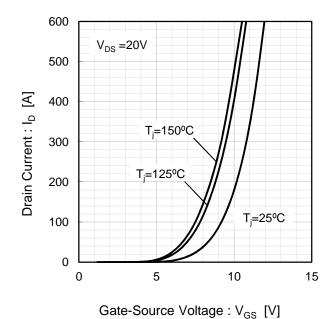
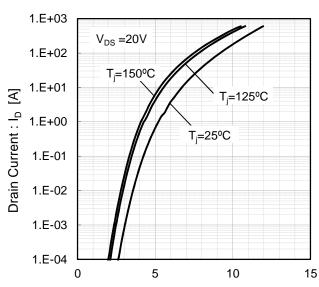


Fig.8 Drain Current vs. Gate-Source Voltage



Gate-Source Voltage : V_{GS} [V]

Fig.9 Switching Characteristics [T_i=25°C]

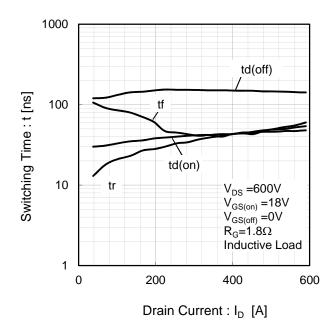


Fig.10 Switching Characteristics [T_i=125°C]

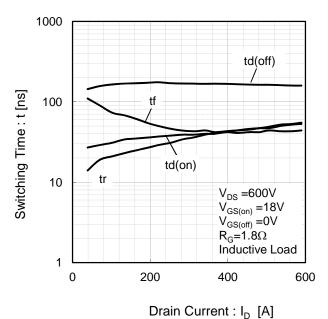


Fig.11 Switching Characteristics [T_i=150°C]

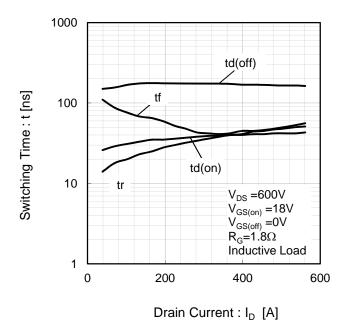
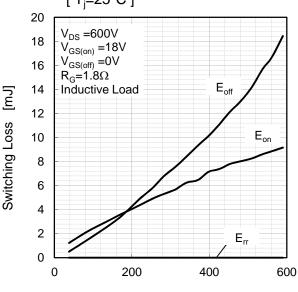
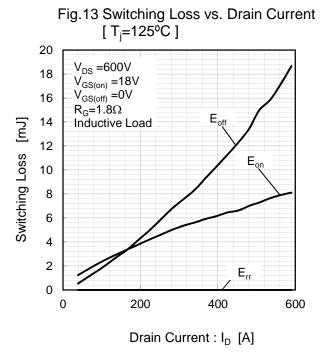
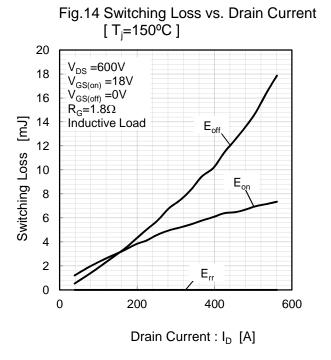
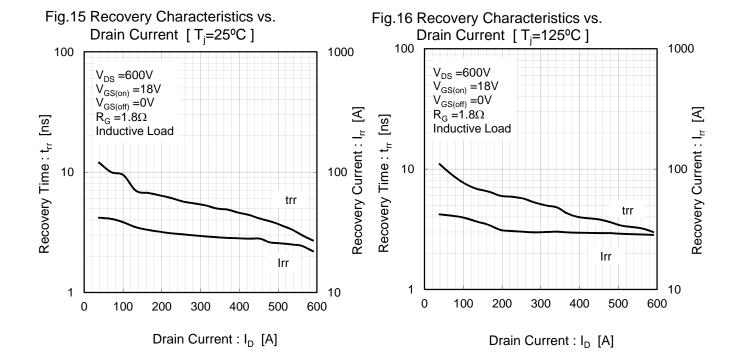


Fig.12 Switching Loss vs. Drain Current [$T_i=25^{\circ}C$]









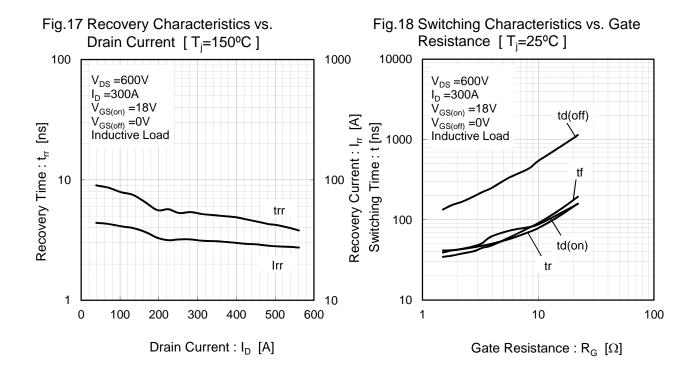


Fig.19 Switching Characteristics vs. Gate Resistance [T_i=125°C]

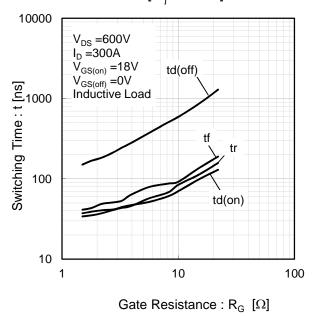
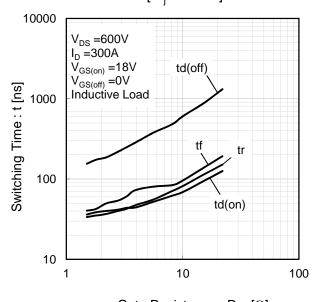
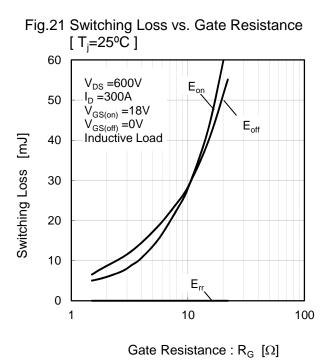


Fig.20 Switching Characteristics vs. Gate Resistance [T_i=150°C]





Gate Resistance : R_G [Ω]

Switching Loss [mJ]

Fig.23 Switching Loss vs. Gate Resistance $[T_i=150^{\circ}C]$ 60 V_{DS} =600V $I_{D} = 300A$ 50 $V_{GS(on)} = 18V$ $V_{GS(off)} = 0V$ Inductive Load Switching Loss [mJ] 40 30 20 E_{on} 10 E_{rr} 0 10 100 Gate Resistance : R_G [Ω]

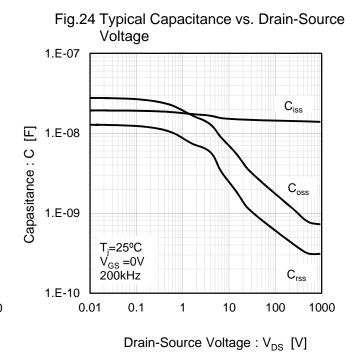


Fig.25 Gate Charge Characteristics

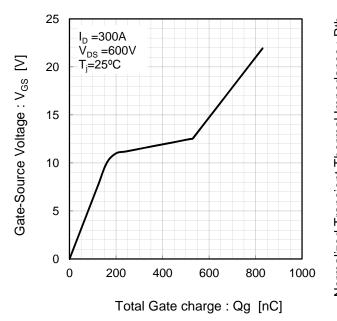
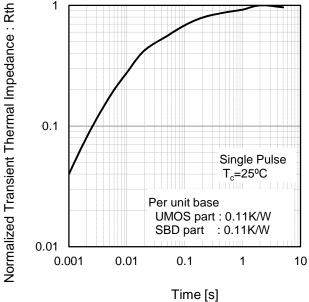


Fig.26 Normalized Transient Thermal Impedance 1



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