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# ON Semiconductor® TIP42 / TIP42C PNP Epitaxial Silicon Transistor

### Features

- Medium Power Linear Switching Applications
- Complement to TIP41 Series



1.Base 2.Collector 3.Emitter

### **Ordering Information**

Part Number	Top Mark	Package	Packing Method
TIP42	TIP42	TO-220 3L (Single Gauge)	Bulk
TIP42C	TIP42C	TO-220 3L (Single Gauge)	Bulk
TIP42CTU	TIP42C	TO-220 3L (Single Gauge)	Rail

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_c = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter		Value	Unit	
V <sub>CBO</sub>		TIP42	-40	V	
	Collector-Base Voltage	TIP42C	-100		
V <sub>CEO</sub>		TIP42	-40	- V	
	Collector-Emitter Voltage	TIP42C	-100		
V <sub>EBO</sub>	Emitter-Base Voltage		-5	V	
۱ <sub>C</sub>	Collector Current (DC)		-6	A	
I <sub>CP</sub>	Collector Current (Pulse)		-10	A	
I <sub>B</sub>	Base Current		-2	А	
Т <sub>Ј</sub>	Junction Temperature		150	°C	
T <sub>STG</sub>	Storage Temperature Range		-65 to 150	°C	

### **Thermal Characteristics**

Values are at  $T_C = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Value	Unit
P <sub>C</sub>	Collector Dissipation ( $T_C = 25^{\circ}C$ )	65	W
	Collector Dissipation ( $T_A = 25^{\circ}C$ )	2	vv

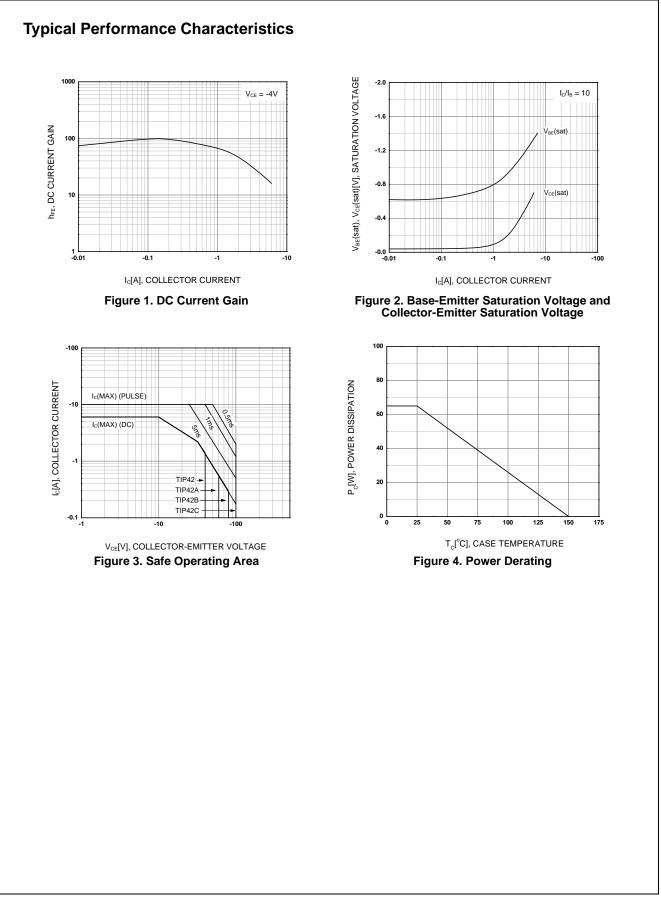
### **Electrical Characteristics**

Values are at  $T_C = 25^{\circ}C$  unless otherwise noted.

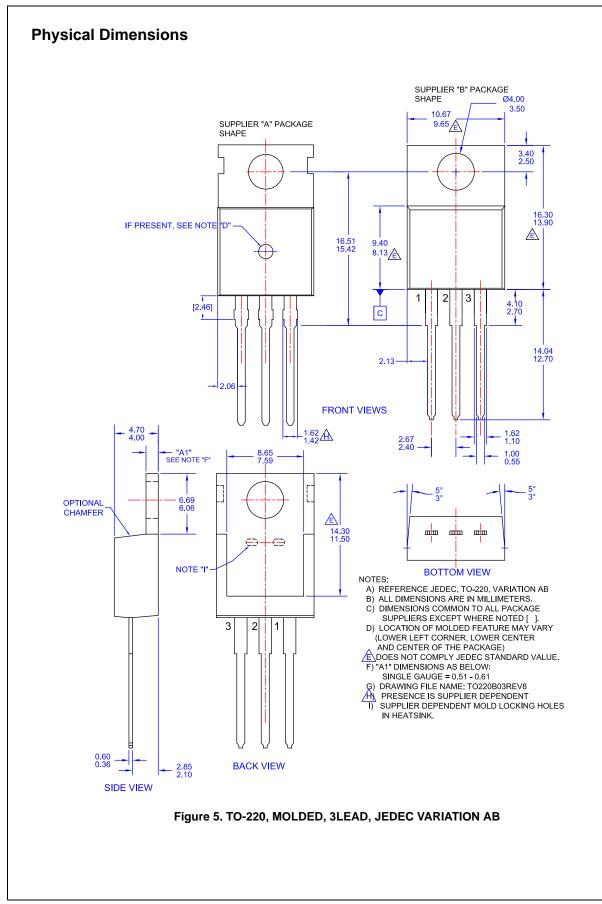
Symbol	Parameter		Conditions	Min.	Max.	Unit
V <sub>CEO</sub> (sus)	Collector-Emitter Sustaining Voltage <sup>(1)</sup>	TIP42	- I <sub>C</sub> = -30 mA, I <sub>B</sub> = 0	-40		V
		TIP42C		-100		
I <sub>CEO</sub>	Collector Cut-Off Current	TIP42	$V_{CE} = -30 \text{ V}, \text{ I}_{B} = 0$		-0.7	mA
		TIP42C	$V_{CE} = -60 \text{ V}, \text{ I}_{B} = 0$		-0.7	
I <sub>CES</sub>	Collector Cut-Off Current	TIP42	$V_{CE} = -40 \text{ V}, \text{ V}_{EB} = 0$		-400	μA
		TIP42C	$V_{CE} = -100 \text{ V}, \text{ V}_{EB} = 0$		-400	
I <sub>EBO</sub>	Emitter Cut-Off Current		$V_{EB} = -5 V, I_{C} = 0$		-1	mA
h <sub>FE</sub>	DC Current Gain <sup>(1)</sup>		$V_{CE} = -4 \text{ V}, \text{ I}_{C} = -0.3 \text{ A}$	30		
			$V_{CE} = -4 \text{ V}, \text{ I}_{C} = -3 \text{ A}$	15	75	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage <sup>(1)</sup>		I <sub>C</sub> = -6 A, I <sub>B</sub> = -600 mA		-1.5	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage <sup>(1)</sup>		$V_{CE} = -4 V, I_{C} = -6 A$		-2.0	V
f <sub>T</sub>	Current Gain Bandwidth Product		$V_{CE} = -10 \text{ V}, I_{C} = -500 \text{ mA},$ f = 1 MHz	3.0		MHz

### Note:

1. Pulse test: pw  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2%.



TIP42 / TIP42C — PNP Epitaxial Silicon Transistor



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