

# **High Voltage Transistor**

# **PNP Silicon**

# MMBT5401L, SMMBT5401L, NSVMMBT5401L

#### **Features**

- S and NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	V <sub>CEO</sub>	-150	Vdc
Collector - Base Voltage	V <sub>CBO</sub>	-160	Vdc
Emitter - Base Voltage	V <sub>EBO</sub>	-5.0	Vdc
Collector Current - Continuous	I <sub>C</sub>	-500	mAdc

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL CHARACTERISTICS

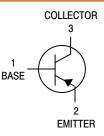
Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1) T <sub>A</sub> = 25°C	P <sub>D</sub>	225	mW
Derate Above 25°C		1.8	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate (Note 2) T <sub>A</sub> = 25°C	P <sub>D</sub>	300	mW
Derate Above 25°C		2.4	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	417	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

1

- 1. FR-5 =  $1.0 \times 0.75 \times 0.062$  in.
- 2. Alumina = 0.4  $\times$  0.3  $\times$  0.024 in 99.5% alumina.



SOT-23 (TO-236) CASE 318 STYLE 6



#### **MARKING DIAGRAM**



2L = Specific Device Code

M = Date Code\*

= Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or overbar may vary depending upon manufacturing location.

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MMBT5401LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel
SMMBT5401LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel
MMBT5401LT3G	SOT-23 (Pb-Free)	10,000 / Tape & Reel
NSVMMBT5401LT3G	SOT-23 (Pb-Free)	10,000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector – Emitter Breakdown Voltage $(I_C = -1.0 \text{ mAdc}, I_B = 0)$	V <sub>(BR)</sub> CEO	-150	_	Vdc
Collector – Base Breakdown Voltage ( $I_C = -100 \mu Adc, I_E = 0$ )	V <sub>(BR)CBO</sub>	-160	-	Vdc
Emitter – Base Breakdown Voltage ( $I_E = -10 \mu Adc, I_C = 0$ )	V <sub>(BR)EBO</sub>	-5.0	-	Vdc
Collector–Base Cutoff Current $(V_{CB} = -120 \text{ Vdc}, I_E = 0)$ $(V_{CB} = -120 \text{ Vdc}, I_E = 0, T_A = 100^{\circ}\text{C})$	Ісво	- -	-50 -50	nAdc μAdc
ON CHARACTERISTICS				
DC Current Gain $ \begin{aligned} &(I_C = -1.0 \text{ mAdc, } V_{CE} = -5.0 \text{ Vdc)} \\ &(I_C = -10 \text{ mAdc, } V_{CE} = -5.0 \text{ Vdc)} \\ &(I_C = -50 \text{ mAdc, } V_{CE} = -5.0 \text{ Vdc)} \end{aligned} $	h <sub>FE</sub>	50 60 50	- 240 -	-
Collector – Emitter Saturation Voltage ( $I_C = -10$ mAdc, $I_B = -1.0$ mAdc) ( $I_C = -50$ mAdc, $I_B = -5.0$ mAdc)	V <sub>CE(sat)</sub>	- -	-0.2 -0.5	Vdc
Base – Emitter Saturation Voltage ( $I_C = -10$ mAdc, $I_B = -1.0$ mAdc) ( $I_C = -50$ mAdc, $I_B = -5.0$ mAdc)	V <sub>BE(sat)</sub>	- -	-1.0 -1.0	Vdc
SMALL-SIGNAL CHARACTERISTICS				
Current – Gain — Bandwidth Product (I <sub>C</sub> = -10 mAdc, V <sub>CE</sub> = -10 Vdc, f = 100 MHz)	f <sub>T</sub>	100	300	MHz
Output Capacitance (V <sub>CB</sub> = -10 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>obo</sub>	-	6.0	pF
Small Signal Current Gain ( $I_C = -1.0 \text{ mAdc}$ , $V_{CE} = -10 \text{ Vdc}$ , $f = 1.0 \text{ kHz}$ )	h <sub>fe</sub>	40	200	_
Noise Figure (I <sub>C</sub> = -200 $\mu$ Adc, V <sub>CE</sub> = -5.0 Vdc, R <sub>S</sub> = 10 $\Omega$ , f = 1.0 kHz)	NF	_	8.0	dB

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

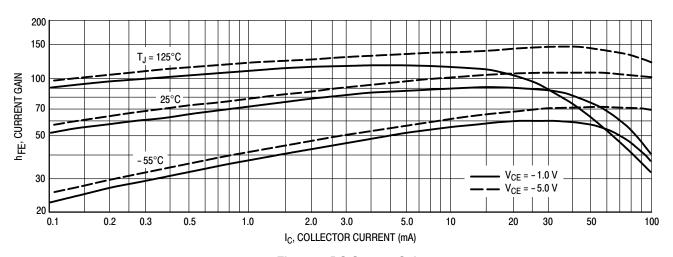


Figure 1. DC Current Gain

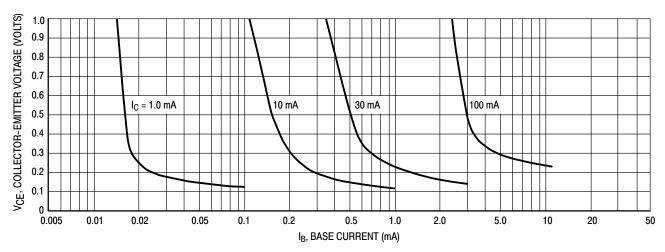


Figure 2. Collector Saturation Region

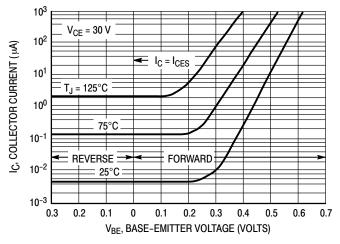


Figure 3. Collector Cut-Off Region

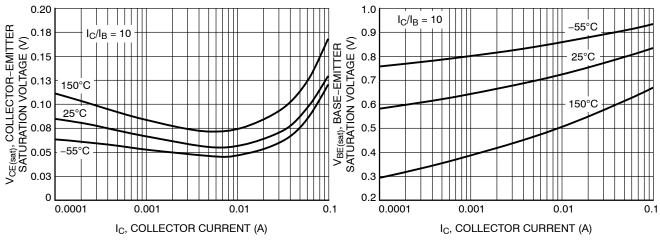


Figure 4. Collector Emitter Saturation Voltage vs. Collector Current

Figure 5. Base Emitter Saturation Voltage vs. Collector Current

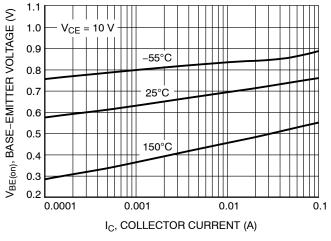


Figure 6. Base Emitter Voltage vs. Collector Current

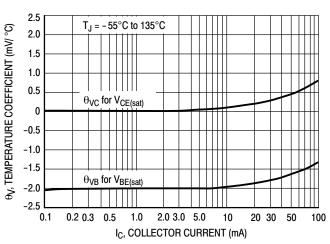


Figure 7. Temperature Coefficients

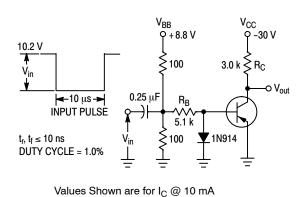


Figure 8. Switching Time Test Circuit

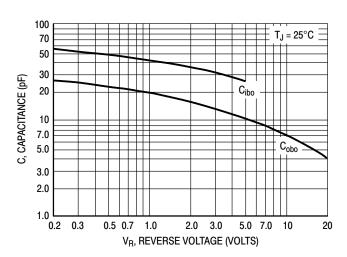
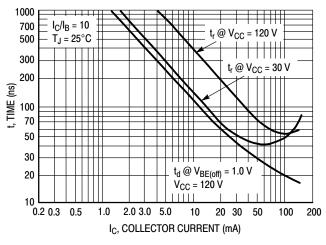


Figure 9. Capacitances



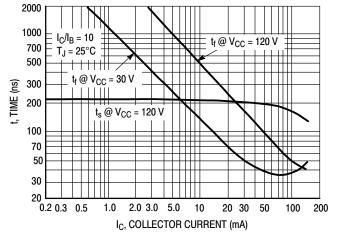
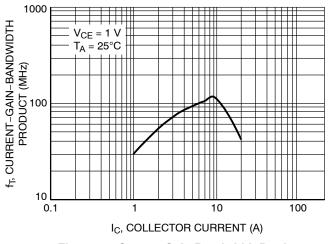
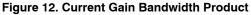


Figure 10. Turn-On Time

Figure 11. Turn-Off Time





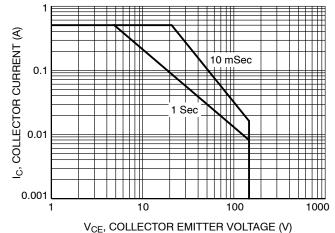


Figure 13. Safe Operating Area

**MILLIMETERS** 

MIN

0.89

0.01

0.37

0.08

2.80

1.20

1.78

0.30

0.35

2.10

O°

NOM

1.00

0.06

0.44

0.14

2.90

1.30

1.90

0.43

0.54

2.40

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#### SOT-23 (TO-236) 2.90x1.30x1.00 1.90P **CASE 318 ISSUE AU**

**DATE 14 AUG 2024** 

MAX

1.11

0.10

0.50

0.20

3.04

1.40

2.04

0.55

0.69

2.64

10°

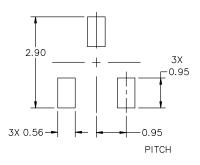




DETAIL "A" Scale 3:1







#### NOTES:

DIM

Α

Α1

b

С

D

Ε

е L

L1

HE

Τ

- DIMENSIONING AND TOLERANCING 1. PER ASME Y14.5M, 2018. CONTROLLING DIMENSIONS:
- MILLIMETERS.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE
- BASE MATERIAL.
  DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

## **GENERIC MARKING DIAGRAM\***



XXX = Specific Device Code

= Date Code

= Pb-Free Package

# RECOMMENDED MOUNTING FOOTPRINT

\* For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

#### **STYLES ON PAGE 2**

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<sup>\*</sup>This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking.

# SOT-23 (TO-236) 2.90x1.30x1.00 1.90P CASE 318 ISSUE AU

DATE 14 AUG 2024

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR			
STYLE 9: PIN 1. ANODE 2. ANODE 3. CATHODE	STYLE 10: PIN 1. DRAIN 2. SOURCE 3. GATE	2. CATHODE 2.	2: STYLE 13: CATHODE PIN 1. SOURCE CATHODE 2. DRAIN ANODE 3. GATE	STYLE 14: PIN 1. CATHODE 2. GATE 3. ANODE
STYLE 15: PIN 1. GATE 2. CATHODE 3. ANODE	STYLE 16: PIN 1. ANODE 2. CATHODE 3. CATHODE	2. ANODE 2.	3: STYLE 19: NO CONNECTION PIN 1. CATHODE CATHODE 2. ANODE ANODE 3. CATHODE-ANODE	STYLE 20: PIN 1. CATHODE 2. ANODE 3. GATE
STYLE 21: PIN 1. GATE 2. SOURCE 3. DRAIN	STYLE 22: PIN 1. RETURN 2. OUTPUT 3. INPUT			STYLE 26: PIN 1. CATHODE 2. ANODE 3. NO CONNECTION
STYLE 27: PIN 1. CATHODE 2. CATHODE 3. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE 3. ANODE			

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