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March 2013

KSD471A NPN Epitaxial Silicon Transistor

Features

- Audio Frequency Power Amplifier
- Complement to KSB564A
- Collector Current: I_C = 1 A
- Collector Power Dissipation: P_C = 800 mW
- Suffix "-C" means Center Collector
 (1. Emitter 2. Collector 3. Base)



Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter	Ratings	Unit
V _{CBO}	Collector-Base Voltage	40	V
V _{CEO}	Collector-Emitter Voltage	30	V
V _{EBO}	Emitter-Base Voltage 5		
I _C	Collector Current	1	Α
P_{C}	Collector Power Dissipation	800	mW
T_J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 to +150	°C

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Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Unit
BV _{CBO}	Collector-Base Breakdown Voltage	$I_C = 100 \mu A, I_E = 0$	40			V
BV _{CEO}	Collector-Emitter Breakdown Voltage $I_C = 10 \text{ mA}, I_B = 0$					V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = 100 \mu A, I_C = 0$	5			V
I _{CBO}	Collector Cut-off Current	$V_{CB} = 30 \text{ V}, I_{E} = 0$			0.1	μΑ
h _{FE}	DC Current Gain	$V_{CE} = 1 \text{ V, } I_{C} = 100 \text{ mA}$	120		400	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 1 A, I _B = 0.1 A			0.5	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = 1 A, I _B = 0.1 A			1.2	V
f _T	Current Gain BandWidth Product	$V_{CE} = 6 \text{ V}, I_{C} = 10 \text{ mA}$		130		MHz
C _{ob}	Output Capacitance	V _{CB} = 6 V, I _E = 0, f = 1 MHz		16		pF

h_{FE} Classification

Classification	Y	G
h _{FE}	120 ~ 240	200 ~ 400

Typical Performance Characteristics

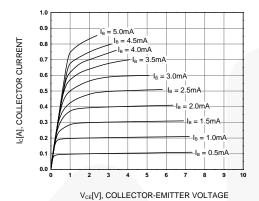


Figure 1. Static Characteristic

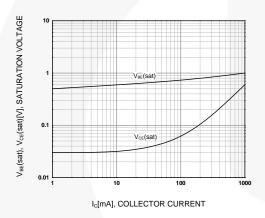


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

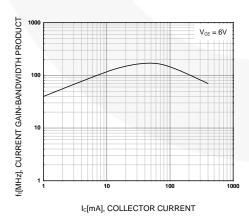


Figure 5. Current Gain Bandwidth Product

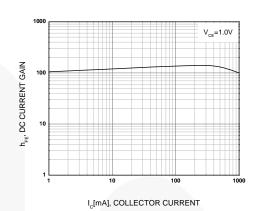


Figure 2. DC current Gain

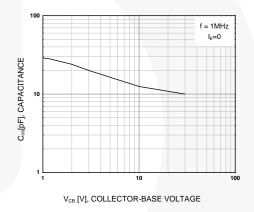
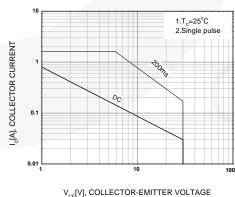


Figure 4. Collector Output Capacitance



CELVI, COLLEGION LIMITEN VOLIMOL

Figure 6. Safe Operating Area

Physical Dimensions

TO-92

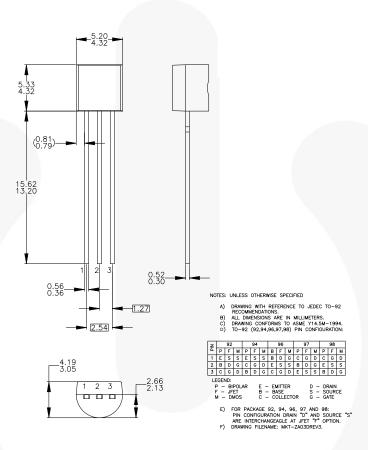


Figure 7. 3LEAD, TO92, JEDEC TO-92 COMPLIANT STRAIGHT LEAD CONFIGURATION (OLD TO92AM3)

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Definition of Torms

Definition of Terms					
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.			
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