

# Complementary Silicon Plastic Power Darlingtons

# BDV65B (NPN), BDV64B (PNP)

... for use as output devices in complementary general purpose amplifier applications.

#### **Features**

- High DC Current Gain HFE = 1000 (min) @ 5 Adc
- Monolithic Construction with Built-in Base Emitter Shunt Resistors
- These are Pb-Free Devices\*

#### **MAXIMUM RATINGS**

Rating	Symbol	Max	Unit
Collector-Emitter Voltage	$V_{CEO}$	100	Vdc
Collector-Base Voltage	$V_{CB}$	100	Vdc
Emitter-Base Voltage	V <sub>EB</sub>	5.0	Vdc
Collector Current - Continuous - Peak	I <sub>C</sub>	10 20	Adc
Base Current	Ι <sub>Β</sub>	0.5	Adc
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	125 1.0	W W/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C

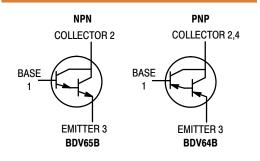
#### THERMAL CHARACTERISTICS

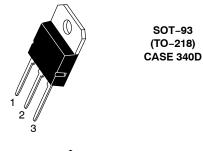
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.0	°C/W

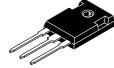
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

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# 10 AMPERE DARLINGTON COMPLEMENTARY SILICON POWER TRANSISTORS 60-80-100-120 VOLTS, 125 WATTS







TO-247 CASE 340L STYLE 3

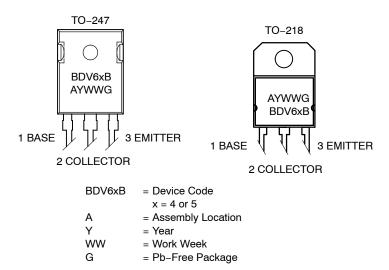
NOTE: Effective June 2012 this device will be available only in the TO-247 package. Reference FPCN# 16827.

#### **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

#### **MARKING DIAGRAMS**



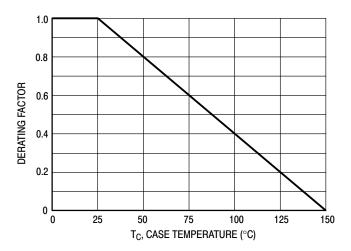


Figure 1. Power Derating

#### **ELECTRICAL CHARACTERISTICS**

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				-
Collector–Emitter Sustaining Voltage (1) $(I_C = 30 \text{ mAdc}, I_B = 0)$	V <sub>CEO(sus)</sub>	100	-	Vdc
Collector Cutoff Current (V <sub>CE</sub> = 50 Vdc, I <sub>B</sub> = 0)	I <sub>CEO</sub>	_	1.0	mAdc
Collector Cutoff Current (V <sub>CB</sub> = 100 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	-	0.4	mAdc
Collector Cutoff Current $(V_{CB} = 50 \text{ Vdc}, I_E = 0, T_C = 150^{\circ}\text{C})$	I <sub>CBO</sub>	-	2.0	mAdc
Emitter Cutoff Current (V <sub>BE</sub> = 5.0 Vdc, I <sub>C</sub> = 0)	I <sub>EBO</sub>	-	5.0	mAdc
ON CHARACTERISTICS				
DC Current Gain (I <sub>C</sub> = 5.0 Adc, V <sub>CE</sub> = 4.0 Vdc)	h <sub>FE</sub>	1000	-	-
Collector–Emitter Saturation Voltage (I <sub>C</sub> = 5.0 Adc, I <sub>B</sub> = 0.02 Adc)	V <sub>CE(sat)</sub>	_	2.0	Vdc
Base–Emitter Saturation Voltage (I <sub>C</sub> = 5.0 Adc, V <sub>CE</sub> = 4.0 Vdc)	V <sub>BE(on)</sub>	_	2.5	Vdc

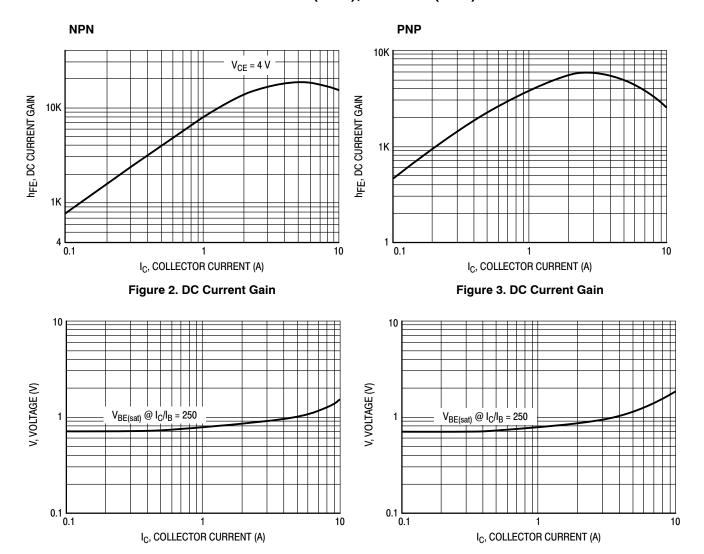


Figure 4. "On" Voltages

Figure 5. "On" Voltages

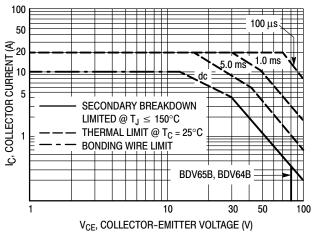


Figure 6. Active Region Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate  $I_C$  –  $V_{CE}$  limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 6 is based on  $T_{J(pk)} = 150^{\circ}\text{C}$ ,  $T_{C}$  is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided  $T_{J(pk)} \leq 150^{\circ}\text{C}$ .  $T_{J(pk)}$  may be calculated from the data in Figure 7. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

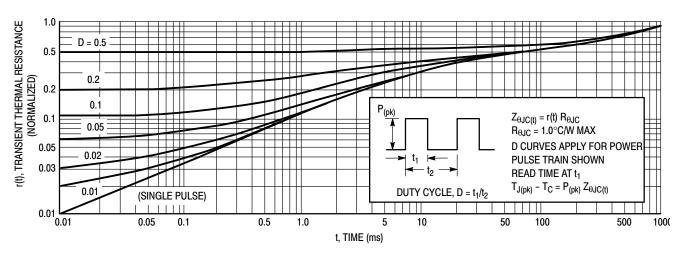


Figure 7. Thermal Response

#### **ORDERING INFORMATION**

Device Order Number	Package Type	Shipping <sup>†</sup>
BDV64BG	TO-247 (Pb-Free)	30 Units / Rail

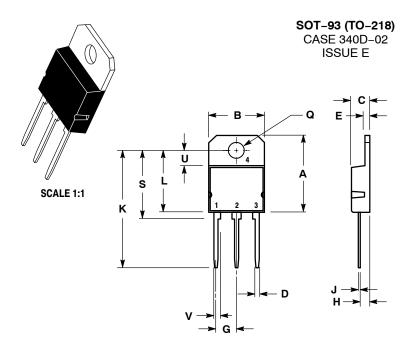
#### **DISCONTINUED** (Note 1)

BDV65B	TO-218	30 Units / Rail
BDV65BG	TO-247 (Pb-Free)	30 Units / Rail
BDV64B	TO-218	30 Units / Rail

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

<sup>1.</sup> **DISCONTINUED:** These devices are not recommended for new design. Please contact your **onsemi** representative for information. The most current information on these devices may be available on <a href="https://www.onsemi.com">www.onsemi.com</a>.





STYLE 1: PIN 1. BASE

2. COLLECTOR 3. EMITTER COLLECTOR STYLE 2: PIN 1. ANODE CATHODE
 ANODE

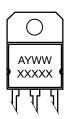
4. CATHODE

**DATE 03 JAN 2002** 

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
Α		20.35		0.801
В	14.70	15.20	0.579	0.598
С	4.70	4.90	0.185	0.193
D	1.10	1.30	0.043	0.051
E	1.17	1.37	0.046	0.054
G	5.40	5.55	0.213	0.219
Н	2.00	3.00	0.079	0.118
J	0.50	0.78	0.020	0.031
K	31.00	REF	1.220	REF
L		16.20		0.638
Q	4.00	4.10	0.158	0.161
S	17.80	18.20	0.701	0.717
U	4.00 REF		0.157	REF
٧	1.75 REF		0.0	)69

#### **GENERIC MARKING DIAGRAM\***



= Assembly Location

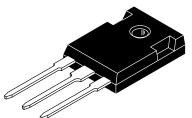
= Year WW = Work Week XXXXX = Device Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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DESCRIPTION:	SOT-93 (TO-218)		PAGE 1 OF 1

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TO-247 CASE 340L ISSUE G

**DATE 06 OCT 2021** 

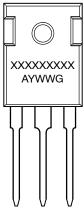
#### NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: MILLIMETER

	MILLIMETERS		MILLIMETERS INCHES	
DIM	MIN.	MAX.	MIN.	MAX.
Α	20.32	21.08	0.800	0.830
В	15.75	16.26	0.620	0.640
С	4.70	5.30	0.185	0.209
D	1.00	1.40	0.040	0.055
E	1.90	2.60	0.075	0.102
F	1.65	2.13	0.065	0.084
G	5.45 BSC		0.215 BSC	
Н	1.50	2.49	0.059	0.098
J	0.40	0.80	0.016	0.031
K	19.81	20.83	0.780	0.820
L	5.40	6.20	0.212	0.244
N	4.32	5.49	0.170	0.216
Р		4.50		0.177
Q	3.55	3.65	0.140	0.144
U	6.15 BSC		0.242 BSC	
W	2.87	3.12	0.113	0.123

	SCALE 1:1	
N N N N N N N N N N N N N N N N N N N	B	SEATING PLANE  T SEATING PLANE  H H
	<b>⊕</b> 0.25 (0.010) <b>₩</b>  Y A <b>\$</b>	

#### **GENERIC MARKING DIAGRAM\***



STYLE 1: PIN 1. GATE 2. DRAIN 3. SOURCE 4. DRAIN

PIN 1. CATHODE 2. ANODE

3. GATE 4. ANODE

STYLE 5:

STYLE 2: PIN 1. ANODE 2. CATHODE (S) 3. ANODE 2 4. CATHODES (S)

PIN 1. MAIN TERMINAL 1 2. MAIN TERMINAL 2

3. GATE 4. MAIN TERMINAL 2

STYLE 6:

STYLE 3: PIN 1. BASE 2. COLLECTOR 3. EMITTER 4. COLLECTOR STYLE 4: PIN 1. GATE 2. COLLECTOR 3. EMITTER 4. COLLECTOR

XXXXX = Specific Device Code = Assembly Location Α

Υ = Year WW = Work Week G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

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