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## Is Now



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# NVATS5A112PLZ

## Power MOSFET

–60 V, 43 mΩ, –27 A, P-Channel

Automotive Power MOSFET designed for compact and efficient designs and including high thermal performance.

AEC-Q101 qualified MOSFET and PPAP capable suitable for automotive applications.

### Features

- Low On-Resistance
- High Current Capability
- 100% Avalanche Tested
- AEC-Q101 qualified and PPAP capable
- ATPAK package is pin-compatible with DPAK (TO-252)
- Pb-Free, Halogen Free and RoHS compliance

### Typical Applications

- Reverse Battery Protection
- Load Switch
- Automotive Front Lighting
- Automotive Body Controllers

### SPECIFICATIONS

**ABSOLUTE MAXIMUM RATING** at Ta = 25°C (Note 1)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	VDSS	–60	V
Gate to Source Voltage	VGSS	±20	V
Drain Current (DC)	ID	–27	A
Drain Current (Pulse) PW ≤ 10 μs, duty cycle ≤ 1%	IDP	–81	A
Power Dissipation Tc = 25°C	PD	48	W
Operating Junction and Storage Temperature	Tj, Tstg	–55 to +175	°C
Avalanche Energy (Single Pulse) (Note 2)	EAS	50	mJ
Avalanche Current (Note 3)	IAV	–13	A

Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

2 : VDD = –10 V, L = 500 μH, IAV = –13 A

3 : L ≤ 500 μH, Single pulse

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction to Case Steady State (Tc = 25°C)	RθJC	3.1	°C/W
Junction to Ambient (Note 4)	RθJA	80.5	°C/W

Note 4 : Surface mounted on FR4 board using a 130 mm<sup>2</sup>, 1 oz. Cu pad.

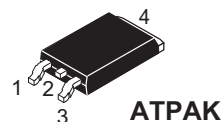
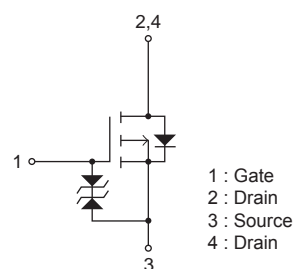


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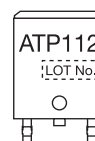
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VDSS	RDS(on) Max	ID Max
–60 V	43 mΩ @ –10 V	–27 A
	59 mΩ @ –4.5 V	
	63 mΩ @ –4 V	

### ELECTRICAL CONNECTION P-Channel



### MARKING



### ORDERING INFORMATION

See detailed ordering and shipping information on page 6 of this data sheet.

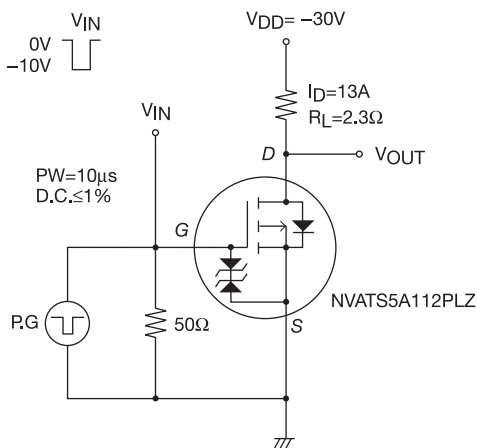
# NVATS5A112PLZ

## ELECTRICAL CHARACTERISTICS at Ta = 25°C (Note 5)

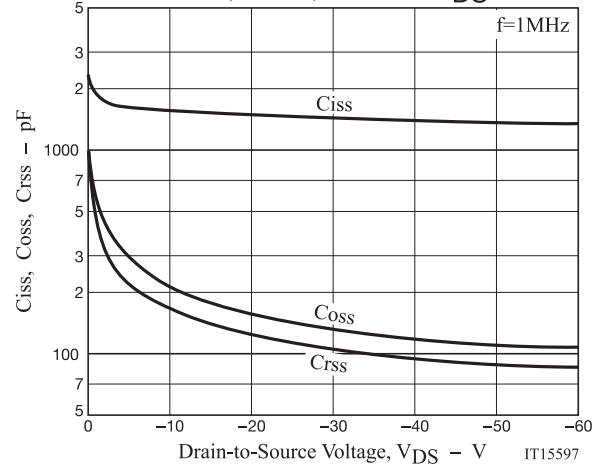
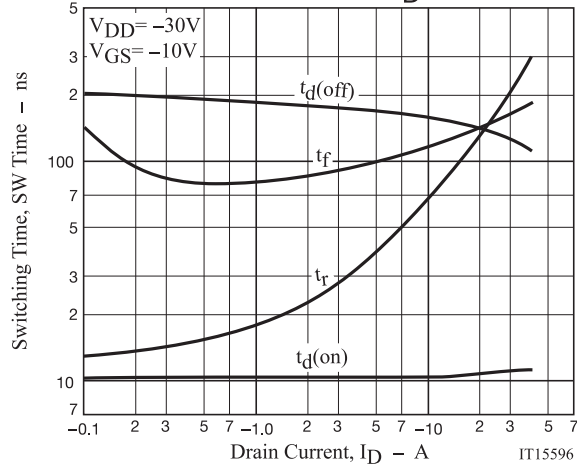
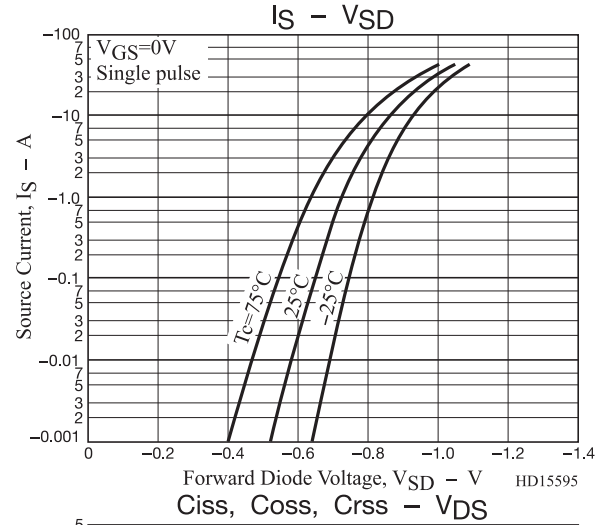
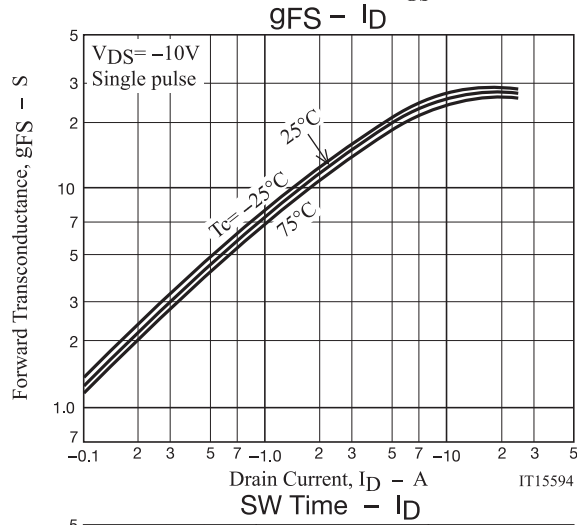
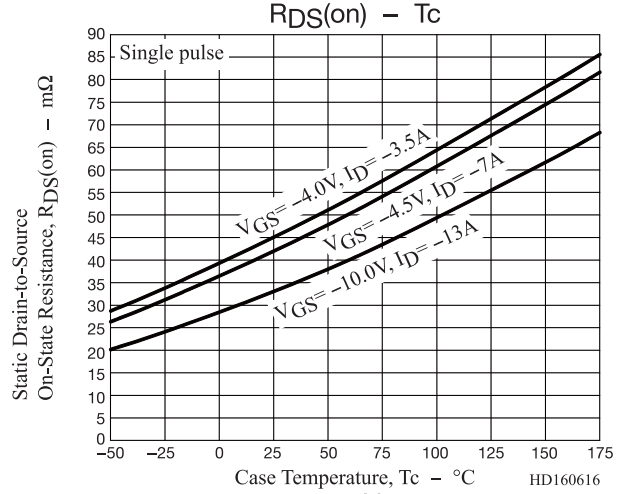
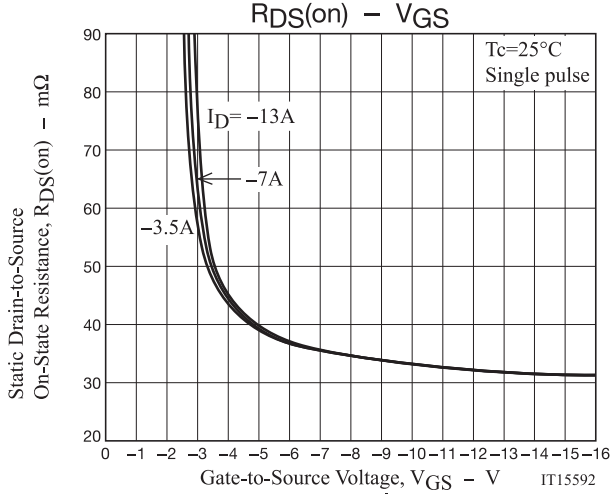
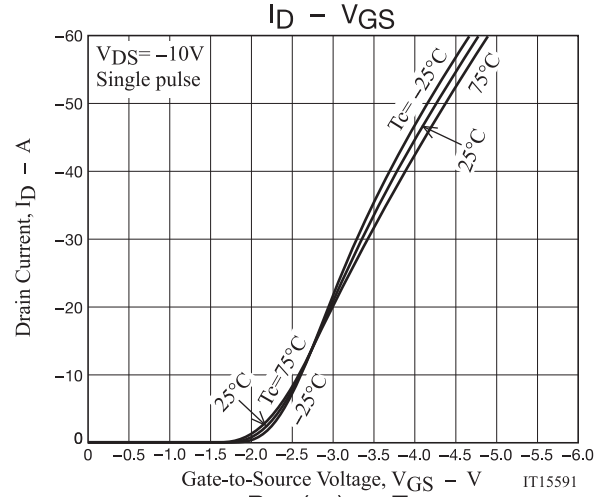
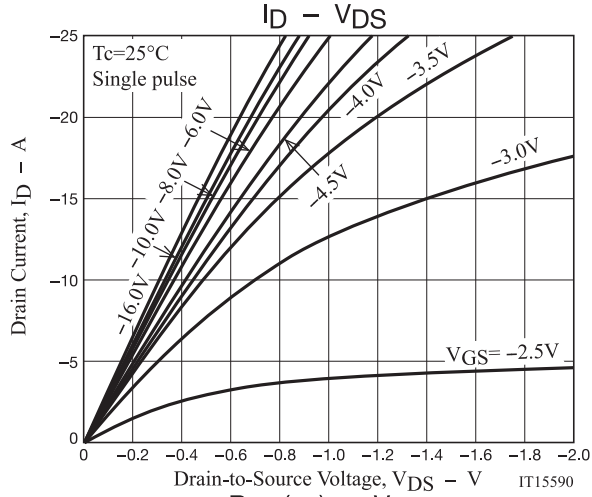
Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> = -1 mA, V <sub>GS</sub> = 0 V	-60			V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -60 V, V <sub>GS</sub> = 0 V			-1	μA
Gate to Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V			±10	μA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -1 mA	-1.2		-2.6	V
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -13 A		24		S
Static Drain to Source On-State Resistance	R <sub>DS(on)</sub>	I <sub>D</sub> = -13 A, V <sub>GS</sub> = -10 V		33	43	mΩ
		I <sub>D</sub> = -7 A, V <sub>GS</sub> = -4.5 V		42	59	mΩ
		I <sub>D</sub> = -3.5 A, V <sub>GS</sub> = -4 V		45	63	mΩ
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = -20 V, f = 1 MHz		1,450		pF
Output Capacitance	C <sub>oss</sub>			155		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			125		pF
Turn-ON Delay Time	t <sub>d(on)</sub>	See Fig. 1		10		ns
Rise Time	t <sub>r</sub>			80		ns
Turn-OFF Delay Time	t <sub>d(off)</sub>			150		ns
Fall Time	t <sub>f</sub>			120		ns
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = -30 V, V <sub>GS</sub> = -10 V, I <sub>D</sub> = -25 A		33.5		nC
Gate to Source Charge	Q <sub>gs</sub>			5.3		nC
Gate to Drain "Miller" Charge	Q <sub>gd</sub>			7.9		nC
Forward Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = -25 A, V <sub>GS</sub> = 0 V		-0.97	-1.5	V

Note 5 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted.  
Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

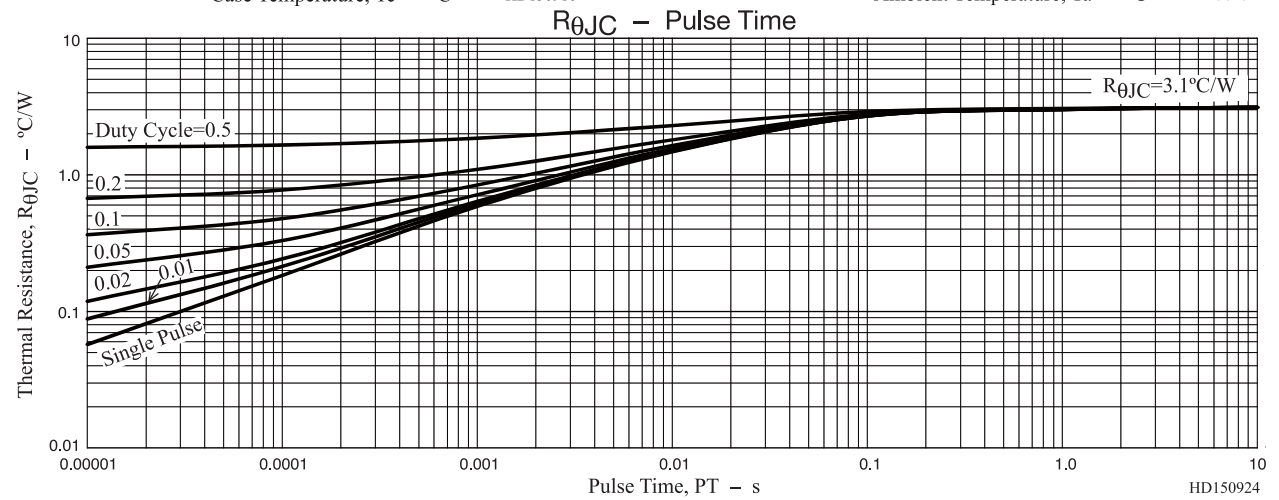
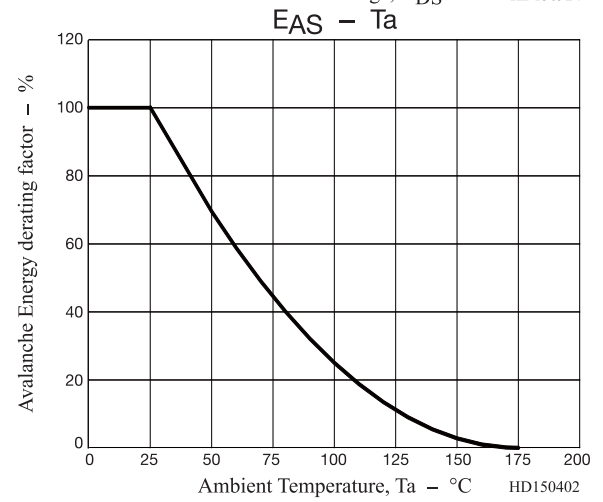
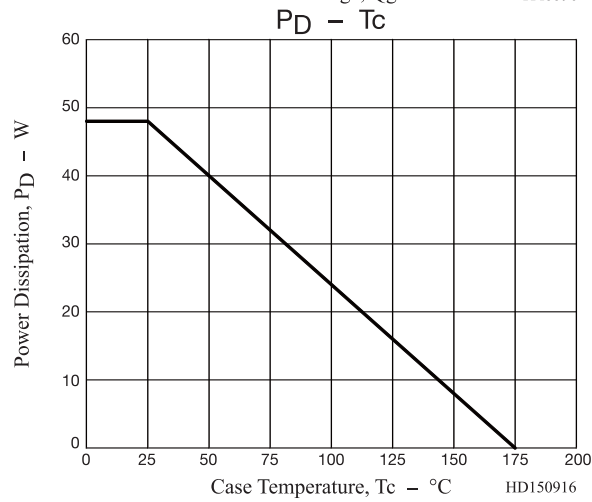
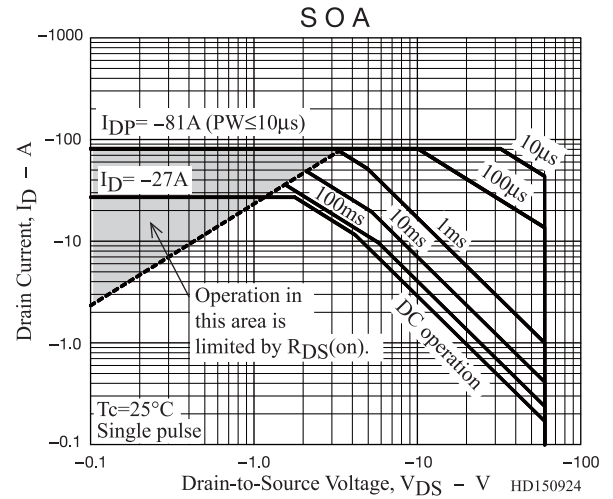
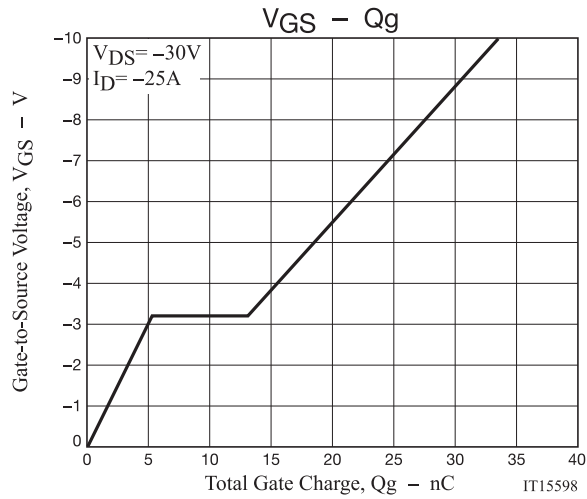
**Fig.1 Switching Time Test Circuit**



# NVATS5A112PLZ



# NVATS5A112PLZ



**NVATS5A112PLZ**

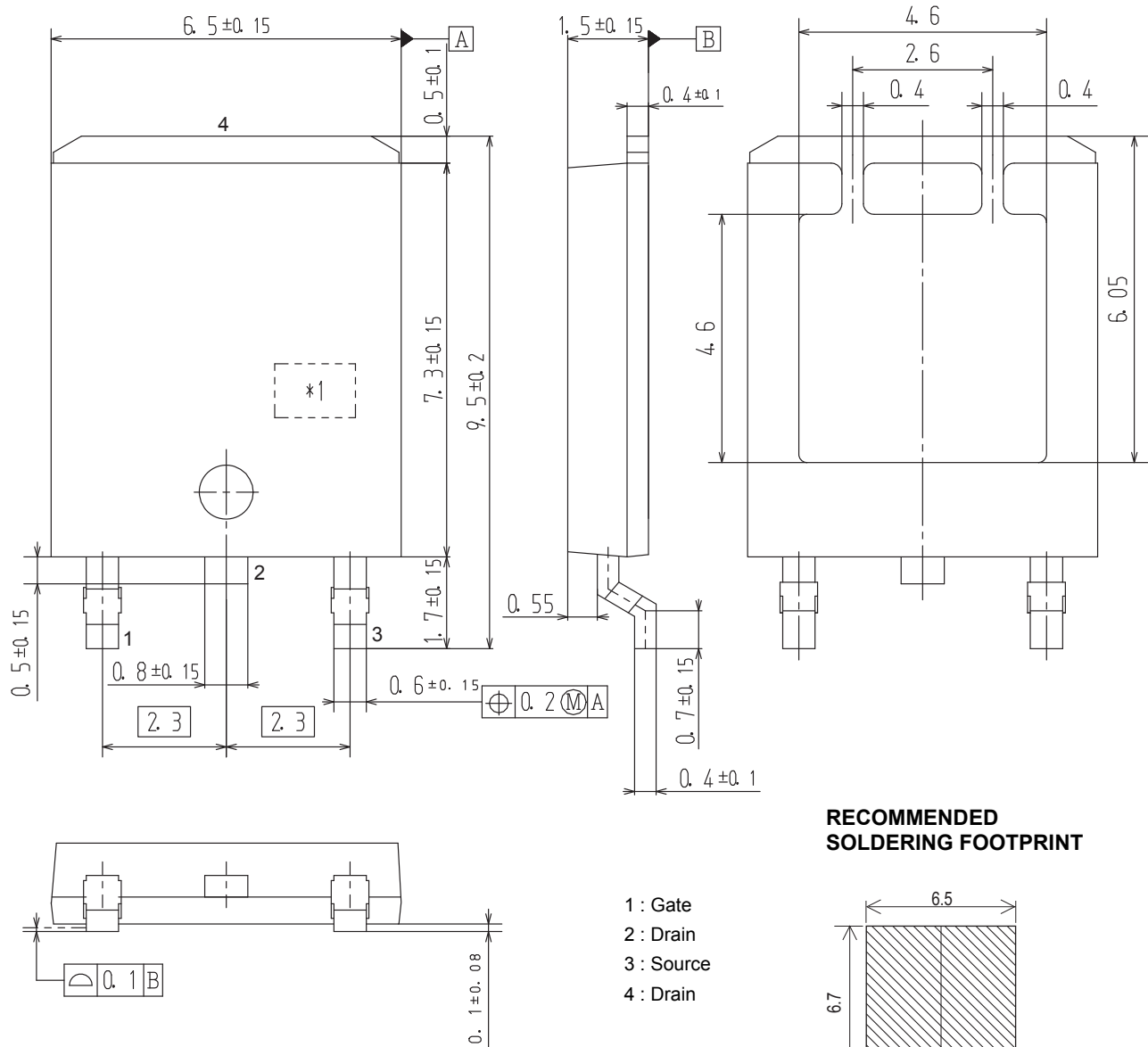
## PACKAGE DIMENSIONS

unit : mm

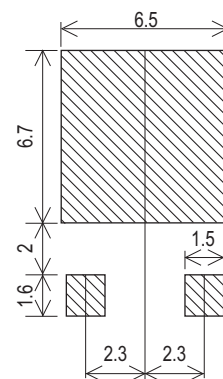
### DPAK (Single Gauge) / ATPAK

CASE 369AM

ISSUE 0



## RECOMMENDED SOLDERING FOOTPRINT



Pin2 is idle pin with electrical  
designation only carried

\*1: Lot indication

## NVATS5A112PLZ

### ORDERING INFORMATION

Device	Marking	Package	Shipping (Qty / Packing)
NVATS5A112PLZT4G	ATP112	DPAK(Single Gauge) / ATPAK (Pb-Free / Halogen Free)	3,000 / Tape & Reel

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. [http://www.onsemi.com/pub\\_link/Collateral/BRD8011-D.PDF](http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF)

Note on usage : Since the NVATS5A112PLZ is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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