# onsemi

## **Small Signal Diode**

### LL4148

#### Features

• This is a Pb-Free and Halide Free Device

#### **ABSOLUTE MAXIMUM RATINGS**

(Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.) (Notes 1 and 2)

-				
Symbol	Parameter		Value	Unit
V <sub>RRM</sub>	Maximum Repetitive Reverse Voltage		100	V
I <sub>F(AV)</sub>	Average Rectified Forward Current		200	mA
۱ <sub>f</sub>	Recurrent Peak Forward Current		500	mA
I <sub>FSM</sub>	Non-Repetitive Peak Forward	Pulse Width = 1.0 s	1.0	А
	Surge Current	Pulse Width = 1.0 $\mu$ s	2.0	
T <sub>STG</sub>	Storage Temperature Range		-65 to +200	°C
TJ	Operating Junction Temperature Range		–55 to +175	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. These ratings are based on a maximum junction temperature of 200°C.

 These are steady-state limits. onsemi should be consulted on applications involving pulsed or low-duty-cycle operations.

#### THERMAL CHARACTERISTICS

(Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.) (Note 3)

Symbol	Parameter	Value	Unit
PD	Power Dissipation	500	mW
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	300	°C/W

3. JEDEC Standard 51–3 method (PCB Board size  $76 \times 114 \times 0.6T$  mm<sup>3</sup>)

#### ELECTRICAL CHARACTERISTICS (Values are at T<sub>A</sub> = 25°C unless otherwise noted.)

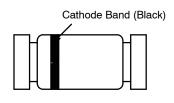
Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>R</sub>	Breakdown Voltage	I <sub>R</sub> = 100 μA	100	-	V
		I <sub>R</sub> = 5.0 μA	75	-	
V <sub>F</sub>	Forward Voltage	I <sub>F</sub> = 10 mA	-	1.0	V
I <sub>R</sub>	Reverse Leakage	V <sub>R</sub> = 20 V	-	25	nA
		V <sub>R</sub> = 20 V, T <sub>A</sub> = 150°C	-	50	μΑ
CT	Total Capacitance	V <sub>R</sub> = 0, f = 1.0 MHz	_	4.0	pF
t <sub>rr</sub>	Reverse Recovery Time	$\rm I_F$ = 10 mA, $\rm V_R$ = 6.0 V (60 mA), $\rm I_{rr}$ = 1.0 mA, $\rm R_L$ = 100 $\Omega$	-	4.0	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



SOD-80 CASE 100AD

#### MARKING DIAGRAM



#### **ORDERING INFORMATION**

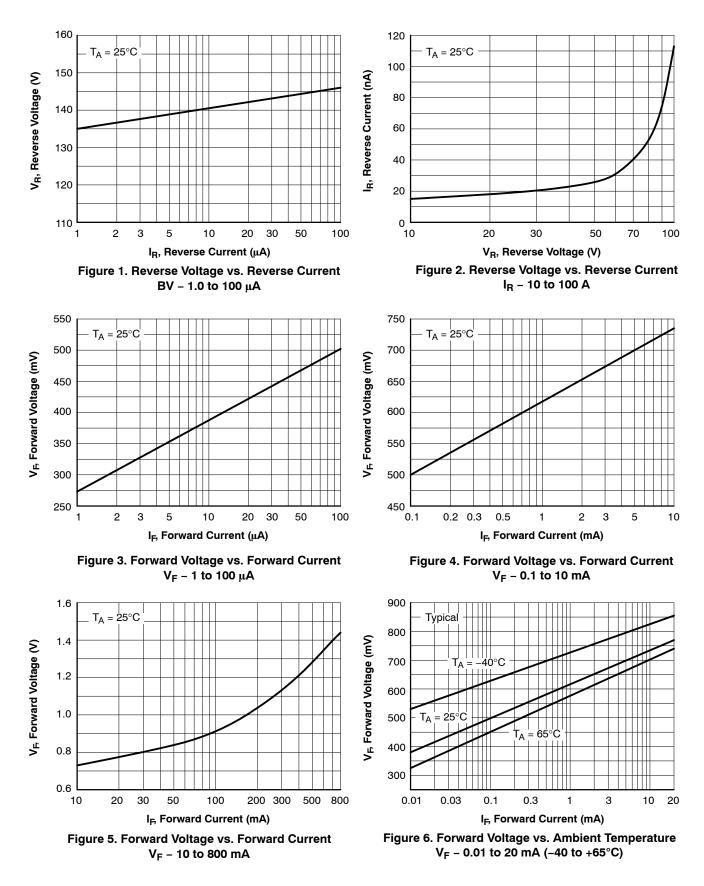
Device	Package	Shipping <sup>†</sup>
LL4148	SOD-80 (Pb-Free/	2500 / Tape & Reel
	Halide Free)	(7″)

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, <u>BRD8011/D</u>.

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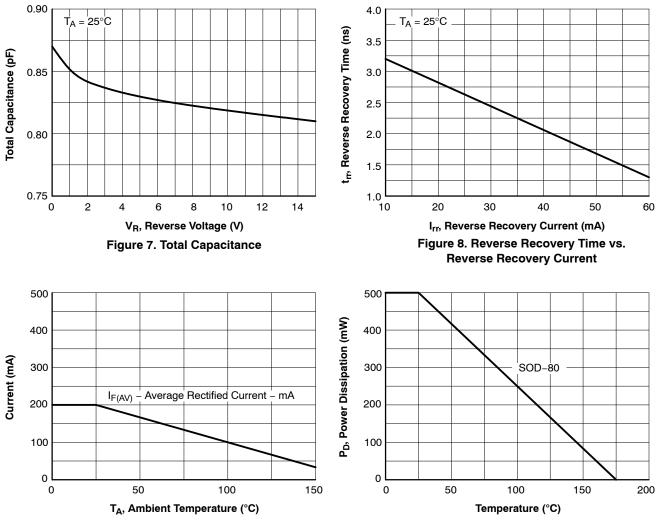
#### LL4148





#### LL4148

#### TYPICAL PERFORMANCE CHARACTERISTICS (Continued)



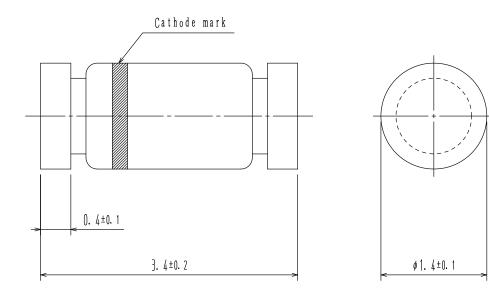






MiniMELF / SOD-80 CASE 100AD ISSUE O

DATE 30 APR 2012



NOTES: UNLESS OTHERWISE SPECIFIED A) PACKAGE STANDARD REFERENCE: JEDEC DO-213, VARIATION AC. B) ALL DIMENSIONS ARE IN MILLIMETERS.

C CORNER RADIUS IS OPTIONAL.

D) DRAWING FILE NAME: SOD80A REV01

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DESCRIPTION:	MINIMELF / SOD-80		PAGE 1 OF 1

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