SVC704

Varactor Diode Single Varactor Diode for VCO 16V, 50nA, CR=6.0, rs=0.8Ω, MCPH3

Features

- High capacitance ratio (VR=1.0 to 7.0V)
- Small-sized package
- Low series resistance

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Reverse Voltage	VR		16	V
Junction Temperature	Тј		150	°C
Storage Temperature	Tstg		-55 to +150	°C

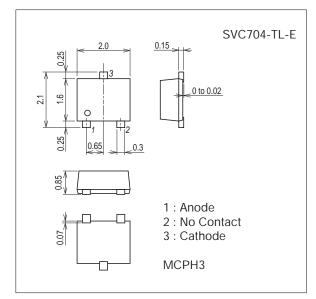
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

• Package

• JEITA, JEDEC

Package Dimensions

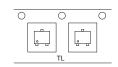
unit : mm (typ) 7019A-001



Product & Package Information

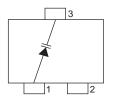
- : MCPH3
- : SC-70, SOT-323
- Minimum Packing Quantity : 3,000 pcs./reel

Packing Type : TL





Electrical Connection





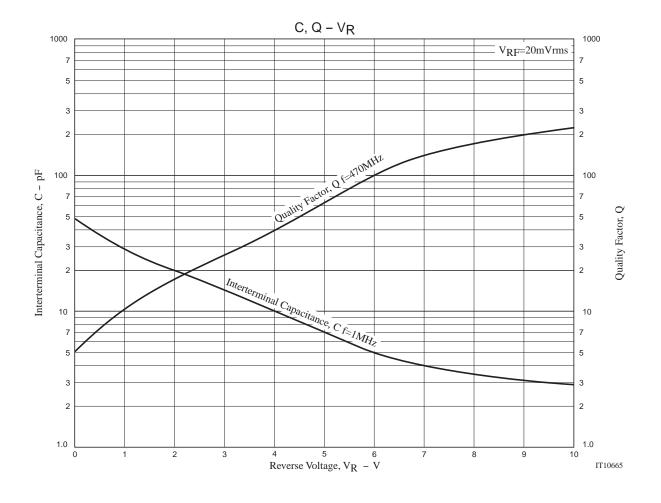
http://onsemi.com

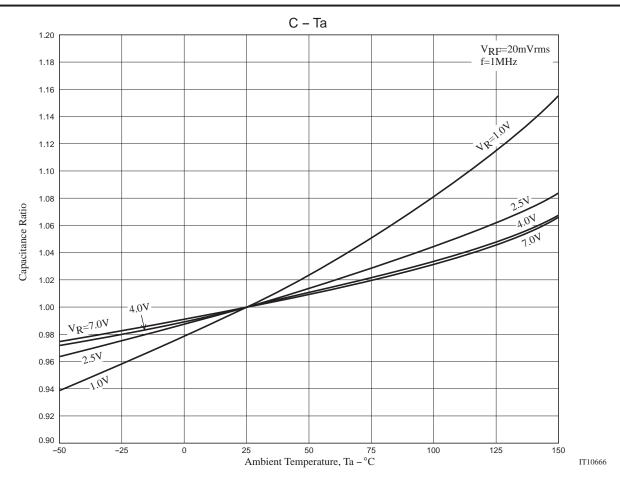
Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions		Unit			
Parameter	Symbol	Conditions	min	typ	max	Unit	
Breakdown Voltage	V(BR)R	IR=10μA	16			V	
Reverse Current	IR	V _R =10V			50	nA	
	C1.0V	V _R =1.0V, f=1MHz	26.0		30.0	pF	
Interterminal Capacitance*	C7.0V	V _R =7.0V, f=1MHz	3.4		4.6	pF	
Series Resistance	rs	VR=1.0V, f=470MHz			0.8	Ω	
Capacitance Ratio	CR	C1.0V / C7.0V	6.0				

Ordering Information

Device	Package	Shipping	memo		
SVC704-TL-E	I MCPH3		Pb Free		





Taping Specification SVC704-TL-E

1. Packing Format

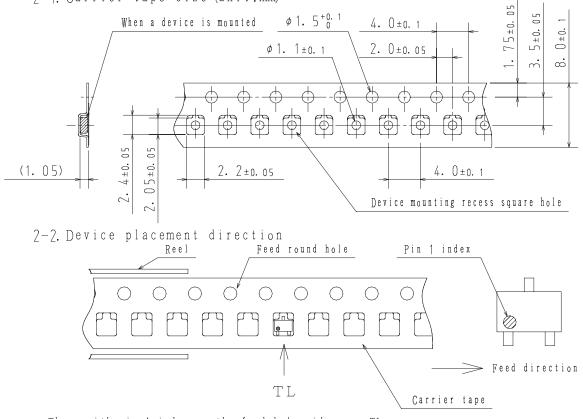
Package Name	Carrier Tape	Maximun Number of devices contained (pcs)			Packing format			
	Туре	Reel	Inner box	Outer box	Inner BOX (C-1)			Outer BOX (A-7)
МСРНЗ	МСРНЗ	3,000	15,000	90,000	5 ree	els containe	ed	6 inner boxes contained
					Dime	nsions:mm (external)	Dimensions:mm (external)
					18	$3 \times 72 \times$	185	440×195×210
<u>R</u> eel label, Inner box label <u>Outer box label</u>								
Packing met	thod			(u 1	nit:r	nm)	The fo	a label at the time of factory shipments. rm of a label may change in physical bution process.
0	>		<	ť	69	>	<	108
	Type LOT Quan Orig Reel la	No. tity in						TYPE CODE III IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII
				eatment	of the	terminal i	is lead f	
				Label LEAD FRH			Phase hase 3A	
					-	0.0.1.1.1		

LEAD FREE 4

JEITA Phase 3

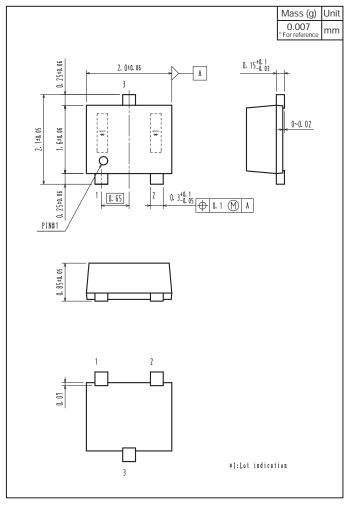
2. Taping configuration

2-1. Carrier tape size (unit:mm)

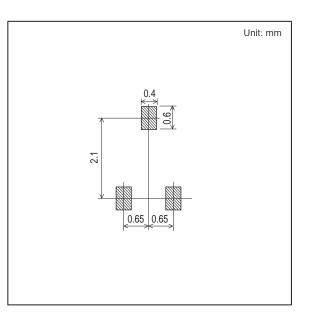


Those with pin 1 index on the feed hole side $\cdots \cdots TL$

Outline Drawing SVC704-TL-E



Land Pattern Example



SVC704

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