

PNP Epitaxial Silicon Transistor

BSR16

PNP General Purpose Amplifier

- This Device Designed for Use as General Purpose Amplifier and Switches Requiring Collector Currents to 500 mA
- Sourced from Process 63
- See BCW68G for Characteristics

ABSOLUTE MAXIMUM RATINGS

(T_A = 25°C, unless otherwise specified.)

Symbol	Parameter	Value	Unit
V _{CEO}	V _{CEO} Collector-Emitter Voltage		V
V _{CBO}	Collector-Base Voltage	-60	V
V _{EBO}	Emitter-Base Voltage	-5.0	V
I _C	Collector Current - Continuous	-800	mA
T _J , T _{ST}	Operating and Storage Junction Temperature Range	−55 ~ +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. These ratings are based on a maximum junction temperature of 150°C.
- These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

THERMAL CHARACTERISTICS

(T_A = 25°C, unless otherwise specified)

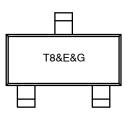
Symbol	Parameter	Max.	Unit
P _D	Total Device Dissipation Derate above 25°C	350 2.8	mW mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	°C/W

3. Device mounted on FR-4 PCB 40 mm × 40 mm × 1.5 mm.



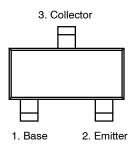
CASE 318

MARKING DIAGRAM



T8 Specific Device Code &E Designates Space &G Date Code (Week)

PIN ASSIGNMENT



ORDERING INFORMATION

Device	Package	Shipping [†]
BSR16	SOT-23 (Pb-Free)	3,000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

BSR16

$\textbf{ELECTRICAL CHARACTERISTICS} \ (T_A = 25^{\circ}C, \ unless \ otherwise \ specified)$

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
OFF CHARA	CTERISTICS	•		•		•
BV _{(BR)CEO}	Collector-Emitter Breakdown Voltage	$I_C = -10 \text{ mA}, I_B = 0$	-60			V
BV _{(BR)CBO}	Collector-Base Breakdown Voltage	$I_C = -100 \mu A, I_E = 0$	-60			٧
BV _{(BR)EBO}	Emitter-Base Breakdown Voltage	$I_E = -10 \mu A, I_C = 0$	-5.0			٧
I _{CBO}	Collector Cut-off Current	V _{CB} = -50 V V _{CB} = -50 V, T _A = 150°C			-10 -10	nA μA
I _{CEX}	Collector Cut-off Current	$V_{CE} = -30 \text{ V}, V_{EB} = -0.5 \text{ V}$			-50	nA
I _{BEX}	Reverse Base Current	V _{CE} = -30 V, V _{EB} = -3.0 V			-50	nA
ON CHARAC	TERISTICS	·				
h _{FE}	DC Current Gain	$\begin{split} I_C &= -0.1 \text{ mA, } V_{CE} = -10 \text{ V} \\ I_C &= -1.0 \text{ mA, } V_{CE} = -10 \text{ V} \\ I_C &= -10 \text{ mA, } V_{CE} = -10 \text{ V} \\ I_C &= -150 \text{ mA, } V_{CE} = -10 \text{ V} \\ I_C &= -500 \text{ mA, } V_{CE} = -10 \text{ V} \end{split}$	75 100 100 100 50	300		
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = -150 mA, I _B = -15 mA I _C = -500 mA, I _B = -50 mA			-0.4 -1.6	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = -150 mA, I _B = -15 mA I _C = -500 mA, I _B = -50 mA			-1.3 -2.6	V
SMALL SIGN	IAL CHARACTERISTICS					
f _T	Current Gain Bandwidth Product	I _C = -50 mA, V _{CE} = -20 V, f = 100 MHz, T _A = 25°C	200			MHz
C _{cb}	Output Capacitance	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1.0 \text{ MHz}$			8.0	pF
C _{eb}	Emitter-Base Capacitance	$V_{CB} = -2.0 \text{ V}, I_E = 0, f = 1.0 \text{ MHz}$			30	pF
WITCHING	CHARACTERISTICS					
t _{on}	Turn-On Time	$V_{CC} = -30 \text{ V}, I_C = -150 \text{ mA},$			45	ns
t _d	Delay Time	I _{B1} = -15 mA			10	ns
t _r	Rise Time				40	ns
t _{off}	Turn-Off Time	$V_{CC} = -6 \text{ V}, I_{C} = -150 \text{ mA},$			100	ns
t _s	Storage Time	$I_{B1} = I_{B2} = -15 \text{ mA}$			80	ns
t _f	Fall Time				30	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

MILLIMETERS

MIN

0.89

0.01

0.37

0.08

2.80

1.20

1.78

0.30

0.35

2.10

O°

NOM

1.00

0.06

0.44

0.14

2.90

1.30

1.90

0.43

0.54

2.40





SOT-23 (TO-236) 2.90x1.30x1.00 1.90P **CASE 318 ISSUE AU**

DATE 14 AUG 2024

MAX

1.11

0.10

0.50

0.20

3.04

1.40

2.04

0.55

0.69

2.64

10°





DETAIL "A" Scale 3:1







NOTES:

DIM

Α

Α1

b

С

D

Ε

е L

L1

HE

Τ

- DIMENSIONING AND TOLERANCING 1. PER ASME Y14.5M, 2018. CONTROLLING DIMENSIONS:
- MILLIMETERS.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE
- BASE MATERIAL.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

= Date Code

= Pb-Free Package

RECOMMENDED MOUNTING FOOTPRINT

* For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

STYLES ON PAGE 2

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^{*}This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking.

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DATE 14 AUG 2024

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR			
STYLE 9: PIN 1. ANODE 2. ANODE 3. CATHODE	STYLE 10: PIN 1. DRAIN 2. SOURCE 3. GATE	2. CATHODE 2.	2: STYLE 13: CATHODE PIN 1. SOURCE CATHODE 2. DRAIN ANODE 3. GATE	STYLE 14: PIN 1. CATHODE 2. GATE 3. ANODE
STYLE 15: PIN 1. GATE 2. CATHODE 3. ANODE	STYLE 16: PIN 1. ANODE 2. CATHODE 3. CATHODE	2. ANODE 2.	3: STYLE 19: NO CONNECTION PIN 1. CATHODE CATHODE 2. ANODE ANODE 3. CATHODE-ANODE	STYLE 20: PIN 1. CATHODE 2. ANODE 3. GATE
STYLE 21: PIN 1. GATE 2. SOURCE 3. DRAIN	STYLE 22: PIN 1. RETURN 2. OUTPUT 3. INPUT			STYLE 26: PIN 1. CATHODE 2. ANODE 3. NO CONNECTION
STYLE 27: PIN 1. CATHODE 2. CATHODE 3. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE 3. ANODE			

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