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Low Noise Transistors NPN Silicon

MAXIMUM RATINGS

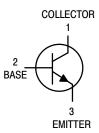
Rating	Symbol	BC549	BC550	Unit
Collector–Emitter Voltage	V _{CEO}	30	45	Vdc
Collector-Base Voltage	V _{CBO}	30	50	Vdc
Emitter-Base Voltage	V _{EBO}	5.0		Vdc
Collector Current — Continuous	Ι _C	100		mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	625 5.0		mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	1.5 12		Watt mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150		°C

BC549B,C BC550B,C



THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	R_{\thetaJA}	200	°C/W
Thermal Resistance, Junction to Case	$R_{ extsf{ heta}JC}$	83.3	°C/W



ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	:	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Collector–Emitter Breakdown Voltage ($I_C = 10$ mAdc, $I_B = 0$)	BC549B,C BC550B,C	V _{(BR)CEO}	30 45			Vdc
Collector–Base Breakdown Voltage ($I_C = 10 \ \mu Adc, I_E = 0$)	BC549B,C BC550B,C	V _{(BR)CBO}	30 50			Vdc
Emitter–Base Breakdown Voltage ($I_E = 10 \ \mu Adc, I_C = 0$)		V _{(BR)EBO}	5.0	—	—	Vdc
Collector Cutoff Current ($V_{CB} = 30 \text{ V}, I_E = 0$) ($V_{CB} = 30 \text{ V}, I_E = 0, T_A = +125^{\circ}\text{C}$)		I _{CBO}			15 5.0	nAdc μAdc
Emitter Cutoff Current ($V_{EB} = 4.0$ Vdc, $I_C = 0$)		I _{EBO}	—	_	15	nAdc

BC549B,C BC550B,C

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted) (Continued)

Characteristic		Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS						
DC Current Gain ($I_C = 10 \mu Adc$, $V_{CE} = 5.0 Vdc$)	BC549B/550B BC549C/550C	h _{FE}	100 100	150 270		_
$(I_{C} = 2.0 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc})$	BC549B/550B BC549C/550C		200 420	290 500	450 800	
		V _{CE(sat)}		0.075 0.3 0.25	0.25 0.6 0.6	Vdc
Base–Emitter Saturation Voltage ($I_C = 100 \text{ mAdc}, I_B = 5.0 \text{ mAdc}$)		V _{BE(sat)}	—	1.1	—	Vdc
$\begin{array}{l} \text{Base-Emitter On Voltage} \\ (I_C = 10 \ \mu\text{Adc}, \ V_{CE} = 5.0 \ \text{Vdc}) \\ (I_C = 100 \ \mu\text{Adc}, \ V_{CE} = 5.0 \ \text{Vdc}) \\ (I_C = 2.0 \ \text{mAdc}, \ V_{CE} = 5.0 \ \text{Vdc}) \end{array}$		V _{BE(on)}	 0.55	0.52 0.55 0.62	 0.7	Vdc
SMALL-SIGNAL CHARACTERISTICS						
Current–Gain — Bandwidth Product ($I_C = 10 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}, f = 100 \text{ MHz}$)		fT	—	250	—	MHz
Collector–Base Capacitance $(V_{CB} = 10 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$		C _{cbo}	_	2.5	_	pF
Small–Signal Current Gain ($I_C = 2.0 \text{ mAdc}, V_{CE} = 5.0 \text{ V}, f = 1.0 \text{ kHz}$)	BC549B/BC550B BC549C/BC550C	h _{fe}	240 450	330 600	500 900	_
Noise Figure (I _C = 200 μ Adc, V _{CE} = 5.0 Vdc, R _S = 2.0 kΩ, (I _C = 200 μ Adc, V _{CE} = 5.0 Vdc, R _S = 100 kΩ,	,	NF ₁ NF ₂		0.6	2.5 10	dB

NOTES:

1. I_B is value for which I_C = 11 mA at V_{CE} = 1.0 V. 2. Pulse test = 300 μ s – Duty cycle = 2%.

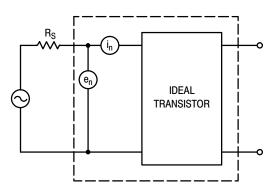


Figure 1. Transistor Noise Model

BC549B,C BC550B,C

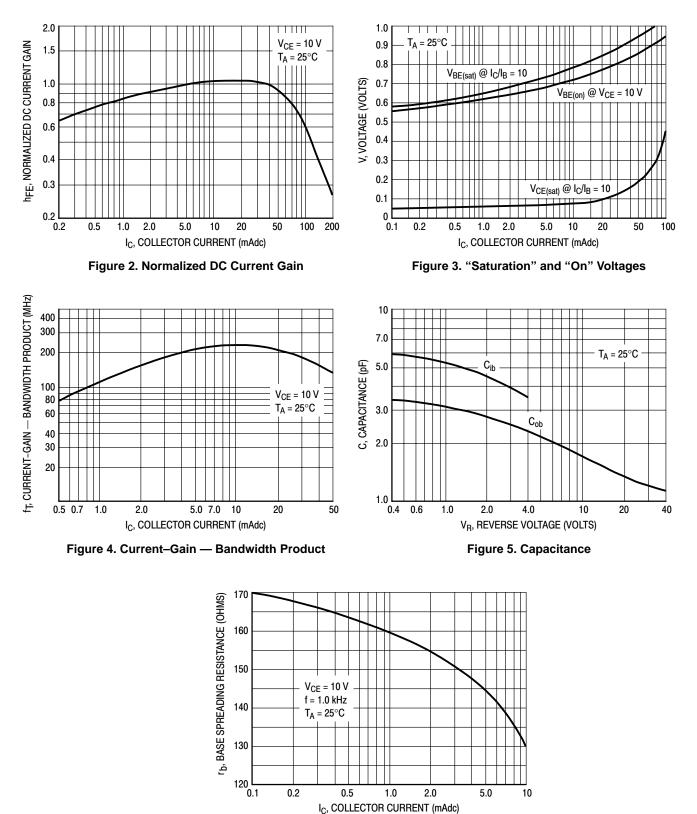
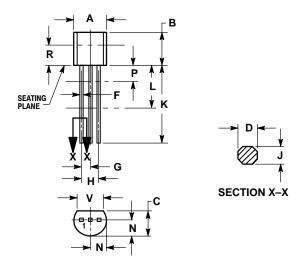


Figure 6. Base Spreading Resistance

PACKAGE DIMENSIONS

CASE 029-04 (TO-226AA) ISSUE AD



NOTES

DIMENSIONING AND TOLERANCING PER ANSI 1. Y14.5M, 1982. CONTROLLING DIMENSION: INCH.

CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. 3.

DIMENSION F APPLIES BETWEEN P AND L. DIMENSION D AND J APPLY BETWEEN L AND K MINIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.45	5.20	
В	0.170	0.210	4.32	5.33	
С	0.125	0.165	3.18	4.19	
D	0.016	0.022	0.41	0.55	
F	0.016	0.019	0.41	0.48	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
J	0.015	0.020	0.39	0.50	
K	0.500		12.70		
L	0.250		6.35		
N	0.080	0.105	2.04	2.66	
Ρ		0.100		2.54	
R	0.115		2.93		
۷	0.135		3.43		

STYLE 17: PIN 1. COLLECTOR 2. BASE 3. EMITTER

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