## Microtips Technology USA 3504 Lake Lynda Drive, Suite 110, Orlando, FL. USA 32817 Product Specification Model: AWY-480272T43PC08 Rev. No. Issued Date. Page.

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## Thin Film Transistor LCD MODULE MODEL: AWY-480272T43PC08 Customer's No.:

Acceptance

3504 Lake Lynda Drive, Suite110, Orlando, FL., USA 32817

Approved and Checked by				

Approved by	Checked by	Made by
MTUSA	MTUSA	MTUSA
2015/12/31	2015/12/31	2015/12/31
NICK	JOE	TOM

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## **Revision Record**

Rev Date	Contents	Note
2015/12/31	New issue.	

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#### 1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by ACROWISE electronics.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution

## 2. General Information LCM

Item	Standard Values	Units
LCD type	4.3"TFT	
Dot arrangement	480(RGB)×272	dots
Color filter array	RGB vertical stripe	
Display mode	TN / Transmission / Normally White	
Gray Scale Inversion Direction	6 O'clock	
Eyes Viewing Direction	12 O'clock	
Driver IC	ST7282E	
Module size	105.6(W)×67.30(H)×4.55(T)	mm
Active area	95.04(W)×53.86(H)	mm
Dot pitch	0.198 (W)×0.198 (H)	mm
Interface	24-bit Parallel RGB Interface	
Operating temperature	-20 ~ +70	°C
Storage temperature	-30 ~ +80	°C
Back Light	10 White LEDS	
Weight	TBD	g

#### **CTP**

Item	Standard Values	Units
CTP type	COVER LENS + SENSOR + FPC	-
CTP Driver IC	FT5306	-
Surface hardness	6H	-
Transmittance	≥83%	-
Operation Voltage	2.8V-3.3 V	
CTP size	105.14(W)×66.21 (H)×1.55(T)	mm
CTP Viewing area	95.84(W)×54.66(H)	mm
CTP Interface	I <sup>2</sup> C	-

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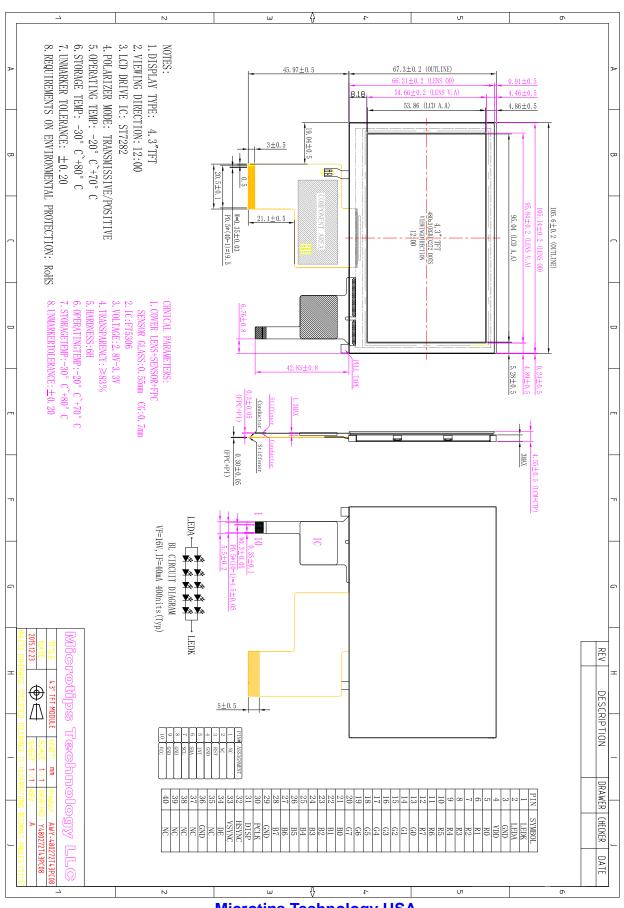
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## 3. External Dimensions



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## 4. Interface Description LCM

	1	
Pin No.	Pin Name	Description
2	LEDK	LED backlight (Cathode).
2	LEDA	LED backlight (Anode).
3	GND	Ground.
4	VDD	Power supply.
5-12	R0-R7	Red Data.
13-20	G0-G7	Green Data.
21-28	B0-B7	Blue Data.
29	GND	Ground
30	PCLK	Dot clock signal input. Latching input data at its rising edge.
31	DISP	Display on/off.
32	HSYNC	Horizontal sync input. Negative polarity.
33	VSYNC	Vertical sync input. Negative polarity.
34	DE	Data enable input. Active high to enable the input data bus.
35	NC	No connection
36	GND	Ground.
37	NC	No connection
38	NC	No connection
39	NC	No connection
40	NC	No connection

## **CTP**

Pin No.	Pin Name	Pin Name
1	NC	No connection
2	NC	No connection
3	RST	CTP reset pin. Active low to enter reset state.
4	GND	CTP Power ground
5	INT	CTP interruption signal.
6	SDA	CTP I <sup>2</sup> C_data
7	SCL	CTP I <sup>2</sup> C_clock.
8	GND	CTP Power ground
9	GND	CTP Power ground
10	VCC	CTP Digital Power.

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5. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit
Logic Supply Voltage	VDD	-0.3	4.6	V
CTP Supply Voltage	VCC	-0.3	3.6	V
Input Voltage	Vin	-0.3	VDD+0.3	V
Operating Temperature	Тор	-20	70	°C
Storage Temperature	Тѕт	-30	80	°C
Storage Humidity	HD	20	90	%RH

#### 6. DC Characteristics

<u></u>						
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Logic Supply Voltage	VDD	3.0	3.3	3.6	V	-
CTP Supply Voltage	VCC	2.5	3.3	3.6	V	
Input High Voltage	V <sub>IH</sub>	0.7 VDD	-	VDD	V	
Input Low Voltage	V <sub>IL</sub>	GND	-	0.3 VDD	V	-
Output High Voltage	V <sub>OH</sub>	VDD-0.4	-	VDD	V	-
Output Low Voltage	V <sub>OL</sub>	GND	-	VDD+0.4	V	-
I/O Leak Current	I⊔	-	-	50	uA	-

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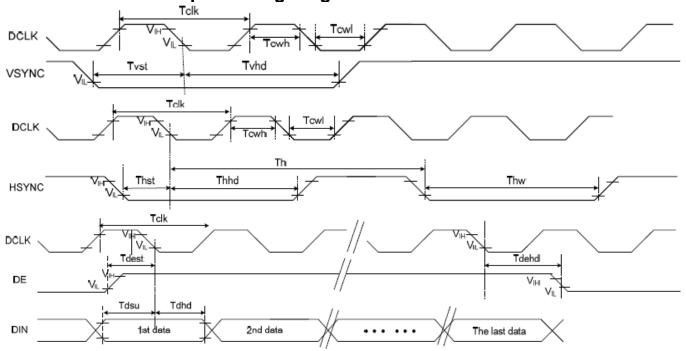
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## 7. Timing Characteristics

## 7.1 Clock and Data Input Timing Diagram



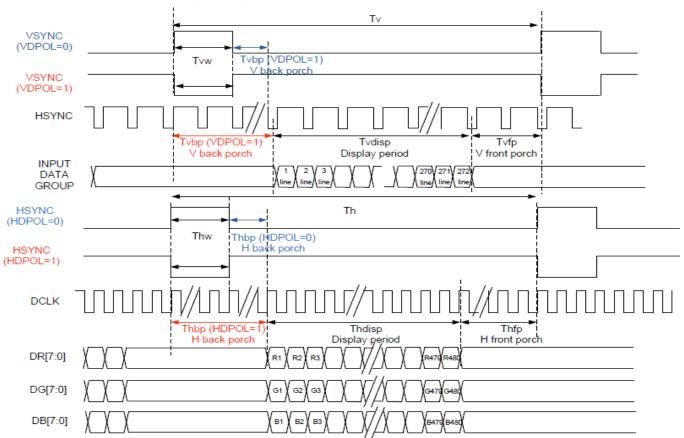
7.2 Serial RGB input timing requirement

112 001141	Item	Symbol	Min.	Тур.	Max.	Unit	Remark
	Item	Syllibol	IVIIII.	тур.	Wax.	Offic	Kelliaik
DCLK Free	quency	Fclk	8	9	12	MHz	
DCLK Per	iod	Tclk	83	111	125	Ns	
HSYNC	Period Time	Th	485	531		DCLK	
	Display Period	Thdisp		480		DCLK	
	Back Porch	Thbp	3	43		DCLK	By H_Blanking setting
	Front Porch	Thfp	2	8		DCLK	
	Pulse Width	Thw	2	4		DCLK	
VSYNC	Period Time	Tv	276	292		Н	
	Display Period	Tvdisp		272		Н	
	Back Porch	Tvbp	2	12		Н	By V_Blanking setting
	Front Porch	Tvfp	2	8		Н	
	Pulse Width	Tvw	2	4		Н	

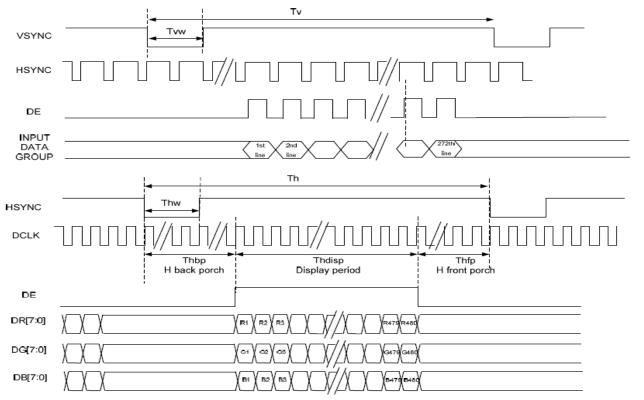
Note: It is necessary to keep Tvbp =12 and Thbp =43 in sync mode. DE mode is unnecessary to keep it.

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## 7.3 SYNC Mode Timing Diagram



## 7.4 SYNC-DE Mode Timing Diagram



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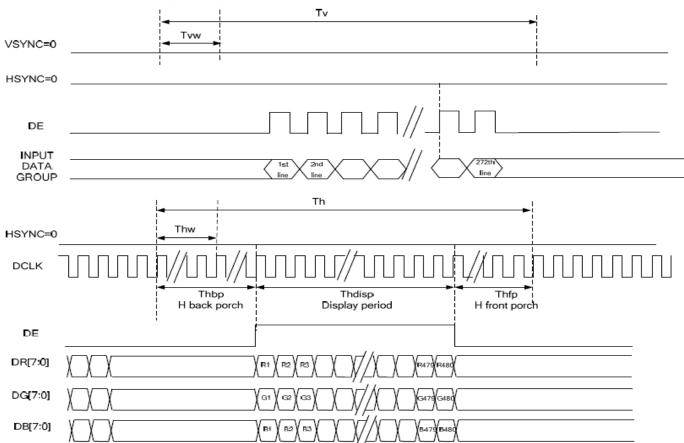
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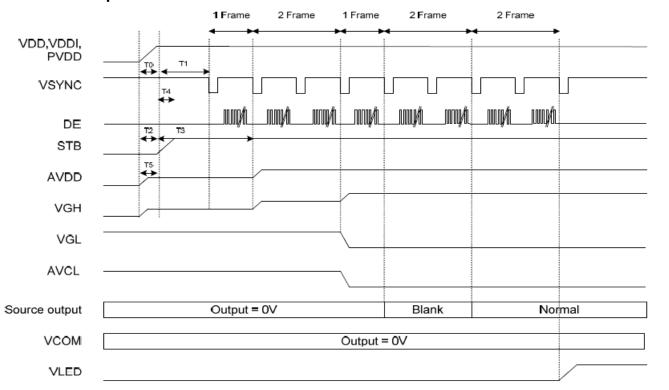
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## 7.5 DE Mode Timing Diagram



## 7.6 Power ONOFF SEQUENCE

#### **Power On Sequence**



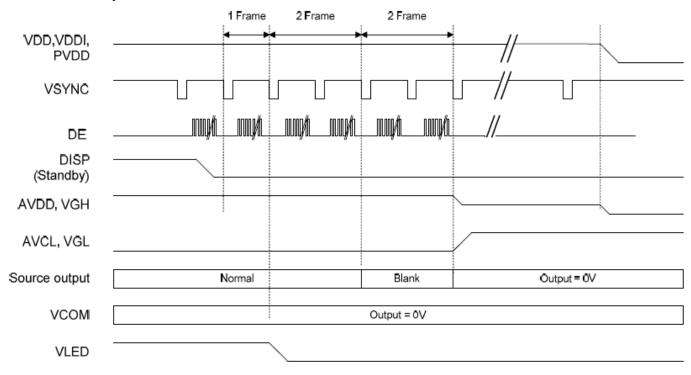
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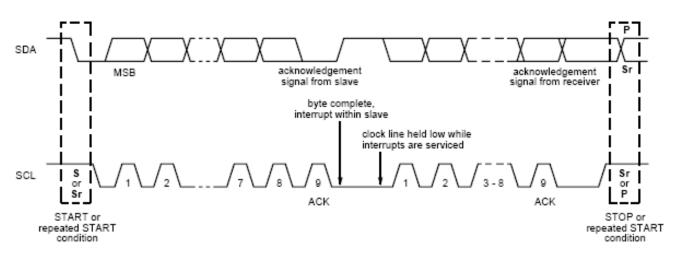
	Description	Min. Time
T0	Determined by the external power	
T1	Time from stable VDD, VDDI, PVDD set-up to the first VSYNC	T1=0
T2	Time from AVDD=0V to AVDD=3.3V	T2=T0
T3	Time from AVDD=3.3V to AVDD=6.0V	T3=T1+ (1*Frame)
T4	Time from stable VDD, VDDI, PVDD set-up to DISP asserted	T4=0
T5	Time from VGH=0V to VGH=3.3V	T5=T0

#### **Power Off Sequence**



## 7.7 I<sup>2</sup>C

#### **I2C Serial Data Transfer Format**



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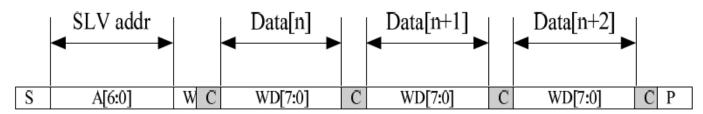
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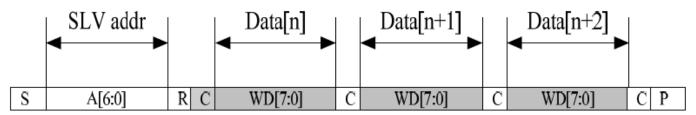
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## I2C master write, slave read



## I2C master read, slave write



## **Mnemonics Description**

Mnemonics	Description
S	I2C Start or I2C Restart
A[6:0]	Slave address A[6:4]: 3'b011 A[3:0]: data bits are identical to those of I2CCON[7:4] register.
W	1'b0: Write
R	1'b1: Read
С	ACK
P	STOP: the indication of the end of a packet (if this bit is missing, S will indicate the end of the current packet and the beginning of the next packet)

#### **I2C Timing Characteristics**

Parameter	Unit	Min	Max
SCL frequency	KHz	0	400
Bus free time between a STOP and START condition	us	4.7	\
Hold time (repeated) START condition	us	4.0	\
Data setup time	ns	250	\
Setup time for a repeated START condition	us	4.7	\
Setup Time for STOP condition	us	4.0	\

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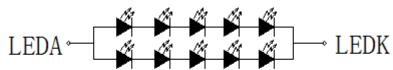
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## 8. Backlight Characteristic



## BL CIRCUIT DIAGRAM

Item	Symbol	MIN	TYP	MAX	UNIT	Test Condition
Supply Voltage	Vf	14.5	16.0	17.5	V	lf=20mA
Supply Current	If	-	40	-	mA	-
Luminous Intensity for LCM	-	350	400	1	cd/m <sup>2</sup>	If=20mA
Uniformity for LCM	-	80	-	-	%	lf=20mA
Life Time	-	20000	(30000)	-	Hr	lf=20mA
Backlight Color	White					

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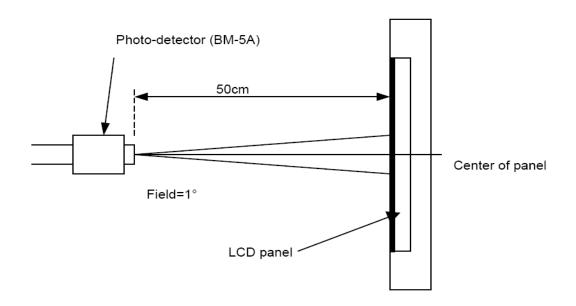
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9. Optical Characteristics

Item	Conditions		Min.	Тур.	Max.	Unit	Note	
	Horizontal	θL	70	80	-	degree		
Viewing Angle	Honzoniai	θR	70	80	-		(1),(2),(6)	
(CR>10)	\	θт	50	50	-			
	Vertical	θв	60	70	-			
Contrast Ratio	Center		500	700	-	-	(1),(3),(6)	
Posnonso Timo	Rising			20 30	30	me	(1),(4),(6)	
Response Time	Falling		-		30	ms		
	Red x			TBD		-		
	Red y			TBD		-		
	Green x			TBD		-		
CF Color	Green y			TBD		-	(1) (6)	
Chromaticity (CIE1931)	Blue x		Тур.	TBD	Тур.	-	(1), (6)	
	Blue y White x White y		-0.05	TBD	+0.05	-		
				TBD		-		
				TBD		-		

Note (1) Measurement Setup: The LCD module should be stabilized at given temp. 25°C for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.



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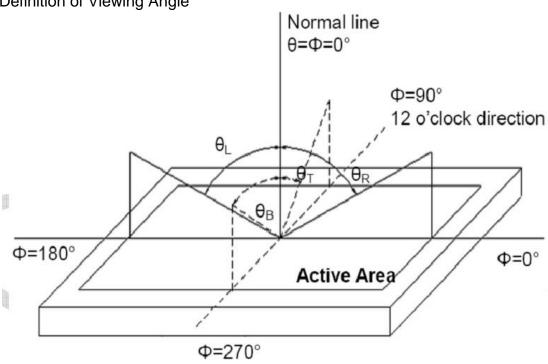
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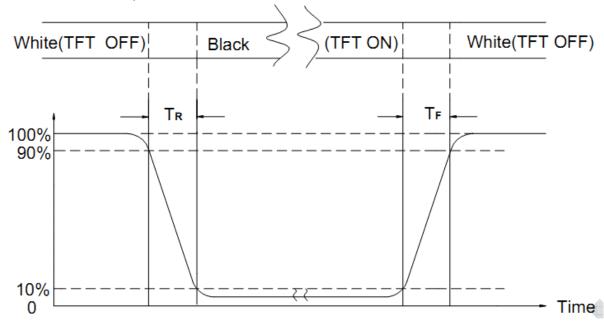




Note (3) Definition of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression Contrast Ratio (CR) = L63 / L0

L63: Luminance of gray level 63, L0: Luminance of gray level 0 Note (4) Definition of response time



Note (5) Definition of Transmittance (Module is without signal input)

Transmittance = Center Luminance of LCD / Center Luminance of Back Light x 100%

Note (6) Definition of color chromaticity (CIE1931)

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Color coordinates measured at the center point of LCD

10. Reliability Test Conditions and Methods

No.	Test Items	Test Condition	Inspection After Test
1	High Temperature Storage	80°C±2°C×200Hours	
2	Low Temperature Storage	-30°C±2°C×200Hours	
3	High Temperature Operating	70°C±2°C×120Hours	
4	Low Temperature Operating	-20°C±2°C×120Hours	Inspection after 2~4hours storage at room temperature, the samples
(5)	Temperature Cycle(Storage)	-20°C $\longrightarrow$ 25°C $\longrightarrow$ 70°C (30min) 1cycle Total 10cycle	should be free from defects: 1, Air bubble in the LCD. 2, Seal leak. 3, Non-display. 4, Missing segments.
6	Damp Proof Test (Storage)	50°C±5°C×90%RH×120Hours	<ul><li>5, Glass crack.</li><li>6, Current IDD is twice</li></ul>
7	Vibration Test	Frequency:10Hz~55Hz~10Hz Amplitude:1.5mm X,Y,Z direction for total 3hours (packing condition test will be tested by a carton)	higher than initial value. 7, The surface shall be free from damage. 8, The electric characteristic requirements shall be satisfied.
8	Drooping Test	Drop to the ground from 1M height one time every side of carton. (packing condition test will be tested by a carton)	onan be satisfied.
9	ESD Test	Voltage:±8KV,R:330Ω,C:150PF,Air Mode,10times	

#### **REMARK:**

- 1. The Test samples should be applied to only one test item.
- 2, Sample side for each test item is 5~10pcs.
- 3, For Damp Proof Test, Pure water(Resistance  $> 10 \text{M}\Omega$ ) should be used.
- 4,In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.
- 5, EL evaluation should be accepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.
- 6, Failure Judgment Criterion: Basic Specification Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

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## 11. Inspection Standard

#### 11.1. QUALITY:

THE QUALITY OF GOODS SUPPLIED TO PURCHASER SHALL COME UP TO THE FOLLOWING STANDARD.

#### 11.1.1. THE METHOD OF PRESERVING GOODS

AFTER DELIVERY OF GOODS FROM AMSON TO PURCHASER. PURCHASER SHALL CONTROL THE LCM AT -10  $^{\circ}$ C TO 40  $^{\circ}$ C ,AND IT MIGHT BE DESIRABLE TO KEEP AT THE NORMAL ROOM TEMPERATURE AND HUMIDITY UNTIL INCOMING INSPECTION OR THROWING INTO PROCESS LINE.

#### 11.1.2. INCOMING INSPECTION

#### (A) THE METHOD OF INSPECTION

IF PURCHASER MAKE AN INCOMING INSPECTION, A SAMPLING PLAN SHALL BE APPLIED ON THE CONDITION THAT QUALITY OF ONE DELIVERY SHALL BE REGARDED AS ONE LOT.

#### (B) THE STANDARD OF QUALITY

ISO-2859-1 (SAME AS MIL-STD-105E), LEVEL II SINGLE PLAN.

CLASS	AQL(%)
CRITICAL	0.4 %
MAJOR	0.65 %
MINOR	1.5 %
TOTAL	1.5 %

EVERY ITEM SHALL BE INSPECTED ACCORDING TO THE CLASS.

#### (C) MEASURE

IF AS THE RESULT OF ABOVE RECEIVING INSPECTION, A LOT OUT IS DISCOVERED. PURCHASER SHALL BE INFORM SELLER OF IT WITHIN SEVEN DAYS. BUT FIRST SHIPMENT WITHIN FOURTEEN DAYS.

#### 11.1.3. WARRANTY POLICY

AMSON WILL PROVIDE ONE-YEAR WARRANTY FOR THE PRODUCTS ONLY IF UNDER SPECIFICATION OPERATING CONDITIONS. AMSON WILL REPLACE NEW PRODUCTS FOR THESE DEFECT PRODUCTS WHICH UNDER WARRANTY PERIOD AND BELONG TO THE RESPONSIBILITY OF AMSON.

#### 11.2. CHECKING CONDITION

- 11.2.1. CHECKING DIRECTION SHALL BE IN THE 45 DEGREE AREA TO FACE THE SAMPLE.
- 11.2.2. CHECKER SHALL SEE OVER 300±25 mm. WITH BARE EYES FAR FROM SAMPLE AND USING 2 PCS. OF 20W FLUORESCENT LAMP.

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#### 11.3. INSPECTION PLAN:

	ı
PACKING & SHOULD INDICATE ON THE PACKAGE  INDICATE  2. MODEL MIXED AND QUANTITY OTHER MODEL MIXEDREJECTE QUANTITY SHORT OR OVERREJECTE  3. PRODUCT INDICATION "MODEL NO." SHOULD INDICATE ON	
QUANTITY SHORT OR OVERREJECTE 3. PRODUCT INDICATION "MODEL NO." SHOULD INDICATE ON	.
	Major
4. DIMENSION, ACCORDING TO SPECIFICATION OR ASSEMBLY LCD GLASS SCRATCH DRAWING. AND SCRIBE DEFECT.	Major
5. VIEWING AREA POLARIZER EDGE OR LCD'S SEALING LI IS VISABLE IN THE VIEWING AREAREJECTED	NE Minor
6. BLEMISH - BLACK SPOT - ACCORDING TO STANDARD OF VISUAL WHITE SPOT IN THE LCD INSPECTION(INSIDE VIEWING AREA)  AND LCD GLASS CRACKS	_ Minor
7. BLEMISH - BLACK SPOT ACCORDING TO STANDARD OF VISUAL INSPECTION (INSIDE VIEWING AREA) ON THE POLARIZER	_ Minor
8. BUBBLE IN POLARIZER ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
9. LCD'S RAINBOW COLOR STRONG DEVIATION COLOR (OR NEW RING) OF LCDREJECTED. OR ACCORDING TO LIMITED SAMPLE (IF NEEDED, AND INSIDE VIEWING AR	Minor
10. ELECTRICAL AND OPTICAL ACCORDING TO SPECIFICATION OR CHARACTERISTICS DRAWING . (INSIDE VIEWING AREA )  (CONTRAST, VOP, CHROMATICITY ETC.)	Critical
ELECTRICAL 11.MISSING LINE MISSING DOT LINE CHARACTERREJECTED	Critical
12.SHORT CIRCUIT  WRONG PATTERN DISPLAY  DISPLAY \ CURRENT CONSUMPTION  OUT OF SPECIFICATION REJECTED	Critical
13. DOT DEFECT (FOR COLOR AND TFT) ACCORDING TO STANDARD OF VISU. INSPECTION	

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11.4	. STANI	DARD OF VISUAL INSPECT	ION			
NO.	CLASS	ITEM	JUDGEMENT			
			(A) ROUND TYPE: unit : mm.			
			DIAMETER (mm.) ACCEPTABLE Q'TY			
			$\Phi \leq 0.1$ DISREGARD			
		BLACK AND WHITE SPOT	$0.1 < \Phi \leq 0.25$ 3 (Distance>5mm)			
		FOREIGN MATERIEL	0.25 < Φ 0			
11.4.1	MINOR	DUST IN THE CELL	NOTE: Φ=(LENGTH+WIDTH)/2			
		BLEMISH	(B) LINEAR TYPE: unit : mm.			
		SCRATCH	LENGTH WIDTH ACCEPTABLE Q'TY			
			V = 0.03 DISREGARD $V = 0.03$ DISREGARD $V = 0.07$ DISREGARD $V = 0.07$ DISREGARD			
			L ≤ 5.0   0.03 < W ≤ 0.07   3 (Distance>5mm)   0.07 < W   FOLLOW ROUND TYPE			
			0.07 VV FOLLOW ROUND TYPE			
			unit : mm.			
			DIAMETER ACCEPTABLE Q'TY			
		BUBBLE IN POLARIZER DENT ON POLARIZER	$\Phi \leq 0.2$ DISREGARD			
11.4.2	MINOR		$0.2 < \Phi \leq 0.5$ 2 (Distance>5mm)			
			0.5 < Ф 0			
$\perp$						
		Dot Defect	Items ACC. Q'TY			
			Bright dot N≦ 4			
			Dark dot N≦ 4			
			Pixel Define: 1. Divol			
			Pixel Define: Pixel ——			
	MINOR		R G B			
11.4.3						
			◆ Dot → ◆ Dot →			
			Note 1: The definition of dot: The size of a defective dot over			
			1/2 of whole dot is regarded as one defective dot.			
			Note 2: Bright dot: Dots appear bright and unchanged in size			
			in which LCD panel is displaying under black pattern.			
			Note 3: Dark dot: Dots appear dark and unchanged in size in			
			which LCD panel is displaying under pure red, green			
			,blue pattern.			

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NO.	CLASS	ITEM	JUDGEMEN	T	
11.4.4	MINOR	LCD GLASS CHIPPING	P X S	Y> S	Reject
11.4.5	MINOR	LCD GLASS CHIPPING	SX	X or Y > S	Reject
11.4.6	MAJOR	LCD GLASS GLASS CRACK	Y	Y > (1/2) T	Reject
11.4.7	MAJOR	LCD GLASS SCRIBE DEFECT	A + B	1. a> L/3 , A 2. B : ACCOF TO DIME	Reject RDING
11.4.8	MINOR	LCD GLASS CHIPPING ( ON THE TERMINAL AREA )	T	$\Phi = (x+y)/2 > 2.5$	mm Reject
11.4.9	MINOR	LCD GLASS CHIPPING ( ON THE TERMINAL SURFACE )	TZX	Y > (1/3) T	Reject
11.4.10	MINOR	LCD GLASS CHIPPING	T Z	Y> T	Reject

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#### 11.5 INSPECTION STANDARD OF TOUCH PANEL

NO.	CLASS	ITEMS JUDGEMENT			
11.5.1	MAJOR	Touch Panel Crack			Reject
11.5.2	MINOR	Touch Panel	Corner	$X \le 2mm, Y \le 2mm, Z < 1/2T$	Accept
TT.5.2 WIINC	MINOR	Chipping	Edge	X ≤ 3mm, Y ≤ 3mm, Z < 1/2T	Accept
			0 11	W≦0.05, L≦20mm	Accept
11.5.3	MINOR Dust and (L		Scratch Foreign materiel inear Type)	0.05mm <w≦0.07mm; l≦10.0mm<br="">Distance between seratch&gt;5.0mm</w≦0.07mm;>	Accept 3 ea Max.
				W>0.07mm	Reject
				Φ ≦ 0.25mm	Accept
11.5.4	11.5.4 MINOR	Scratch Dust and Foreign materiel (Round Type: ⊕=(Length+Width)/2)	0.25mm < ⊕ ≦ 0.35mm Distance between spots > 5.0mm	Accept 5 ea Max.	
				Φ > 0.35mm	Reject
				Φ ≤ 0.35mm	Accept
11.5.5	MINOR	Touch Panel Dent / Fish Eyes		0.35mm<	Accept 3 ea Max.
				Φ > 1.0mm	Reject
				Φ ≤ 0.2mm	Accept
11.5.6	11.5.6 MINOR Touch Panel Air Bubble			0.2mm < ⊕ ≦ 0.5mm Distance between bubbles > 5.0mm	Accept 3 ea Max.
				Φ > 0.5mm	Reject
11.5.7	MINOR	Touch Panel		W≦0.05mm, L≦5mm Distance between scratch>5.0mm	Accept 3 ea Max.
11.5.7	WIINOR	Printir	ng area Scratch	W>0.05mm or L>5mm ( W>0.05 Follow 11.5.4 Round type )	Reject
11.5.8	MINOR	Touch Panel White Haze Mark / Dust		Can not be removed	Reject

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## 12. Handling Precautions

## 12.1 Mounting method

The LCD panel of ACROWISE TFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

#### 12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent [Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (CI), Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

#### 12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to power or ground, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

#### 12.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

#### 12.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

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Usage under the maximum operating temperature, 50%Rh or less is required.

#### 12.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
   [It is recommended to store them as they have been contained in the inner container at the time of delivery from us.

#### 12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

#### 13. Precaution for Use

#### 13.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

#### 13.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported to ACROWISE TFT, and some problem is arisen in this specification due to the change
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

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