

Operational Amplifier

Features

- · 4V to 32V Operation
- · Small Footprint Package
- · Unity Gain Stable
- · 2.5 MHz Unity Gain Bandwidth
- · Rail-to-Rail Output
- 6 V/µs Typical Slew Rate
- · Short Circuit Protected

Applications

- · Analog Blocks
- · Data Acquisition
- · Sensor Interface
- · Portable Instrumentation
- · Active Filtering

General Description

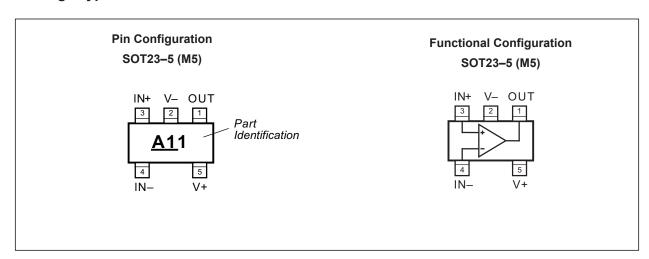
The MIC6211 op amp is a general-purpose, high performance, single- or split-supply, operational amplifier in a space-saving, surface-mount package.

The MIC6211 operates from 4V to 32V, single or differential (split) supply. The input common-mode range includes ground. The device features a 2.5 MHz unity gain bandwidth, 6 V/ μ s slew rate, and is internally unity-gain compensated.

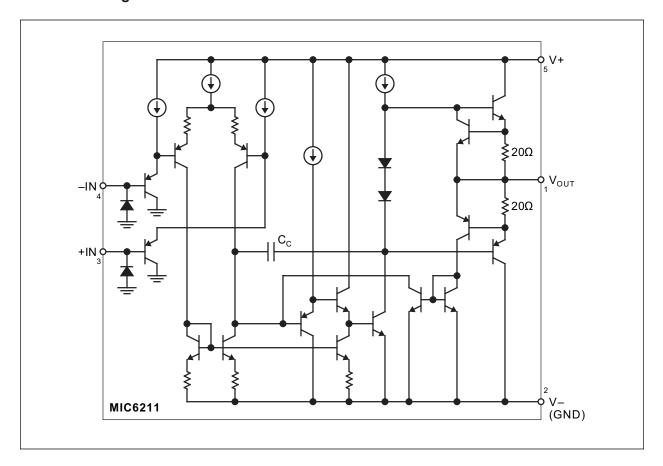
Inputs are protected against reverse polarity (input voltage less than V–) and ESD (electrostatic discharge). Output is current-limited for both sourcing and sinking. Output short-circuits of unlimited duration are allowed, provided the power dissipation specification is not exceeded.

The MIC6211 is available in the tiny, 5-lead SOT-23-5 surface-mount package.

Package Type



Functional Diagram



1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings †

Supply Voltage (V _{V+} to V _{V-})	36V or ±18V
Differential Input Voltage (V _{IN+} to V _{IN-})	
Input Voltage (V _{IN+} , V _{IN})	$(V_{V-} - 0.3V)$ to V_{V+}
Output Short-Circuit Current Duration	
Operating Ratings ‡	
Supply Voltage (V+ to V-)	+4V to +32V

[†] Notice: Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational sections of this specification is not intended. Exposure to maximum rating conditions for extended periods may affect device reliability.

[‡] Notice: The device is not guaranteed to function outside the operating ratings.

ELECTRICAL CHARACTERISTICS (DIFFERENTIAL SUPPLY)

Electrical Characteristics: V+ = +15V, V- = -15V V_{CM} = 0V; R_L = 2 $k\Omega$; T_A = 25°C, T_A = T_J ; unless otherwise noted.						
Parameters	Symbol	Min.	Тур.	Max.	Units	Conditions
Input Offset Voltage	V _{OS}		2	7	mV	_
Average Input Offset Drift	TCV _{OS}		7	_	μV/°C	(Note 1)
Input Bias Current	Ι _Β	1	50	250	nA	_
Input Offset Current	I _{OS}	1	8	30	nA	_
Input Voltage	.,	+13.5	+13.8		\ /	
Range	V _{CM}	-15.0	-15.3	_	V	_
Common-Mode Rejection Ratio	CMRR	65	100	_	dB	V _{CM} = +13.5V, -15.0V
Power Supply Rejection Ratio	PSRR	65	110	_	dB	V _S = ±2.5V to ±15V
Large-Signal Voltage Gain	A _{VOL}	25	180	_	V/mV	V _O = ±10V
Maximum Output Voltage Swing	V _{OUT}	±12.5	±14	_	V	_
Bandwidth	B_W	_	2.5	_	MHz	_
Slew Rate	S _R	_	6	_	V/µs	_
Short-Circuit Output Current	I _{SC}	30	50	_	mA	Sourcing or Sinking
Supply Current	I _S	_	1.3	2.0	mA	_

Note 1: Not production tested.

ELECTRICAL CHARACTERISTICS (SINGLE SUPPLY)

Electrical Characteristics: V+ = +5V, V- = 0V, V_{CM} = 0.1V; T_A = 25°C, T_A = T_J ; unless otherwise noted.							
Parameters	Symbol	Min.	Тур.	Max.	Units	Conditions	
Input Offset Voltage	Vos	_	2	7	mV	_	
Average Input Offset Drift	TCV _{OS}	_	7	_	μV/°C	(Note 1)	
Input Bias Current	I _B	_	65	250	nA	_	
Input Offset Current	Ios	_	8	30	nA	_	
Input Voltage	V	+3.5	+3.7		V		
Range	V _{CM}	0	-0.3	_	V		
Common-Mode Rejection Ratio	CMRR	45	70	_	dB	V _{CM} = 0V to 3.5V	
Power Supply Rejection Ratio	PSRR	65	105	_	dB	V _S = ±2.5V to ±15V	
Large-Signal Voltage Gain	A _{VOL}	15	170	_	V/mV	V_{O} = 1.5V to 3.5V, R_{L} = 2 k Ω	
Maximum Output Voltage Swing	V _{OUT}	±3.8	+4.0		V	R_L = 10 kΩ to GND	
			+1.0	+1.2		R_L = 10 kΩ to +5V	
Short-Circuit Output Current	I _{SC}	20	40	_	mA	Sourcing or Sinking	
Supply Current	I _S	_	1.2	1.8	mA	_	

Note 1: Not production tested.

TEMPERATURE SPECIFICATIONS (Note 1)

Parameters	Symbol	Min.	Тур.	Max.	Units	Conditions
Temperature Ranges						
Ambient Temperature Range	T _A	-40	_	+85	°C	_
Package Thermal Resistance						
Thermal Resistance SOT-23-5	θ_{JA}	_	200	_	°C/W	Mounted to PCB

Note 1: The maximum allowable power dissipation is a function of ambient temperature, the maximum allowable junction temperature and the thermal resistance from junction to air (i.e., T_A, T_J, θ_{JA}). Exceeding the maximum allowable power dissipation will cause the device operating junction temperature to exceed the maximum +85°C rating. Sustained junction temperatures above +85°C can impact the device reliability.

2.0 TYPICAL PERFORMANCE CURVES

Note: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

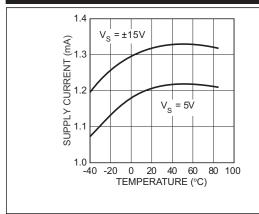


FIGURE 2-1: Supply Current vs. Temperature.

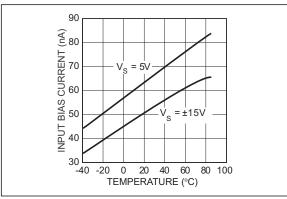


FIGURE 2-2: Input Bias Current vs. Temperature.

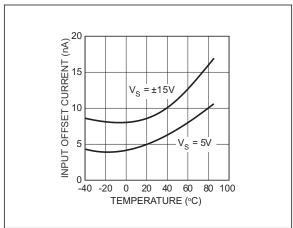


FIGURE 2-3: Input Offset Current vs. Temperature.

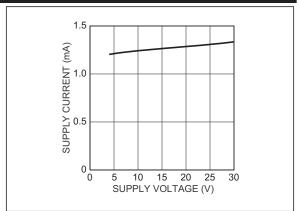


FIGURE 2-4: Supply Current vs. Supply Voltage.

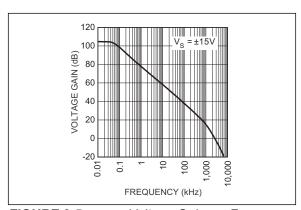


FIGURE 2-5: Voltage Gain vs. Frequency.

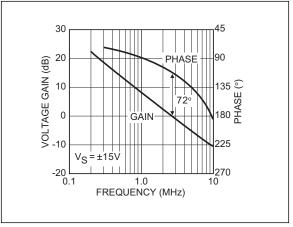


FIGURE 2-6: Gain and Phase vs. Frequency.

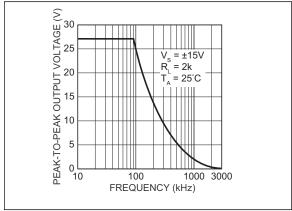


FIGURE 2-7: Response.

Large-Signal Frequency

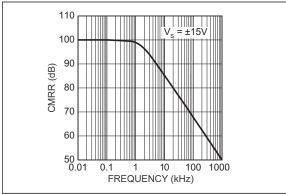


FIGURE 2-8: Ratio vs. Frequency.

Common Mode Rejection



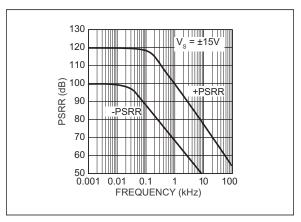
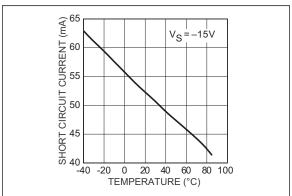


FIGURE 2-9: Power Supply Rejection Ratio vs. Frequency.



Short-Circuit Current vs.

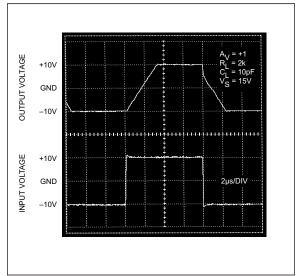
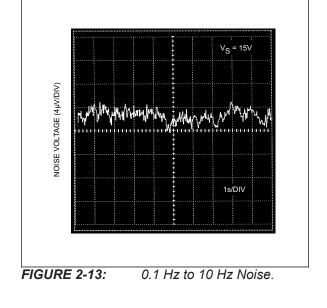


FIGURE 2-11: Large-Signal Transient Response.



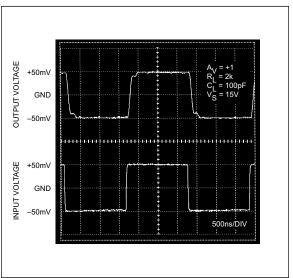


FIGURE 2-12: Small-Signal Transient Response.

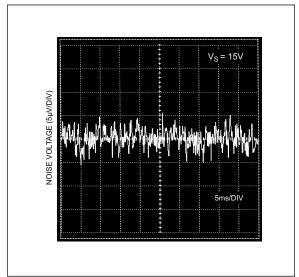


FIGURE 2-14: Wideband Noise.

3.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in Table 3-1.

TABLE 3-1: PIN FUNCTION TABLE

Pin Number	Symbol	Description
1	OUT	Amplifier Output.
2	V–	Negative Supply. Negative supply for split supply application or ground for single supply application.
3	IN+	Non-Inverting Input.
4	IN-	Inverting Input.
5	V+	Positive Supply.

4.0 APPLICATION INFORMATION

4.1 Common-Mode Range and Output Voltage

The input common-mode range of the MIC6211 is from the negative supply voltage to 1.2V below the positive supply voltage. The output voltage swings within 1V of the positive and negative supply voltage.

4.2 Voltage Buffer

Figure 4-1 shows a standard voltage follower/buffer. The output voltage equals the input voltage. This circuit is used to buffer a high impedance signal source. This circuit works equally well with single or split supplies.

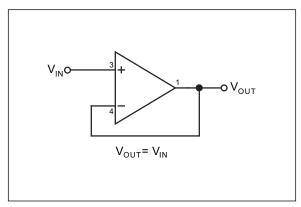


FIGURE 4-1: Voltage Buffer.

4.3 Inverting Amplifier

Figure 4-2 shows an inverting amplifier with its gain set by the ratio of two resistors. This circuit works best with split supplies, but will perform with single supply systems if the non-inverting input (+ input) is biased up above ground.

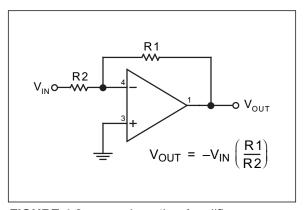


FIGURE 4-2: Inverting Amplifier.

4.4 Voltage Controlled Current Sink

Figure 4-3 is a voltage controlled current sink. A buffer transistor forces current through a programming resistor until the feedback loop is satisfied. Current flow is V_{IN}/R . This circuit works with single or split supplies.

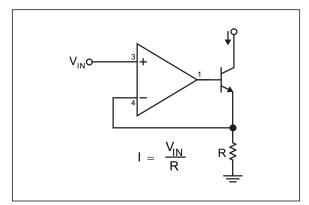


FIGURE 4-3: Voltage Controlled Current Sink.

4.5 High-Pass Filter

Figure 4-4 is an active filter with 20 dB (10×) gain and a low frequency cutoff of 10 Hz. The high gain-bandwidth of the MIC6211 allows operation beyond 100 kHz. This filter configuration is designed for split supplies.

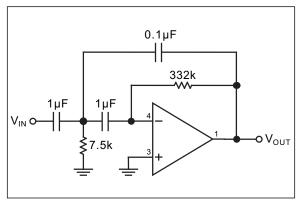


FIGURE 4-4: High—Pass Filter.

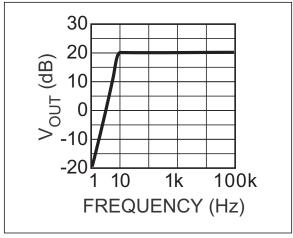


FIGURE 4-5: High-Pass Filter Response.

4.6 Summing Amplifier

Figure 4-5 is a single supply summing amplifier. In this configuration, the output voltage is the sum of V1 and V2, minus the sum of V3 and V4. By adding more resistors to either the inverting or non-inverting input, more voltages may be summed. This single supply version has one important restriction: the sum of V1 and V2 must exceed the sum of V3 and V4, since the output voltage cannot pull below zero with only a single supply.

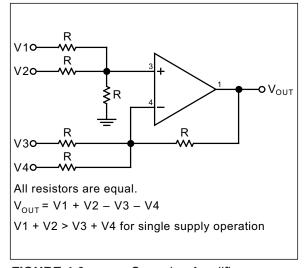
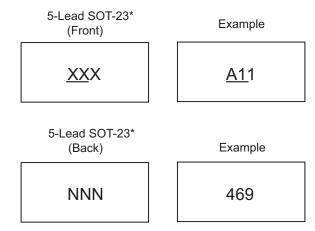


FIGURE 4-6: Summing Amplifier.

5.0 PACKAGING INFORMATION

5.1 Package Marking Information



Legend: XX...X Product code or customer-specific information Y Year code (last digit of calendar year)

YY Year code (last 2 digits of calendar year)
WW Week code (week of January 1 is week '01')

NNN Alphanumeric traceability code

Pb-free JEDEC® designator for Matte Tin (Sn)

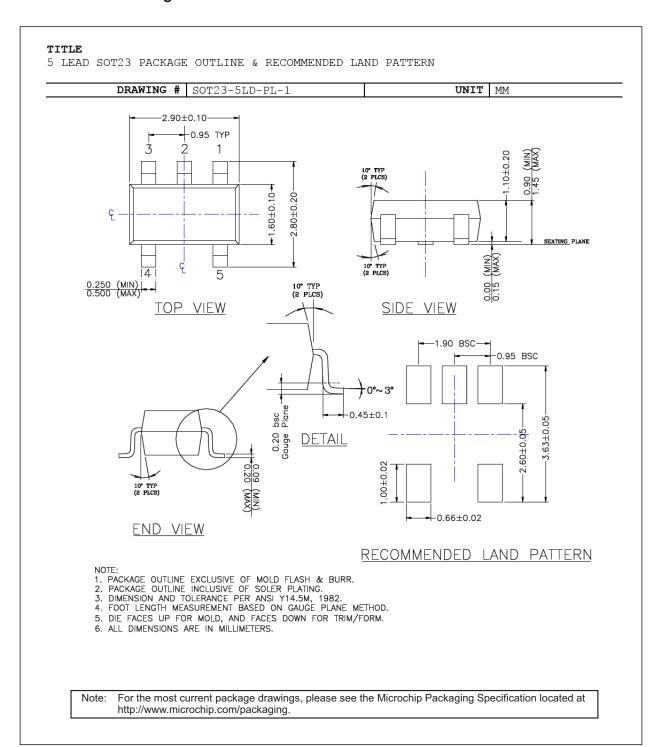
This package is Pb-free. The Pb-free JEDEC designator (e3) can be found on the outer packaging for this package.

•, ▲, ▼ Pin one index is identified by a dot, delta up, or delta down (triangle mark).

Note: In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for customer-specific information. Package may or may not include the corporate logo.

Underbar (_) and/or Overbar (_) symbol may not be to scale.

5-Lead SOT23 Package Outline and Recommended Land Pattern



APPENDIX A: REVISION HISTORY

Revision A (July 2020)

- Converted Micrel document MIC6211 to Microchip data sheet template DS20006346A.
- Minor text changes throughout.

NOTES:

PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, contact your local Microchip representative or sales office.

PART NO. XXDevice Temperature Package Media Type

Device: MIC6211: Operational Amplifier

-40°C to +85°C Temperature:

Package: 5-Lead SOT-23

Media Type: TR = 3,000/Reel Examples:

MIC6211YM5-TR: Op Amp, -40°C to +85°C

Junction Temperature Range, 5-Lead SOT-23 Package, 3,000/Reel

Tape and Reel identifier only appears in the catalog part number description. This identifier is used for ordering purposes and is not printed on the device package. Check with your Microchip Sales Office for package availability with the Tape and Reel option. Note 1:

NOTES:

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