Fast Diode Common Cathode Power Module

APTDX600KK170D16AG



Product Overview

The APTDX600KK170D16AG device is a 1700V, 600A fast diode common cathode power module.

The following figures show the electrical diagram and pinout location of the device.

Figure 1. Electrical Diagram

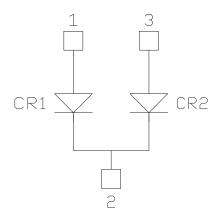
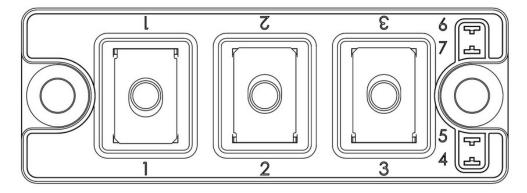


Figure 2. Pinout Location



Note: All ratings are at $T_1 = 25$ °C, unless otherwise specified.



These devices are sensitive to electrostatic discharge. Proper handling procedures must be followed.

Features

The APTDX600KK170D16AG device has the following key features:

- · Fast-recovery times
- · Soft-recovery characteristics
- High-blocking voltage
- High current
- Low-leakage current
- M6 power connectors
- Aluminum Nitride (AlN) substrate for improved thermal performance

Benefits

The APTDX600KK170D16AG device has the following benefits:

- · Low losses
- Low-noise switching
- Direct mounting to heatsink (isolated package)
- Low junction-to-case thermal resistance
- RoHS Compliant

Application

The APTDX600KK170D16AG device has the following applications:

- Uninterruptible Power Supply (UPS)
- Induction heating
- Welding equipment
- High-speed rectifiers



1. Electrical Specification

The following sections describe the electrical specifications of the APTDX600KK170D16AG device.

1.1 Diode Characteristics (Per Diode)

The following table lists the absolute maximum ratings of the APTDX600KK170D16AG device.

Table 1-1. Absolute Maximum Ratings

Symbol	Parameter			Maximum Ratings	Unit
V_{RRM}	Peak repetitive reverse voltage			1700	V
IF	DC forward current		T _C = 25 °C	670 ¹	Α
			T _C = 50 °C	600	
I _{FSM}	Non-repetitive forward surge current	$t_P = 8.3 \text{ ms}$	T _C = 45 °C	3000	
I _{FRM}	Repetitive forward current	t _P = 1 ms	_	1200	

Note:

1. Specification of the diode device, but the output current must be limited due to size of the power connectors.

The following table lists the electrical characteristics of the APTDX600KK170D16AG device.

Table 1-2. Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min.	Тур.	Max.	Unit
V _F	Diode forward voltage	I _F = 600A	T _J = 25 °C	_	2.35	2.7	٧
			T _J = 125 °C	_	2.25	_	
			T _J = 175 °C	_	2.1	_	
I _{RRM}	Reverse leakage current	V _R = 1700V		_	_	50	μΑ

The following table lists the dynamic characteristics of the APTDX600KK170D16AG device.

Table 1-3. Dynamic Characteristics

Symbol	Characteristic	Test Conditions		Min.	Тур.	Max.	Unit
I _{rm}	Reverse recovery current	I _F = 600A	T _J = 25 °C	_	646	_	Α
		V _R = 900V	T _J = 125 °C	_	700	_	
		di/dt = 9500 A/μs	T _J = 175 °C	_	702	_	
Q _{rr}	Reverse recovery charge		T _J = 25 °C	_	75	_	μC
			T _J = 125 °C	_	143	_	
			T _J = 175 °C	_	187	_	
E _{rr}	Reverse recovery energy		T _J = 25 °C	_	52.4	_	mJ
			T _J = 125 °C	_	89.2	_	
			T _J = 175 °C	_	114	_	
R _{thJC}	Junction-to-case thermal resista	nce		_	_	0.102	°C/W



1.2 Thermal and Package Characteristics

The following table lists the thermal and package characteristics of the APTDX600KK170D16AG device.

Table 1-4. Thermal and Package Characteristics

Symbol	Characteristic				Max.	Unit
V _{ISOL}	RMS isolation voltage, any terminal to case, t = 1 min, 50/60Hz			4000	_	٧
Tj	Operating junction temperature range	Operating junction temperature range			175	°C
T _{JOP}	Recommended junction temperature un	-40	T _{Jmax} -25			
T _{STG}	Storage temperature range			-40	125	
T _C	Operating case temperature		-40	125		
Torque	Mounting torque	For terminals	M6	3	5	N.m
		To heatsink				
Wt	Package weight			_	160	g



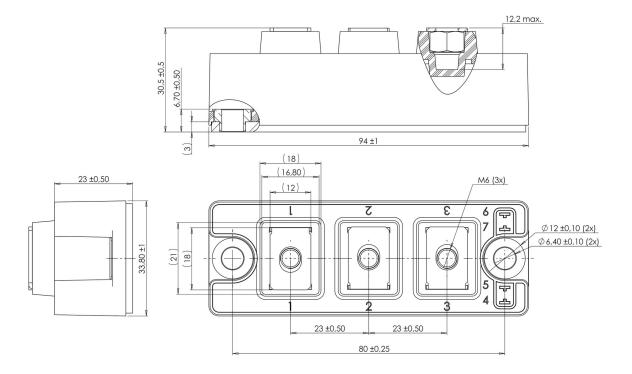
2. Package Specifications

The following section describes the package specification of the APTDX600KK170D16AG device.

2.1 Package Outline

The following figure shows the package outline drawing of the APTDX600KK170D16AG device. The dimensions in the following figure are in millimeters.

Figure 2-1. Package Outline Drawing





3. Typical Performance Curve

The following figures show the performance curves of the APTDX600KK170D16AG device.

Figure 3-1. Maximum Thermal Impedance

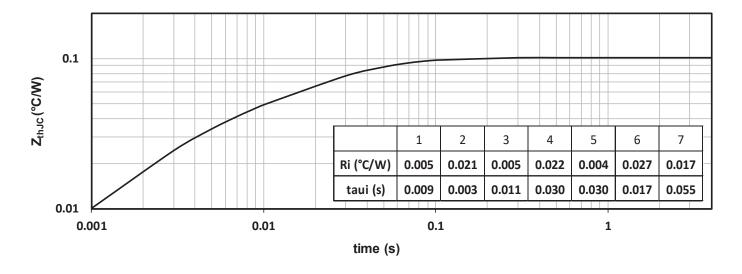


Figure 3-2. Forward Current vs. Forward Voltage

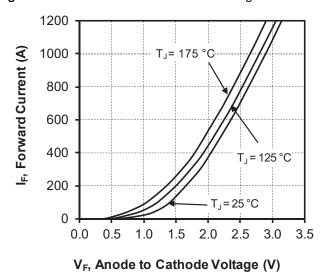
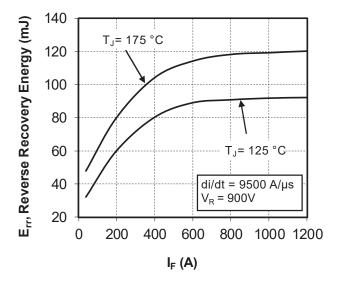
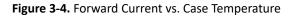
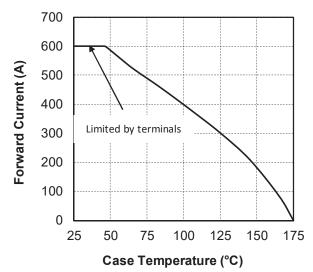


Figure 3-3. Energy losses vs. Forward Current









4. Revision History

The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

Revision	Date	Description
A	02/2024	Initial revision



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