

Product Overview

The APTDF300KK120D16AG device is a 1200V, 300A fast diode common-cathode power module. The following figures show the electrical diagram and pinout location of the device.

Figure 1. Electrical Diagram

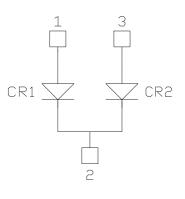
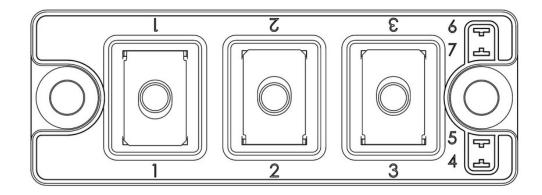


Figure 2. Pinout Location



Note: All ratings are at $T_1 = 25$ °C, unless otherwise specified.

Λ CAUTION These devices are sensitive to electrostatic discharge. Proper handling procedures must be followed.

Features

The APTDF300KK120D16AG device has the following key features:

- Fast-recovery times
- Soft-recovery characteristics
- High-blocking voltage
- High current
- Low-leakage current
- M6 power connectors
- Aluminum Nitride (AIN) substrate for improved thermal performance

Benefits

The APTDF300KK120D16AG device has the following benefits:

- Outstanding performance at high-frequency operation
- Low losses
- Low-noise switching
- Direct mounting to heatsink (isolated package)
- Low junction-to-case thermal resistance
- RoHS Compliant

Application

The APTDF300KK120D16AG device has the following applications:

- Uninterruptible Power Supply (UPS)
- Induction heating
- Welding equipment
- High-speed rectifiers



1. Electrical Specification

The following sections describe the electrical specifications of the APTDF300KK120D16AG device.

1.1 Diode Characteristics (Per Diode)

The following table lists the absolute maximum ratings of the APTDF300KK120D16AG device.

Symbol	Parameter			Maximum Ratings	Unit
V _{RRM}	Peak repetitive reverse voltage			1200	V
I _F	DC forward current		T _C = 25 °C	490	А
			T _C = 100 °C	300	
I _{FSM}	Non-repetitive forward surge current	t _P = 8.3 ms	T _C = 45 °C	1500	
I _{FRM}	Repetitive forward current	t _P = 1 ms	_	600	

Table 1-1. Absolute Maximum Ratings

The following table lists the electrical characteristics of the APTDF300KK120D16AG device.

Table 1-2. Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min.	Тур.	Max.	Unit
V _F	Diode forward voltage	I _F = 300A		—	2.8	3.8	V
		I _F = 600A		-	3.5	—	
		I _F = 300A	T _J = 125 °C	_	2.2	-	
I _{RRM}	Reverse leakage current	V _R = 1200V		—	-	400	μΑ
CT	Junction capacitance	V _R = 200V		_	200	-	pF

The following table lists the dynamic characteristics of the APTDF300KK120D16AG device.

Symbol	Characteristic	Test Conditions		Min.	Тур.	Max.	Unit
t _{rr}	Reverse recovery time	I _F = 300A	T _J = 25 °C	—	325	—	ns
		V _R = 800V	T _J = 125 °C	—	420	—	
Q _{rr}	Reverse recovery charge	di/dt = 800 A/µs	T _J = 25 °C	—	0.29	—	μC
			T _J = 125 °C	—	13.3	—	
I _{rm}	Reverse recovery current		T _J = 25 °C	_	20	—	A
			T _J = 125 °C	—	52	—	
t _{rr}	Reverse recovery time	I _F = 300A	T _J = 125 °C	_	195	—	ns
Q _{rr}	Reverse recovery charge	V _R = 800V		—	23.2	—	μC
I _{rm}	Reverse recovery current	di/dt = 4000 A/µs		—	168	—	А
R _{thJC}	Junction-to-case thermal resis	tance		_	_	0.126	°C/W



1.2 Thermal and Package Characteristics

The following table lists the thermal and package characteristics of the APTDF300KK120D16AG device.

Symbol	Characteristic	Characteristic		Min.	Max.	Unit
V _{ISOL}	RMS isolation voltage, any terminal to case,	RMS isolation voltage, any terminal to case, t = 1 min, 50/60Hz		4000	—	V
Tj	Operating junction temperature range			-40	175	°C
T _{JOP}	Recommended junction temperature under	switching cond	itions	-40	T _{Jmax} – 25	
T _{STG}	Storage temperature range			-40	125	
T _C	Operating case temperature			-40	125	
Torque	Mounting torque	For terminals	M6	3	5	N.m
		To heatsink				
Wt	Package weight			_	160	g



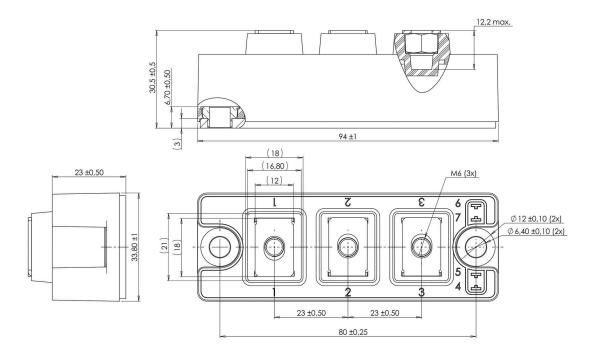
2. Package Specifications

The following section describes the package specification of the APTDF300KK120D16AG device.

2.1 Package Outline

The following figure shows the package outline drawing of the APTDF300KK120D16AG device. The dimensions in the following figure are in millimeters.

Figure 2-1. Package Outline Drawing





3. Typical Performance Curve

The following figures show the performance curves of the APTDF300KK120D16AG device.

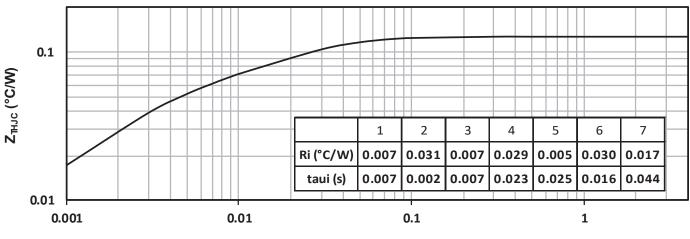


Figure 3-1. Maximum Thermal Impedance



Figure 3-2. Forward Current vs. Forward Voltage

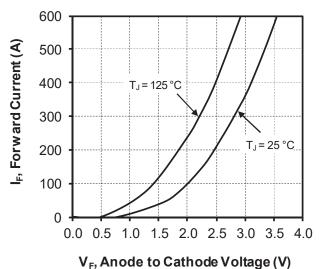
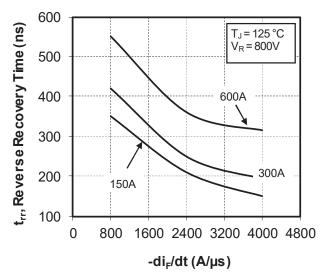


Figure 3-3. t_{rr} vs. Current Rate of Charge





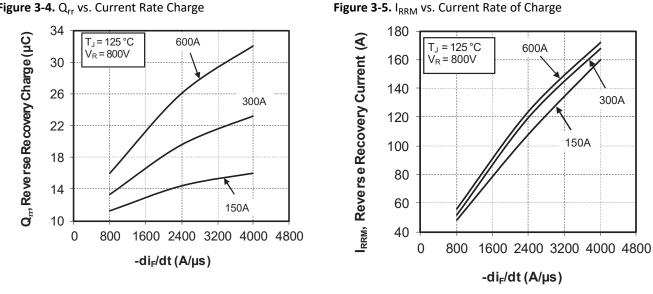
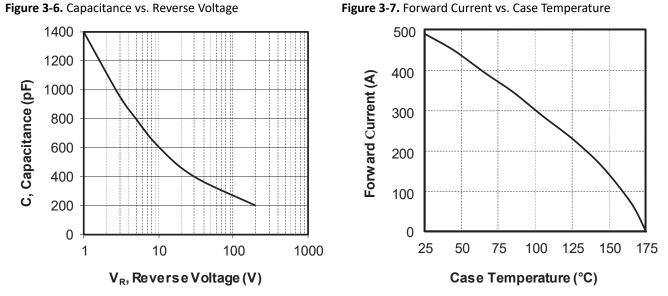


Figure 3-4. Q_{rr} vs. Current Rate Charge







4. Revision History

The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

Revision	Date	Description
A	01/2024	Initial revision



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<u>PART NO</u>). [X](1)	<u>–X</u>	/ <u>XX</u>
Device	 Tape and Reel	 Temperature Range	e Package

Device:	Device A, Device B, etc	
Tape and Reel Option:	Blank	= Standard packaging (tube or tray)
	Т	= Tape and Reel ⁽¹⁾
Temperature Range:	1	= -40°C to +85°C (Industrial)
	E	= -40°C to +125°C (Extended)
Package: ⁽²⁾	JQ	= UQFN
	Р	= PDIP
	ST	= TSSOP
	SL	= SOIC-14
	SN	= SOIC-8
	RF	= UDFN
Pattern:	QTP, SQTP SM (Serial Quick Turn Programm Requirements (blank otherwise)	ning capability), Code or Special

- Device A I/P Industrial temperature, PDIP package
- Device B E/SS Extended temperature, SSOP package

PIS_NOTES

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