

#### **Features**

- · Advanced Trench Cell Design
- Halogen Free. "Green" Device (Note 1)
- Moisture Sensitivity Level 3
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

# **Maximum Ratings**

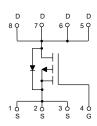
- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 60°C/W Junction to Ambient(Note2)
- Thermal Resistance: 22°C/W Junction to Case

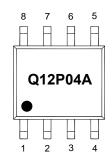
Themal Resistance. 22 6,77 Garietien to Gase					
Parameter		Symbol	Rating	Unit	
Drain-Source Voltage		V <sub>DS</sub>	-40	V	
Gate-Source Volltage		V <sub>GS</sub>	±20	V	
Continuous Drain Current	T <sub>A</sub> =25°C		-12	А	
	T <sub>A</sub> =100°C	I <sub>D</sub>	-7.6		
Pulsed Drain Current <sup>(Note3)</sup>		I <sub>DM</sub>	-48	Α	
Total Power Dissipation <sup>(Note4)</sup>		P <sub>D</sub>	2.0	W	
Single Pulsed Avalanche Energy <sup>(Note5)</sup>		E <sub>AS</sub>	64	mJ	

#### Note:

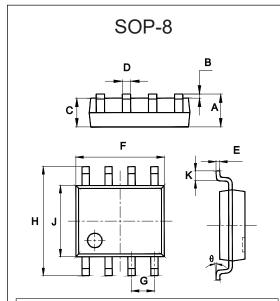
- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. The value of  $R_{\theta JA}$  is measured with the device mounted on  $1in^2$  FR-4 board with 2oz. Copper, in a still air environment with  $T_A$  =25°C. The Power dissipation  $P_{DSM}$  is based on  $R_{\theta JA}$  t≤ 10s and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- 3. Repetitive rating; pulse width limited by max. junction temperature.
- 4.  $P_{\text{D}}$  is based on max. junction temperature, using junction-ambient thermal resistance.
- 5.  $T_J$ =25°C,  $V_{DD}$ =-30V,  $V_{GS}$ =-10V, L=0.5mH

#### **Internal Structure and Marking Code**



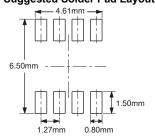


# P-Channel Power MOSFET



DIMENSIONS						
DIM INCHE		HES	MM		NOTE	
DIIVI	MIN		MIN MAX			
Α	0.053	0.069	1.35	1.75		
В	0.004	0.010	0.10	0.25		
С	0.053	0.061	1.35	1.55		
D	0.013	0.020	0.33	0.51		
E	0.007	0.010	0.17	0.25		
F	0.185	0.200	4.70	5.10		
G	0.050		1.270		TYP.	
Н	0.228	0.244	5.80	6.20		
J	0.150	0.157	3.80	4.00		
K	0.016	0.050	0.40	1.27		
θ	0°	8°	0°	8°		

#### Suggested Solder Pad Layout



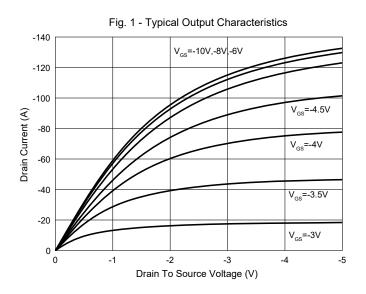


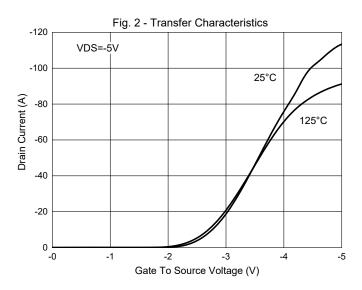
# ELECTRICAL CHARACTERISTICS (T\_A=25 $^{\circ}\text{C}$ unless otherwise specified)

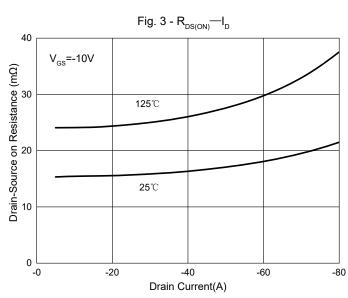
Parameter	Symbol	Test conditions	Min	Тур	Max	Unit	
Static Characteristics			1				
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-40			V	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1.0	-1.6	-2.5	V	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V			±100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-40V, V <sub>GS</sub> =0V			-1.0	μA	
Drain-Source On-Resistance	В	V <sub>GS</sub> =-10V, I <sub>D</sub> =-20A		17	21	mΩ	
Drain-Source On-Resistance	$R_{DS(on)}$	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-10A	21 26		26	- 11122	
Gate Resistance	R <sub>g</sub>	f=1MHz, Open drain		10		Ω	
Diode Characteristics				•			
Continuous Body Diode Current	Is				-12	Α	
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-20A			-1.2	V	
Reverse Recovery Time	t <sub>rr</sub>	- I <sub>S</sub> =-6A, dI <sub>F</sub> /dt=100A/μs		38		ns	
Reverse Recovery Charge	Q <sub>rr</sub>	1804, dif/di-1004/µs		29		nC	
Dynamic Characteristics							
Input Capacitance	C <sub>iss</sub>			2500			
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =-20V, $V_{GS}$ =0V,f=1MHz		196		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			188			
Total Gate Charge	Qg			60.3			
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =-20V, $V_{GS}$ =-10V, $I_{D}$ =-6A		6.6		nC	
Gate-Drain Charge	$Q_{gd}$			12.4			
Turn-On Delay Time	t <sub>d(on)</sub>			7.7			
Turn-On Rise Time	t <sub>r</sub>	V <sub>DS</sub> =-20V, V <sub>GEN</sub> =-10V,		3.6		ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_G=3\Omega$ , $I_{DS}=-6A$		145			
Turn-Off Fall Time	t <sub>f</sub>			53			

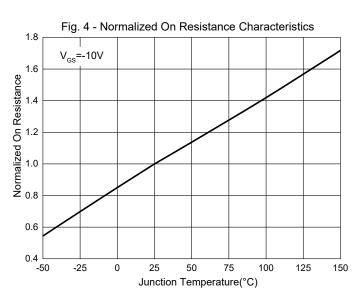


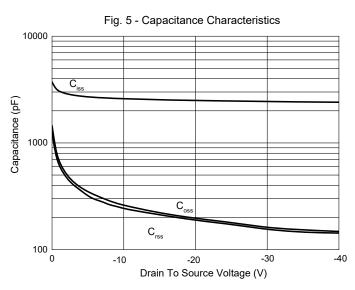
# **Curve Characteristics**

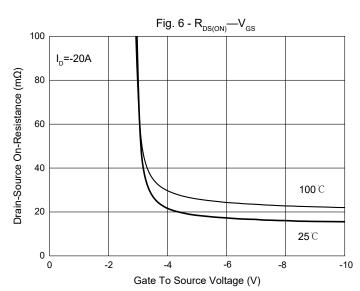






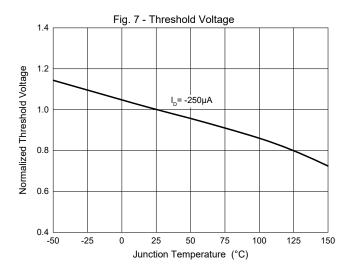


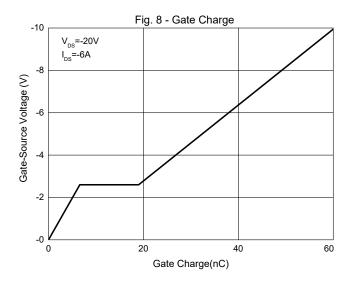


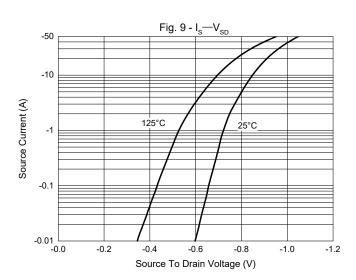


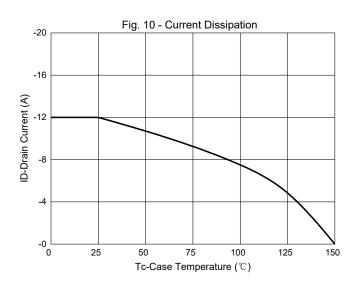


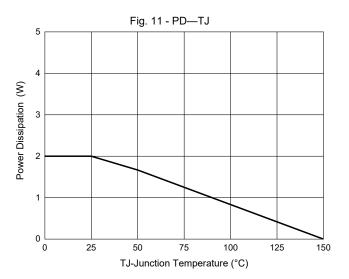
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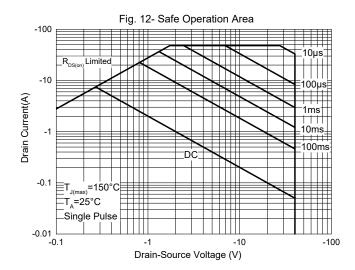


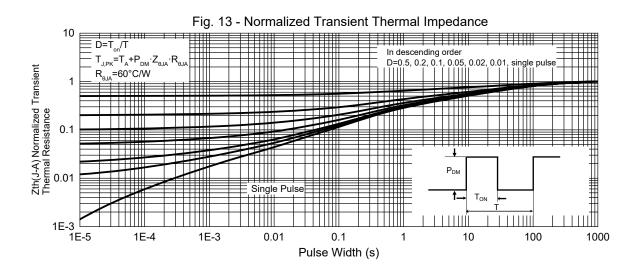






# **Curve Characteristics**







#### **Ordering Information**

Device	Packing
Part Number-TP	Tape&Reel:4Kpcs/Reel

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