

## Features

- Fully Automotive Qualified to AEC-Q101
- Split Gate Trench MOSFET Technology
- High Density Cell Design For Ultra Low  $R_{DS(on)}$
- Moisture Sensitivity Level 1
- Halogen Free."Green"Device<sup>(Note1)</sup>
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

## Maximum Ratings

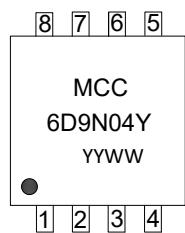
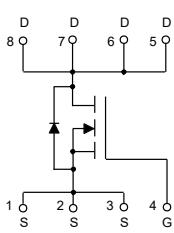
- Operating Junction Temperature Range : -55°C to +175°C
- Storage Temperature Range: -55°C to +175°C
- Thermal Resistance: 65°C/W Junction to Ambient<sup>(Note2)</sup>
- Thermal Resistance: 3°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current $T_C=25^\circ C$	$I_D$	50	A
$T_C=100^\circ C$		35	
Pulsed Drain Current <sup>(Note3)</sup>	$I_{DM}$	200	A
Total Power Dissipation <sup>(Note4)</sup>	$P_D$	50	W
Single Pulse Avalanche Energy <sup>(Note 5)</sup>	$E_{AS}$	68	mJ

Note:

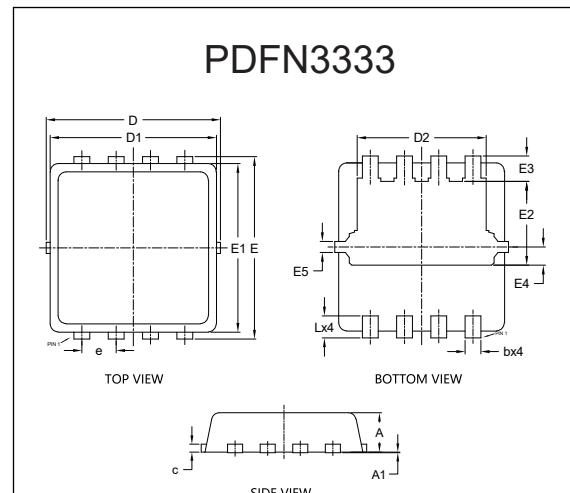
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ C$ .
3. Repetitive rating; pulse width limited by max. junction temperature.
4.  $P_D$  is based on max. junction temperature, using junction-case thermal resistance.
5.  $T_J=25^\circ C$ ,  $V_{DD}=30V$ ,  $V_{GS}=10V$ ,  $R_G=25\Omega$ ,  $L=0.5mH$ .

## Internal Structure and Marking Code



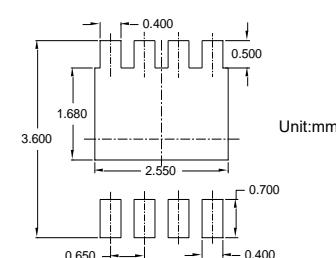
YYWW: 4 codes in total  
YY is the year  
WW is the week

## N-CHANNEL MOSFET



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.028	0.033	0.70	0.85	
A1	0.000	0.002	0.00	0.05	
b	0.008	0.016	0.20	0.40	
c	0.004	0.010	0.10	0.25	
D	0.124	0.136	3.15	3.45	
D1	0.118	0.130	3.00	3.30	
D2	0.089	0.104	2.25	2.65	
E	0.124	0.136	3.15	3.45	
E1	0.114	0.126	2.90	3.20	
E2	0.052	0.068	1.32	1.72	
E3	0.011	0.026	0.28	0.65	
E4	0.013		0.330		TYP
E5	0.008		0.200		TYP
e	0.026		0.650		BSC
L	0.012	0.020	0.300	0.500	

## Suggested Solder Pad Layout



**Electrical Characteristics @ 25°C (Unless Otherwise Specified)**

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	40			V
Gate-Source Leakage Current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 20V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=40V, V_{GS}=0V$			1	$\mu A$
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2	3	4	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=20A$		5.1	6.9	$m\Omega$
Gate Resistance	$R_g$	f=1 MHz, Open drain		1.8		$\Omega$
<b>Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$				50	A
Diode Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_S=20A$			1.2	V
Reverse Recovery Time	$t_{rr}$	$I_F=20A, dI/dt=100A/\mu s$		21		ns
Reverse Recovery Charge	$Q_{rr}$			12		nC
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=20V, V_{GS}=0V, f=1MHz$		1190		pF
Output Capacitance	$C_{oss}$			365		
Reverse Transfer Capacitance	$C_{rss}$			12.6		
Total Gate Charge	$Q_g$	$V_{DS}=20V, V_{GS}=10V, I_D=20A$		14.5		nC
Gate-Source Charge	$Q_{gs}$			4.9		
Gate-Drain Charge	$Q_{gd}$			3		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=20V, V_{GS}=10V, R_G=3\Omega, I_D=20A$		12		ns
Turn-On Rise Time	$t_r$			58		
Turn-Off Delay Time	$t_{d(off)}$			16		
Turn-Off Fall Time	$t_f$			4.2		

## Curve Characteristics

Fig.1 - Typical Output Characteristics

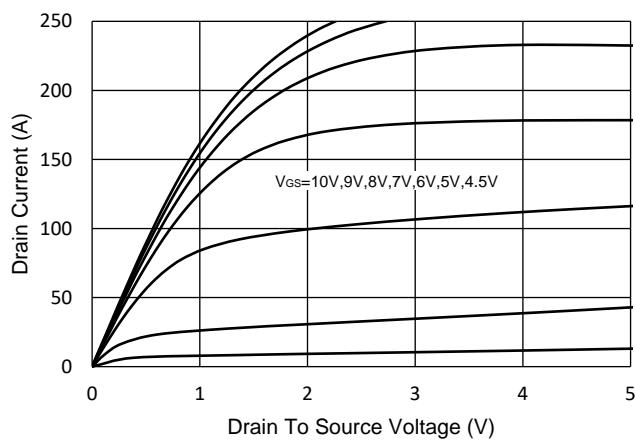


Fig.2 - Transfer Characteristics

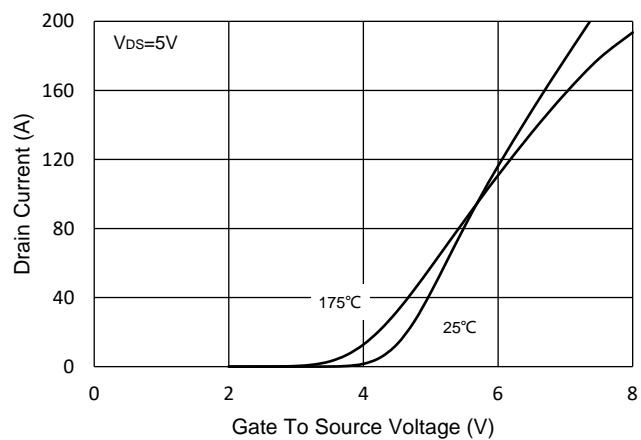


Fig.3 -  $R_{DS(ON)}$  -  $V_{GS}$

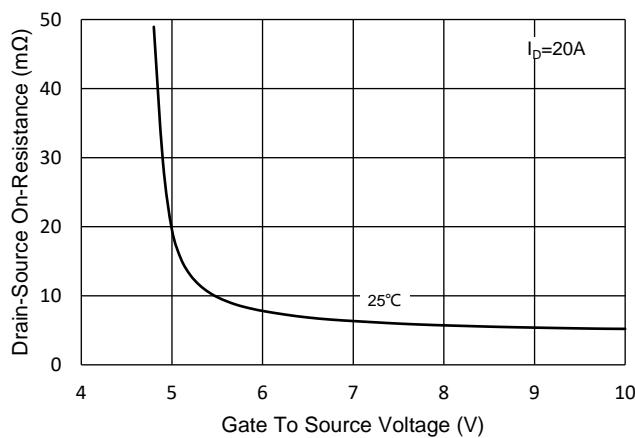


Fig.4 -  $R_{DS(ON)}$  -  $I_D$

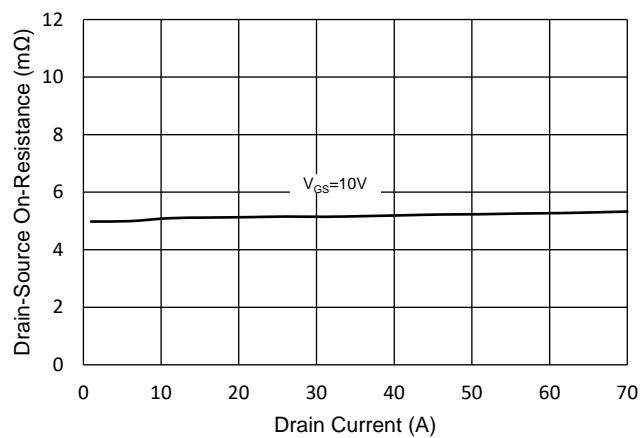


Fig.5 - Capacitance Characteristics

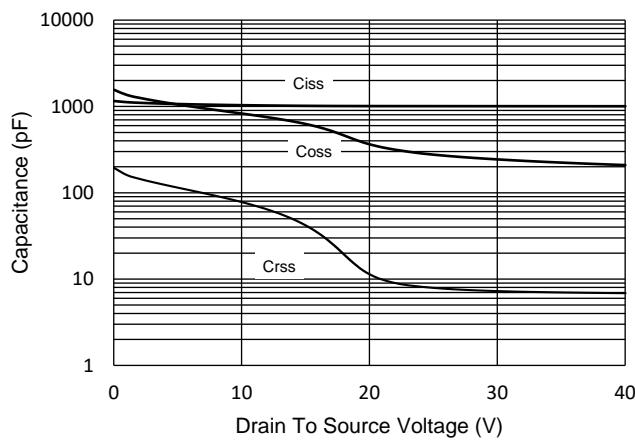
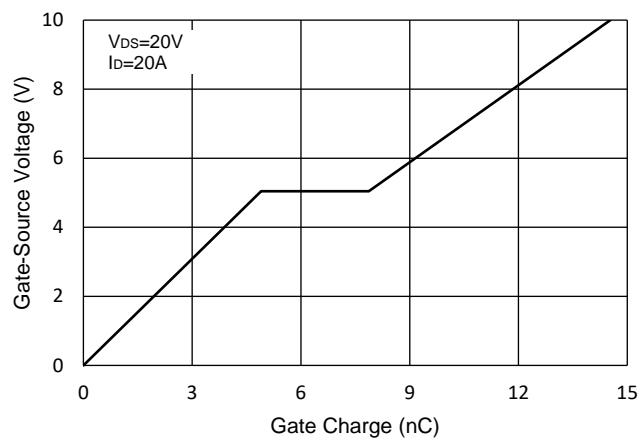


Fig.6 - Gate Charge



## Curve Characteristics

Fig.7 - Normalized Threshold Voltage

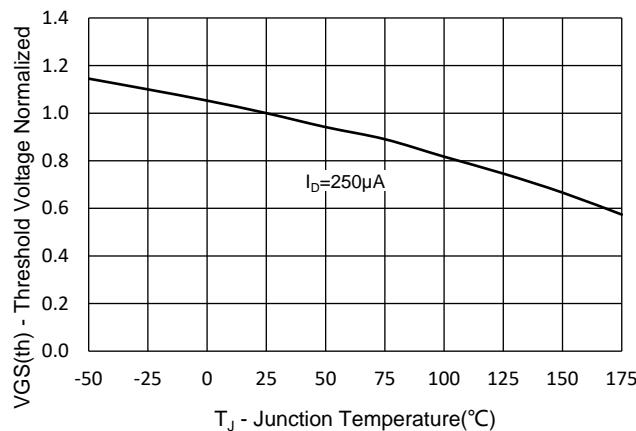


Fig.8 - Normalized On Resistance Characteristics

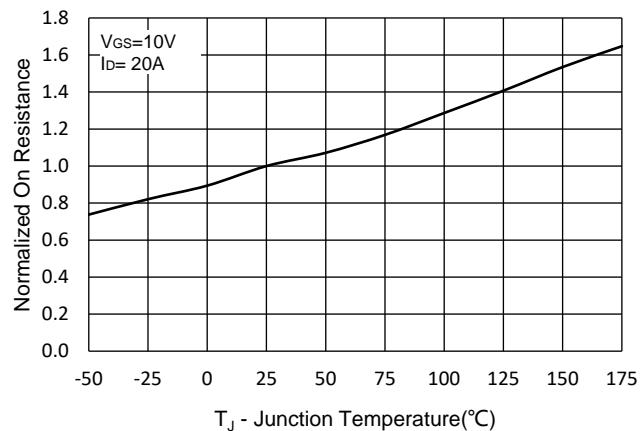


Fig.9 - I<sub>S</sub> - V<sub>SD</sub>

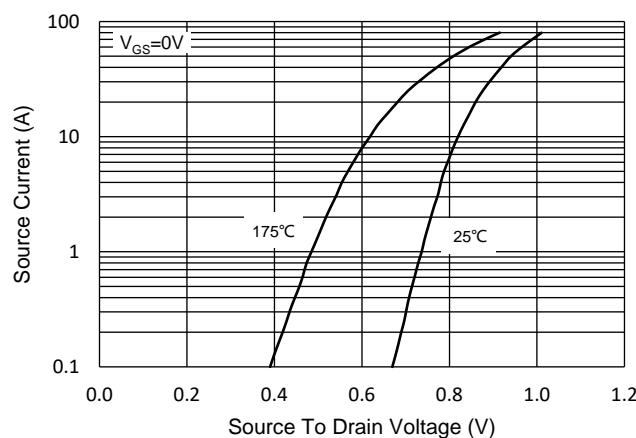


Fig.10 - Drain Current

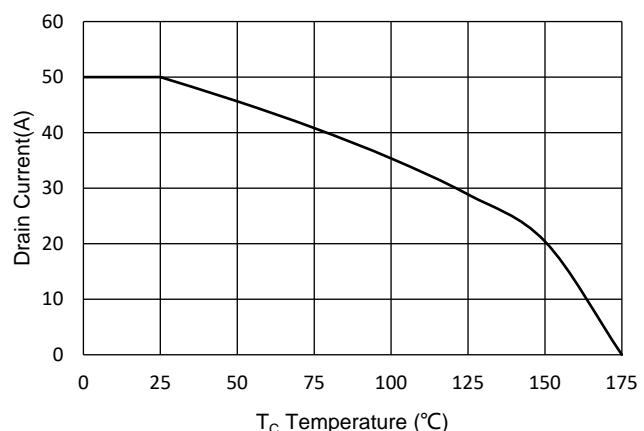
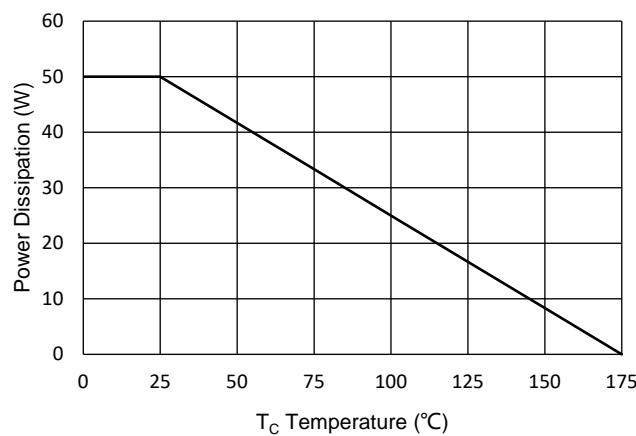


Fig.11 - PD Dissipation



## Curve Characteristics

Fig.12 - Safe Operation Area

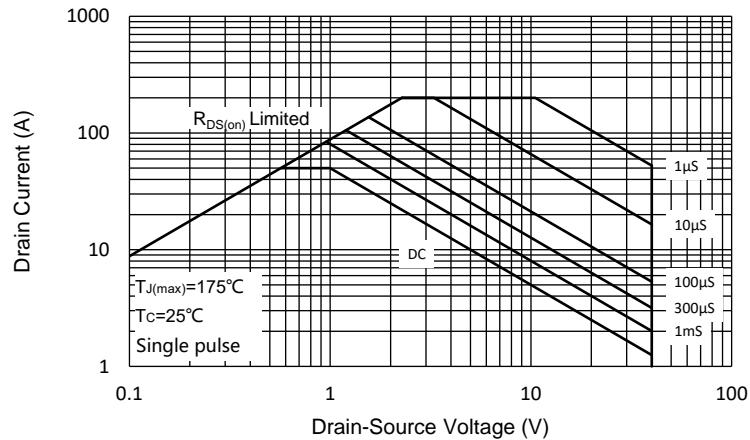
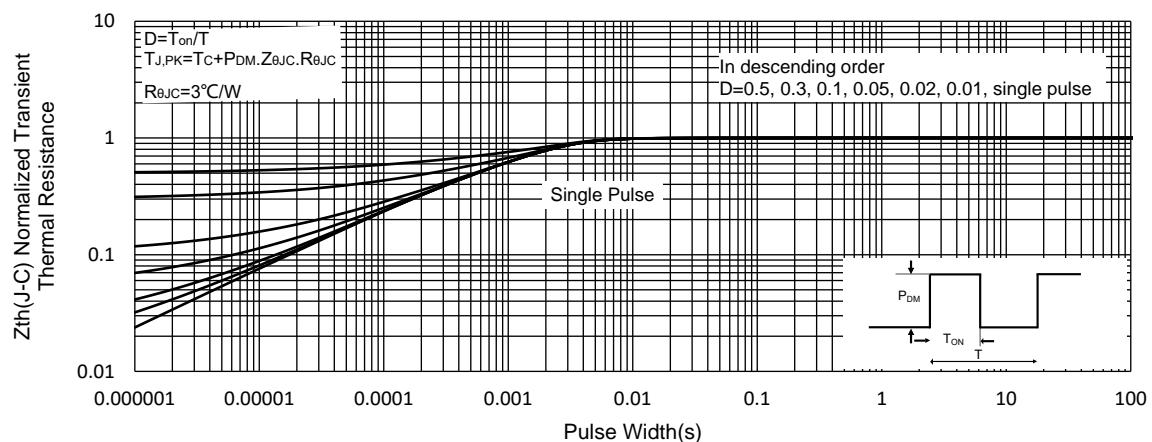


Fig.13 - Normalized Transient Thermal Impedance



## Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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