Evaluates: MAX86160

General Description

The MAX86160 evaluation kit (MAX86160EVSYS#) provides a proven design to evaluate the MAX86160 integrated heart-rate monitor sensor module. The EV kit consist of two boards. IFC6410 is the main board and MAX86160 Breakout EV kit is the daughter board that includes the MAX86160. The EV kit is powered using an external 5V/3A power supply to the IFC6410, and +1.8V through an LDO for the sensor and +5.0V for the internal LEDs of the MAX86160.

The EV kit comes with a MAX86160EFN+ installed in an 18-pin OLGA package.

Features

- Real-Time Monitoring
- Data-Logging Capabilities
- Fully Assembled and Tested
- Windows[®] 7, and Windows 8/8.1-Compatible Software

Ordering Information appears at end of data sheet.

Windows is a registered trademark and registered service mark of Microsoft Corporation.

Quick Start

- MAX86160 EV Kit (MAX86160 Breakout EV kit, IFC6410, 5V/3A power adapter and micro-USB cable included)
- Windows PC

Procedure

The EV kit is fully assembled and tested. Follow the steps below to verify board operation:

- Visit <u>www.maximintegrated.com/evkit-software</u> to download the most recent version of the EV kit software, *SetupPPG_EvKit_max86160_x.x.zip.* Save the EV kit software to a temporary folder and uncompress the ZIP file.
- 2) Open SetupPPG_EvKit_max86160_x.x.msi and follow the instructions from the pop-up windows as shown in Figure 1, Figure 2, and Figure 3.
- Connect the MAX86160 Breakout EV Kit (J4 and J5) to IFC6410 main board (P1 and P3), as shown in Figure 4.
- 4) Power up the boards by connecting the 5V/3A power supply adapter to the IFC6410 (J4) and wall socket.
- 5) Connect the micro-USB cable from the PC to the EV kit board through J3.
- 6) Install the ADB drivers for IFC6410. Drivers can be found in the installed folder of Maxim SensorStudio, ...\Maxim Integrated\SensorStudio5\USB_drivers
- 7) Open SensorStudio5 and verify that the EV kit is connected by observing the status bar at the lowerleft corner of the GUI, as shown in <u>Figure 5</u>. If "Disconnected" is shown, please double-check the board's power supply, USB connection, and press "Refresh" to try.
- From the top menu, choose View and then click on "PPG_EvKit". The MAX86160 PPG evaluation page will then appear, as shown in Figure 6.
- Pressing the Start Monitoring button with the AGC box checked will start the PPG evaluation. LED current will automatically be adjusted by the driver.
- Place the test subject on top of the MAX86160 (U1) of the MAX86160 Breakout EV Kit and observe the PPG graphs. See <u>Figure 7</u>.





Figure 1. Setup SensorStudio Software – Step 1

	- v.5.1.60801.1		integrated				
Please wait while the Setup Wizard installs PPG Eval Kit - v.5.1.60801.1.							
Status:							

Figure 2. Setup SensorStudio Software – Step 2



Figure 3. Setup SensorStudio Software – Step 3

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Figure 4. Hardware Setup

i					
Part Name	Part ID	Part Rev.	Device ID	Driver Version	Algorithm
MAX86160	1E	08	1783471488	2.4.07	None Found
MAX86160	1E	08	1783471488	2.4.07	None Found
MAX86160	1E	08	1783471488	2.4.07	None Found
MAX86160	1E	08	1783471488	2.4.07	None Found
MAX86160	1E	08	1783471488	2.4.07	None Found
	Platform		Data Server		
	Android-KOT	19H	ADB Daemon Conne Server Version: 5.2.2	ection	
	MAX86160 MAX86160 MAX86160 MAX86160 MAX86160	MAX86160 1E MAX86160 1E	MAX86160 1E 08 MAX86160 1E 08	MAX86160 1E 08 1783471488 MAX86160 1E 08 1783471488	MAX86160 1E 08 1783471488 2.4.07 MAX86160 1E 08 1783471488 2.4.07

Figure 5. Maxim SensorStudio GUI

Evaluates: MAX86160

PG Evaluation						Version: 5.1.6080
Start Monitoring	Cog to File Cog to File Select File Browse Select Da	ta	PPG Settings	FIFO Settings PA (mA) Range (r IR 10.0 + 51 Green 10.0 + 51	mA) Sample Rate Pulse Width ADC Range	100 • Hz 400 • usec 12768 • nA
IR Count						Green Count
		X Axis Scale 1 IR Count	000 × samples Green Co	unt Sync Y Sc	ales	

Figure 6. Maxim SensorStudio GUI – PPG Evaluation Page

Evaluates: MAX86160



Figure 7. Maxim SensorStudio GUI – PPG Evaluation Page: Monitoring Data

Detailed Description of Software

The main window of the SensorStudio displays the PPG waveforms (ADC counts over time), Sample Rate settings, pulse width settings and LED currents settings. By default, the AGC (auto-gain control) is enabled, in this mode, the LED currents will be dynamically controlled by the software driver to achieve optimal signal. Under this mode, the sample rate is set to 400hz. In order to manually control the settings menu inside the PPG Settings Tab (sample rate, pulse width and LEDs current), AGC must be unchecked.

Data Logging

The software also provides capability of Data logging. First of all, check the "Log to File" option, and entered the file name by pressing "Browse" (by default under folder \MaximLogs\.). The log file will start saving data once "Start Monitoring" is pressed.

PPG Settings

Within the PPG Settings tab, both the drive current of IR and Green LED can be adjusted individually. Each LED driver is consisted of four different ranges which can be chosen from IR LED Range/Green LED Range

- 0 **51**mA (0.2mA/LSB)
- 0 **102**mA (0.4mA/LSB)
- 0 **153**mA (0.6mA/LSB)
- 0 **204**mA (0.8mA/LSB)

After the range is chosen, the LED current can be adjusted from the IR and Green LED current, respectively.

In addition to the LED Currents, the PPG Settings Tab also consists of controls to the sample rate and pulse width. For more information about the parameters, please refer to the sensor data sheet. The **Sample Rate** drop-down list is adjustable from 10Hz to 3200Hz.

The **Pulse Width** drop-down list is adjustable from 50Hz to 400µsec.

The **ADC Range** drop-down list is adjustable from 4096 to 32768nA.

FIFO Settings

FIFO Almost Full indicate how many new samples can be written to the FIFO before the interrupt is asserted. For example, if it is set to 15, the interrupt triggers when there is 17 empty space left (15 data samples), and so on.

By Checking "**FIFO Rolls on Full**" options, the FIFO will automatically roll over when it is full. Otherwise, the FIFO will stops accepting new data until it's read out.

To reduce the amount of data throughput, adjacent samples (in each individual channel) can be averaged and decimated on the chip by setting the Sample Averaging.

The **Sample Averaging** drop-down list is adjustable from 1 to 32.

RegMapForm

The software also provides capability of viewing and changing the Register Settings of the sensor. This feature can be opened by going to the top menu \rightarrow View \rightarrow Register Map, as shown in Figure 8.

The I²C register can be read back and written to by using the RegMapForm tool (<u>Figure 9</u>). Double-click on the bitfield you would like to change to flip the bit. Then, click Set Reg to update the settings.

By using the manual update feature, one can also read and set the sensor register settings easily.

PF、	Device Info Panel Register Map			
	PPG_EvKit Stop	Log to File Virite Header Select File Browse Select Data	PPG Settings ✓ AGC	FIFO Settings PA (m IR 10. Green 10.

Figure 8. Register Map Options

Evaluates: MAX86160

MAX86160												🗸 I	abel Ce		
Address	Address Hex	Name	Value	Value Hex	[7]	[6]	[5]	[4]	[3]	[2]	[1]	[0]	Access		
0	00	Interrupt Status 1	0	00	A FULL = b'0	PPG RDY = b'0	ALC OVF =	PROX INT =	0	0	0	PWR RDY =	R		
1	01	Interrupt Status 2	0	00	VDD OOR =	0	0	0	0	0	0	0	R		
2	02	Interrupt Enable 1	64	40	A FULL EN =	PPG RDY EN	ALC OVF EN	PROX INT EN	0	0	0	0	RW		
3	03	Interrupt Enable 2	0	00	VDD OOR E	0	0	0	0	0	0	0	RW		
4	04	FIFO Write Pointer	23	17	0	0	0			FIFO WR PTR			RW		
5	05	Over Flow Counter	0	00	0	0	0			OVF Counter			R		
6	06	FIFO Read Pointer	23	17	0	0	0			FIFO RD PTR			RW		
7	07	FIFO Data Register	0	00		FIFO Data		IFO Data			RW				
8	08	FIFO Configuration	31	1F	0	A Full Clear	A Full Type =	FIFO Roll On	FIFO A Full = 17			RW			
9	09	FIFO Data Control Regi	33	21		Slot 2	= LED2		Slot 1 = LED1			Slot 1 = LED1			RW
10	0A	FIFO Data Control Regi	3	03		Slot 4 = OFF Slot 3 = LED3		Slot 3 = LED3			RW				
13	0D	System Control	4	04	0	0	0	0	0	FIFO EN = b'1	SHDN = b'0	RESET = b'0	RW		
14	0E	PPG Configuration 1	211	D3	PPG ADC Rar	nge = 32768nA		PPG Sampler	ate = 100Hz		LED Pulsev	vidth = 400us	RW		
15	0F	PPG Configuration 2	0	00	0	0	0	0	0		Sample Avg =	1	RW		
16	10	Prox Interrupt Threshold	0	00				Prox Intern	upt Thresh				RW		
17	11	LED1 PA	50	32				LED1 Pulse Am	plitude = 10mA				RW		
19	13	LED3 PA	50	32				LED3 Pulse Am	plitude = 10mA				RW		
20	14	LED Range	0	00	0	0	LED3 Ran	ge = 51mA	0	0	LED1 Rar	nge = 51mA	RW		
21	15	LED PILOT PA	0	00				Pilot Pulse	Amplitude				RW		
255	FF	Part ID	30	1E	0	0	0	1	1	1	1	0	R		
elected Re	egister	AI	II Registers	5					N	lanual Updat Address (he	te ex)	F	Read		

Figure 9. Register Map User Interface

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Detailed Description of Hardware

The MAX86160 EV kit provides a proven design to evaluate the MAX86160 integrated heart-rate monitor sensor module. The EV kit is powered through the +5V/3A from the DC Power Adapter to IFC6410 main board. Then +5V supply from the main board is used for the +VLED supply

Component List

PART	QTY	DESCRIPTION
Inforce IFC6410	1	EV KIT motherboard
MAX86160EVKIT	1	MAX86160 Breakout EVKIT

Ordering Information

PART	ТҮРЕ
MAX86160EVSYS#	EVKIT

#Denotes RoHS compliant.

and regulated +1.8V to V_{DD} supply of the MAX86160. The CPU on the IFC6410 communicates with the MAX86160 through interrupt signal and I²C interface. There are a lot of different connectors option on the breakout EV kit board, however, only Connector 2 (J4 and J5) will be utilized in this EV kit.

Component Information, PCB Layout, and Schematic

See the following links for component information, PCB layout diagrams, and schematic.

- MAX86160 EV BOM
- MAX86160 EV PCB
- MAX86160 EV Schematic

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Revision History

REVISION	REVISION	DESCRIPTION	PAGES
NUMBER	DATE		CHANGED
0	8/16	Initial release	—

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim Integrated's website at www.maximintegrated.com.

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TITLE: Bill of Materials DATE: 01/23/2016 DESIGN: max86160_breakout_evkit_p1

ITEM	QTY	REF DES	MAXINV	MFG PART #	MFCTR	VALUE	DESCRIPTION
							CAPACITOR; SMT (0603); CERAMIC CHIP; 0.10F;
2	2	C2 C4 CF	20.000111.10	C0C02C104K9DAC	KENAET	0.1115	10V; TOL=10%; MODEL=C0603 SERIES; TG=-55
3	3	C2, C4, C5	20-00001-10	C0603C104K8RAC	KEIVIEI	0.10F	DEGUTO +125 DEGU; TU=X7K
		C2 C11			NALIDATA.		CAPACITOR, SIVIT (0005), CERAIVIC CHIP, 2.20F, $100, 100, 100, 100, 100, 100, 100, 10$
1	2	C_{12}	20.002112.10	CLIODOOR/IAZZONEIO,	MURATA,	2 2115	100, TOL-10%, TG55 DEGC TO +125 DEGC,
4	3		20-00202-10	CLIUBZZSKP8INININ	SAIVISUNG	2.20F	IC=X/R
11	1	14	01-68031102H2D-19	68031-102HLE		68031-102HLE	SERIES: REPOSTIK II HEADED: STRAIGHT: 201015
- 11	1	14	01-08031102112F-19	08031-102116		SOW-115-01-L-D-	CONNECTOR: FEMALE: SMT: SOW/ SERIES:
12	1	15	01-50W/11501LDV530P-80	SOW-115-01-L-D-VS	SAMTEC		STRAIGHT: 30PINS
12	1	12	01-5000115012005501-80	CRCW06031001FK· FRI-	VISHAY DALE	V3	BESISTOR: 0603: 1K: 1%: 100PPM: 0 10W:
14	1	R2	80-0001K-24	3FKF1001V	PANASONIC	1K	
1-1	-	112	00 00011 24	CRCW06030000ZS:	VISHAY		
				MCR03EZPJ000: ERJ-	DALE/ROHM/		RESISTOR: 0603: 0 OHM: 0%: JUMPER: 0.10W:
15	1	R14	80-0000R-27	3GEY0R00	PANASONIC	0	THICK FILM
							IC; VREG; ULTRA-LOW-NOISE; HIGH PSRR;
17	1	U2	MAX8511EXK18+	MAX8511EXK18+	MAXIM	MAX8511EXK18+	LOW=DROPOUT; LINEAR REGULATOR; SC70-5
20	1		EPCB86160BREAKOUT	MAX86160breakout	MAXIM	РСВ	PCB: MAX86160breakout
							EVKIT PART-IC; MAX86160; OLGA18 2.8MM X
							4.3XMM 1.55MM; 0.525MM PITCH; WITH
							A5753-118-23 ALT SYMBOL SOCKET
4	1	S1	N/A	MAX86160	MAXIM	MAX86160	CONSIDERATION
TOTAL	13						
DO							
NOT							
STUFF							
(DNF)							

		GND, C1_N,					
		C1_P, GPIO,					
		PGND,					
		TP18.					
		+3.3V.					
		FCG C					
		ECG N					
		ECG_R					
1	10			FOOE		EOOE	COMPACT
1	12	LED_DRV5	02-191011013003-00	5005		5005	COMPACT CADACITOR: SMT (0402): CERAMIC CHID: 0 111E:
							CAFACHOR, SWI (0402), CERAWIC CHIF, 0.101,
2	2	C1 C10				0.1115	250, TOL-10%, NIODEL-GRIVI SERIES, TO55
Z	2	CI, CI9	20-00001-868	GRIM155R71E104KE14	MURATA	0.10F	DEGC 10 +125 DEGC; 1C=C0G
				CRM188P71C105KA12			
				GRW100R71C105RA12,			CAPACHOR, SWI (0003), CERAINIC CHIP, IOF,
_	2	CC C0	20.000111.02			1115	100, TOL-10%, MODEL-, TO55 DEGC TO
5	3	6-68	20-00010-63	EIVIK107B7105KA	YUDEN	10F	+125 DEGC; IC=X/K CADACITOD: SMT (0602): CEDAMIC CHID: 10UE:
					CANACHINIC		CAPACITOR, SIVIT (0005), CERAIVIC CHIP, 100F,
6		<u></u>	20.004011.044		SAIVISUNG	10115	0.3V; TOL=20%; MODEL=CL SERIES; TG=-55
6	1	C9	20-0010U-R1A	CL10B106MQ8NRN	ELECTRONICS	100F	DEGC TO +125 DEGC; TC=X/K
							CAPACITOR; SIVIT (0003); CERAIVIC CHIP; 100F;
_						10115	10V; TOL=10%; MODEL=; TG=-55 DEGC TO +85
/	1	C20	20-00100-16	C1608X5R1A106K	TDK	10UF	DEGC; IC=X5R
						SSW-112-22-F-D-	CONNECTOR; FEMALE; SMT; 0.025 POST
8	1	J1	01-SSW11222FDVS24P-17A	SSW-112-22-F-D-VS	SAMTEC	VS	SOCKET; STRAIGHT; 24PINS
							CONNECTOR; FEMALE; THROUGH HOLE; USB
							TYPE C CONNECTOR; RIGHT ANGLE HYBRID;
9	1	J2	01-12401548E42A24P-26	12401548E4#2A	AMPHENOL	12401548E4#2A	24PINS
							CONNECTOR; MALE; SMT; FTSH SERIES; SMT
10	1	13	01-FTSH11701LDH34P-19	FTSH-117-01-L-DH	SAMTEC	FTSH-117-01-L-DH	MICRO HEADER; RIGHT ANGLE; 34PINS
							RESISTOR, 0402, 470 OHM, 1%, 100PPM,
13	3	R1, R6, R7	80-0470R-23	CRCW0402470RFK	VISHAY DALE	470	0.0625W, THICK FILM
		R10, R11,		CRCW06031001FK; ERJ-	VISHAY DALE;		RESISTOR; 0603; 1K; 1%; 100PPM; 0.10W;
14	4	R16	80-0001K-24	3EKF1001V	PANASONIC	1K	THICK FILM
					ARIES		HT SOCKET; PITCH(0.525MM); NO. OF LEADS-
16	1	U1	03-0.5250LGA18-00	A5753-118-23	ELECTRONICS	A5753-118-23	(18); OLGA 4.30MMX2.80MM
							IC; VREG; 500mA LOW-DROPOUT LINEAR
18	1	U3	MAX1818EUT33+	MAX1818EUT33+	MAXIM	MAX1818EUT33+	REGULATOR IN SOT23

					TEXAS		IC; BUF; P82B96 I2C COMPATIBLE DUAL
19	2	U4, U5	10-P82B96PW-U	P82B96PW	INSTRUMENTS	P82B96PW	BIDIRECTIONAL BUS BUFFER; TSSOP8

TOP SILKSCREEN





TOP PASTE









TOP MASK









LAYER 2





LAYER 3





BOTTOM





BOTTOM MASK

BOTTOM PASTE









BOTTOM SILKSCREEN

MAX86160







 $\sim \sim \sim$

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