

MAAL-011217 Rev. V1

Features

- Broadband 2-Stage LNA •
- Gain:
 - 36.8 dB @ 2.5 GHz 35.6 dB @ 3.75 GHz 34.8 dB @ 4.9 GHz
- Noise Figure: 0.58 dB @ 2.5 GHz 0.60 dB @ 3.75 GHz 0.70 dB @ 4.9 GHz
- Single 5 V Supply •
- Compatible with 1.8 V and 3.3 V logic .
- Low DC Current: 106 mA •
- Lead-Free 3 mm 16 Lead QFN Package
- **RoHS*** Compliant

Applications

- 5G Macro and Massive MIMO
- Wireless Infrastructure
- General purpose wireless
- TDD or FDD systems •

Description

The MAAL-011217 is a compact surface mount, highly integrated 2-stage low noise amplifier (LNA). This LNA is housed in a lead-free 3 mm 16-lead QFN plastic package.

The MAAL-011217 features low noise figure, high gain and low power consumption. The LNA requires a single 5 V supply and the Power Down pin is 1.8 V or 3.3 V CMOS compatible.

Ordering Information¹

| Part Number | Package |
|--------------------|-----------------|
| MAAL-011217-TR1000 | 1000 piece reel |
| MAAL-011217-001SMB | Sample Board |

1. Reference Application Note M513 for reel size information.

Functional Schematic



Pin Function²

| Pin # | Function | |
|------------------------------|----------------------------|--|
| 1, 3, 4, 6 - 10, 12, 14 - 16 | Internally No Connect | |
| 2 | RF Input | |
| 5 | Logic Power Down | |
| 11 | RF Output | |
| 13 | Supply Voltage | |
| 17 | Ground Paddle ³ | |

2. MACOM recommends connecting unused package pins to ground. The exposed pad centered on the package bottom must be

3 connected to RF, DC and thermal ground.

* Restrictions on Hazardous Substances, compliant to current RoHS EU directive.

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Pin Description

| Pin # | Name | Description | |
|---------------------------------|--------|---|--|
| 1, 3, 4, 6 - 10, 12, 14 - 16 | NC | Not connected internally. It is recommended to connect N/C pins to RF grounds of the PCB. | |
| 2 | RFIN | RF Input. DC blocking capacitor required. | |
| 5 | PD | Power Down logic control for LNA ON/OFF Modes | |
| 11 | RFOUT | RF Output. See absolute maximum ratings table for DC voltage limits at this pin. | |
| 13 | VDD | 5 V Supply pin needs external decoupling capacitors. | |
| 17 | Paddle | Exposed Pad. The exposed pad must be connected to RF, DC and thermal GND. | |

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AC Electrical Specifications: P_{IN} = -30 dBm, V_{DD} = 5 V, Z_0 = 50 Ω , T_c = +25°C

| Parameter | Test Conditions | Units | Min. | Тур. | Max. |
|------------------------|--|-------|---------------|----------------------|------|
| Gain | 2.5 GHz 3.75 GHz 4.9 GHz | dB | 34 33 — | 36.8 35.6 34.8 | |
| Gain Variation | Over Temperature, 3.75 GHz | dB/°C | — | 0.01 | — |
| Noise Figure | 2.5 GHz 3.75 GHz 4.9 GHz | dB | | 0.58 0.60 0.70 | |
| Noise Figure Variation | Over Temperature, 2.5 GHz Over Temperature, 3.75 GHz | dB/°C | — | 0.003 0.004 | — |
| Input IP3 | P _{IN} /tone = -30 dBm, Tone Delta = 2 MHz, 2.5 GHz 3.75 GHz 4.9 GHz | dBm | _ | -2.5 -2.5 -3.0 | _ |
| Input P1dB | 2.5 GHz 3.75 GHz 4.9 GHz | dBm | | -16 -15 -15 | |
| Input Return Loss | 2.5 GHz 3.75 GHz 4.9 GHz | dB | — | -17 -17 -12 | — |
| Output Return Loss | 2.5 GHz 3.75 GHz 4.9 GHz | dB | _ | -15 -18 -19 | _ |
| Reverse Isolation | e Isolation RF _{OUT} to RF _{IN} 2.5 GHz 3.75 GHz 4.9 GHz | | _ | 51 49 48 | |

DC Electrical Specifications: V_{DD} = 5 V, Z₀ = 50 Ω , T_c = +25°C

| Parameter | Test Conditions | Units | Min. | Тур. | Max. |
|-----------------------------------|-----------------------------|-------------------------------|------|------------|-------------|
| Supply Voltage | | V | 4.75 | 5 | 5.25 |
| Supply Current | LNA ON Mode LNA OFF Mode | LNA ON Mode mA | | 106 0.6 | 140 1 |
| Power Down Logic Input Voltage | LNA ON Mode Disable Mode | LNA ON Mode V Disable Mode | | — | 0.6 3.45 |
| Power Down Logic Input Current | LNA ON Mode Disable Mode | μA | 0 | -4 40 | 80 |

Transient Electrical Specifications: Freq = 2.5 GHz, P_{IN} = -30 dBm, V_{DD} = 5 V, Z_0 = 50 Ω , T_c = +25°C

| Parameter | Test Conditions | Units | Min. | Тур. | Max. |
|-----------------------|---|-------|------|------|------|
| LNA ON Settling Time | Gain shall be within 0.1 dB deviation from final value | μs | _ | 0.6 | _ |
| LNA OFF Settling Time | Power shall be within 10% from final value | μs | | 0.1 | _ |

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Power Down Truth Table

| PD Control | | | |
|--------------|-------------------|--|--|
| LNA ON Mode | Logic Low or Open | | |
| Disable Mode | Logic High | | |

Recommended Operating Conditions

| Parameter | Operation Conditions |
|---|----------------------|
| DC Supply V_{DD} | +4.75 to +5.25 V |
| Logic PD Control Voltage | 0 to +3.3 V |
| Case Temperature (T _C) ⁴ | -40°C to +115°C |

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

| Parameter | Rating | Standard |
|----------------|------------|------------|
| Human Body | 500 V | ESDA/JEDEC |
| Model (HBM) | (Class 1B) | JS-001 |
| Charged Device | 1000 V | ESDA/JEDEC |
| Model (CDM) | (Class C3) | JS-002 |

Power Supplies

De-coupling capacitors should be placed at the V_{DD} supply pin to minimize noise and fast transients. Supply voltage change or transients should have a slew rate smaller than 1 V / 10 μ s. In addition, all control pins should remain at 0 V (+/- 0.3 V) and no RF power should be applied while the supply voltage ramps or while it returns to zero.

Absolute Maximum Ratings^{5,6}

| Parameter | Absolute Maximum |
|--|-------------------------|
| RF Input Power: LNA ON Mode | 33 dBm CW 30 dBm LTE |
| DC Supply V_{DD} | -0.5 to +5.5 V |
| Logic PD Control Voltage | -0.5 to +3.6 V |
| DC Voltages at RF Output | -0.5 to +2.75 V |
| Junction Temperature ^{7,8} LNA ON Mode | +150°C |
| Storage Temperature | -55°C to +150°C |

4. Operating/Case temperature (T_C) is the temperature of the exposed paddle.

5. Exceeding any one or combination of these limits may cause permanent damage to this device.

6. MACOM does not recommend sustained operation near these survivability limits.

7. Operating at nominal conditions with TJ<150°C (LNA ON Mode) will ensure MTTF >>1x10⁶ hours

8. Junction Temperature $(T_J) = T_C + \Theta_{JC} * P_{DISS}$ where P_{DISS} is the total DC & RF dissipated power. Typical thermal resistance $(\Theta_{JC}) = 33.4^{\circ}$ C/W.

a) For $T_c = +25^{\circ}C$,

T_J = 43°C @ 5 V, 106 mA

b) For $T_{c} = +115^{\circ}C$,

T_J = 137°C @ 5 V, 130 mA

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Applications Schematic

Sample Board Layout



Parts list

| Schematic Component | Component Value | Size | Manufacturer |
|---------------------|-----------------|------|---------------------------|
| C1 | 10 µF | 0603 | Murata ZRB18AD71A106KE01 |
| C2 | 10 nF | 0402 | Murata GRM155R71C103KA01D |
| C3 | 470 pF | 0402 | Murata GRM155R71H471KA01D |
| C4 | 5 pF | 0402 | Kyocera CM05CG5R0B50AH |
| C5 | 27 pF | 0402 | Murata GJM1555C1H270FB01 |
| C6 | DNP | DNP | DNP |
| C7 | Cu Shim | 0402 | - |
| C8 | DNP | DNP | DNP |
| L1 | Cu Shim | 0402 | - |
| L2 | Cu Shim | 0402 | - |
| R1 | 100 Ω | 0402 | Yageo RC0402JR-07100R |
| R2 | 1 κΩ | 0402 | Yageo RC0402JR-071K |

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Typical Performance Curves: $P_{IN} = -30 \text{ dBm}, V_{DD} = 5 \text{ V}, Z_0 = 50 \Omega$

Gain⁹

Input Return Loss

DC Current Over VDD and Temp

9. For gain, noise figure, reverse isolation, P1dB and IP3 plots, RF trace and connector losses are de-embedded.

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Output Return Loss

Noise Figure⁹

Typical Performance Curves: P_{IN} = -30 dBm, V_{DD} = 5 V, Z_0 = 50 Ω

Reverse Isolation⁹

Input P1dB⁹

Gain⁹ over Supply

Input IP3⁹

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Applications Section A: 2.3 - 2.7 GHz

Parts list

| Schematic Component | Component Value | Size | Manufacturer |
|---------------------|-----------------|------|---------------------------|
| C1 | 10 µF | 0603 | Murata ZRB18AD71A106KE01 |
| C2 | 10 nF | 0402 | Murata GRM155R71C103KA01D |
| C3 | 470 pF | 0402 | Murata GRM155R71H471KA01D |
| C4 | 5 pF | 0402 | Kyocera CM05CG5R0B50AH |
| C5 | 27 pF | 0402 | Murata GJM1555C1H270FB01 |
| C6 | 0.7 pF | 0402 | Murata GJM1555C1HR70WB01D |
| C7 | 3.9 pF | 0402 | Murata GJM1555C1H3R9CB01D |
| C8 | DNP | DNP | DNP |
| L1 | 1 nH | 0402 | Coilcraft 0402CS-1N0XJLW |
| L2 | Cu Shim | 0402 | — |
| R1 | 100 Ω | 0402 | Yageo RC0402JR-07100R |
| R2 | 1 κΩ | 0402 | Yageo RC0402JR-071K |

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AC Electrical Specifications: Freq = 2.3 - 2.7 GHz, P_{IN} = -30 dBm, V_{DD} = 5 V, Z_0 = 50 Ω , T_C = +25°C

| Parameter | Test Conditions | Units | Min. | Тур. | Max. |
|------------------------|--|-------|------|-------|------|
| Gain | _ | dB | | 37.3 | |
| Gain Variation | Over Temperature | dB/°C | | 0.015 | |
| Noise Figure | _ | dB | | 0.65 | |
| Noise Figure Variation | Over Temperature | dB/°C | | 0.006 | |
| Input IP3 | P _{IN} /tone = -30 dBm, Tone Delta = 2 MHz, | dBm | | -2.5 | |
| Input P1dB | _ | dBm | | -16 | |
| Input Return Loss | _ | dB | | -23 | |
| Output Return Loss | _ | dB | | -23 | |
| Reverse Isolation | RFOUT to RFIN | dB | | 52 | |

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-40°C

+25°C

+115°C

2.7

-40°C +25°C

+115°C

2.7

-40°C

+25°C

+115°C

Typical Performance Curves: $P_{IN} = -30 \text{ dBm}, V_{DD} = 5 \text{ V}, Z_0 = 50 \Omega$

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Applications Section B: 4.4 - 5.0 GHz

Parts list

| Schematic Component | Component Value | Size | Manufacturer | | |
|---------------------|-----------------|------|------------------------------|--|--|
| C1 | 10 µF | 0603 | Murata ZRB18AD71A106KE01 | | |
| C2 | 10 nF | 0402 | Murata GRM155R71C103KA01D | | |
| C3 | 470 pF | 0402 | Murata GRM155R71H471KA01D | | |
| C4 | 5 pF | 0402 | Kyocera CM05CG5R0B50AH | | |
| C5 | 27 pF | 0402 | Murata GJM1555C1H270FB01 | | |
| C6 | 0.2 pF | 0402 | Murata GJM1555C1HR20BB01D | | |
| C7 | Cu Shim | 0402 | — | | |
| C8 | DNP | DNP | DNP | | |
| L1 | Cu Shim | 0402 | _ | | |
| L2 | Cu Shim | 0402 | — | | |
| R1 | 100 Ω | 0402 | Yageo RC0402JR-07100R | | |
| R2 | 1 kΩ | 0402 | Yageo RC0402JR-071K | | |

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AC Electrical Specifications: Freq = 4.4 - 5.0 GHz, P_{IN} = -30 dBm, V_{DD} = 5 V, Z_0 = 50 Ω , T_C = +25°C

| Parameter | Test Conditions | Units | Min. | Тур. | Max. |
|------------------------|---|-------|------|-------|------|
| Gain | - | dB | _ | 35.1 | |
| Gain Variation | Over Temperature | dB/°C | — | 0.01 | |
| Noise Figure | - | dB | — | 0.7 | |
| Noise Figure Variation | Over Temperature | dB/°C | _ | 0.004 | |
| Input IP3 | P _{IN} /tone = -30 dBm, Tone Delta = 2 MHz | dBm | — | -2 | |
| Input P1dB | - | dBm | — | -16 | |
| Input Return Loss | - | dB | _ | -17 | |
| Output Return Loss | - | dB | _ | -19 | |
| Reverse Isolation | RFOUT to RFIN | dB | — | 48 | — |

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-40°C --

+25°C

+115°C

5.0

5.0

4.9

4.9

Frequency (GHz)

-40°C

+25°C

+115°C

4.9

-40°C

+25°C

+115°C

Typical Performance Curves: $P_{IN} = -30 \text{ dBm}, V_{DD} = 5 \text{ V}, Z_0 = 50 \Omega$

10. For gain, noise figure, reverse isolation, P1dB and IP3 plots, RF trace and connector losses up to C5/C7 SMT's have been de-embedded.

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Lead-Free 3 mm 16-Lead QFN[†]

Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 1 requirements in accordance to JEDEC J-STD-020D. Plating is NiPdAu over Copper

Revision History

| Rev | Date | Change Description |
|-----|----------|--------------------|
| V1 | 04/09/24 | Initial Release |
| | | |

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